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when you will load

in write back policy when the block replaced then data is updated in the memory.

in fifo policy is based upon the implementation of queue

LRU is based on vector I am taking a vector and a array keeping record of the rescent element in each set

in LRU this first decide the minimum counter of the memory and then replace any of the minimum counter as random is there thus uses combined LRU + RANDOM

first implement for read in t0 code then implement for write in the code and later combine both as there implementation are independent from each other

read should be implemented on FIFO policy it means a time stamp is needed for each block in the "set"

we are storing a recent as time access.

if its associativity is 1 then no replacement policy will be there.

We not need to store the data for cache implementation as (offset + index+ tag + valid bit) for the given block --if this address found then then hit else replace

the tag bit and change valid bit to 1 rest thing will be same

valid bit will be 0 always when any block haven been initialized else 1

Binary implementation should be done for the given hex and then at last again output should be in hex so binarytohex and hextobinary function should be there

Also I have added some function that do basic arithmetic.

Rest things are commented out there.

Rest are using basic concept.

Note:- My code does not implement 5 th part i.e is write back part