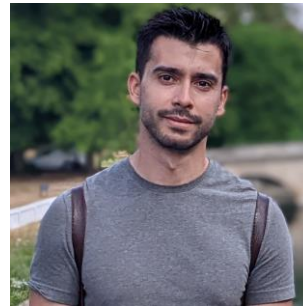


Dependency Graph Parsing as Sequence Labeling



Ana Ezquerro



David Vilares

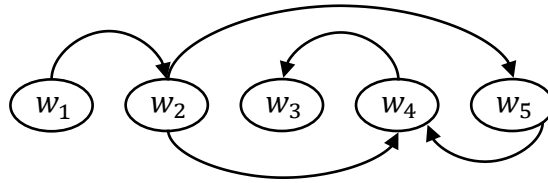


Carlos Gómez-Rodríguez

Motivation: Approaches in Dependency Graph Parsing

Dependency **Graph** Parsing...

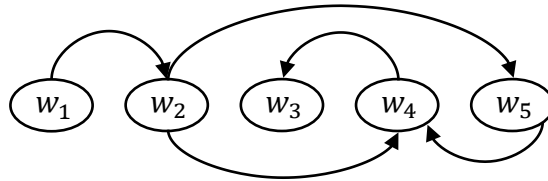
- Sentence: $W = (w_1, \dots, w_n)$.
- Arcs: $A = \{(h, d): w_h \rightarrow w_d, h \neq d\}$.



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It's **not** Dependency *Tree* Parsing.

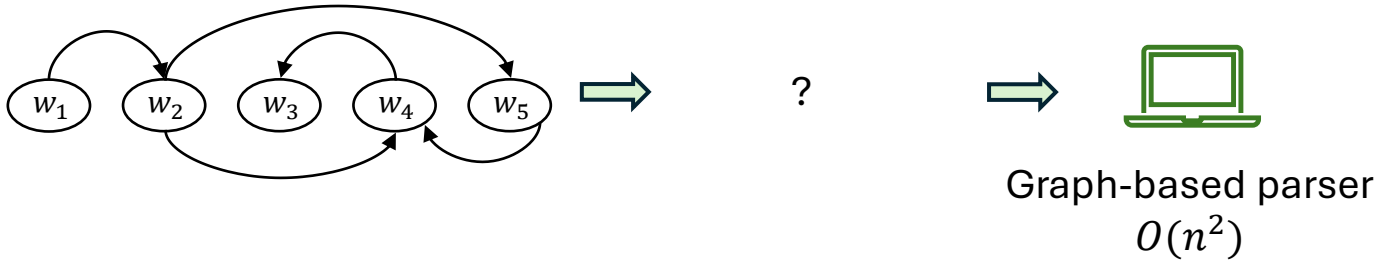
- Multiple roots and heads per node.
- Optionally non-connected.
- Cycles are allowed.



Motivation: Approaches in Dependency Graph Parsing

Dependency **Graph** Parsing...

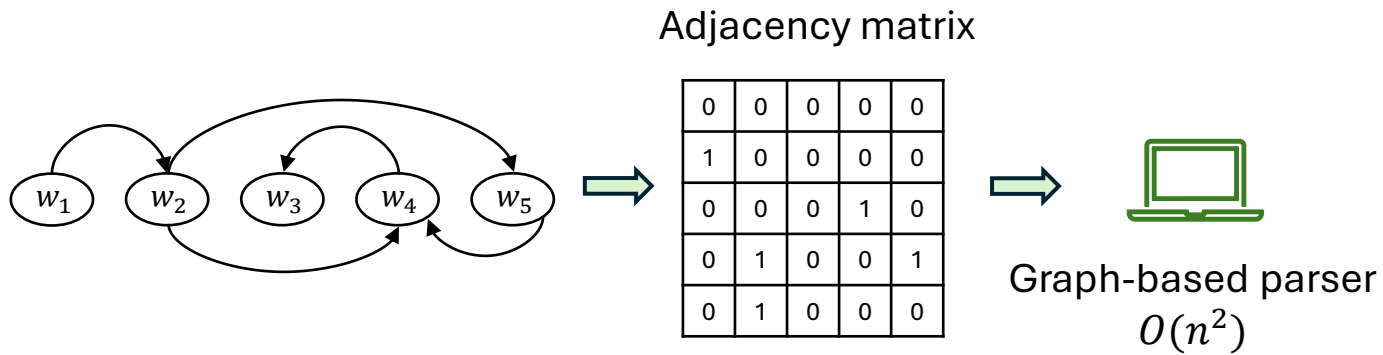
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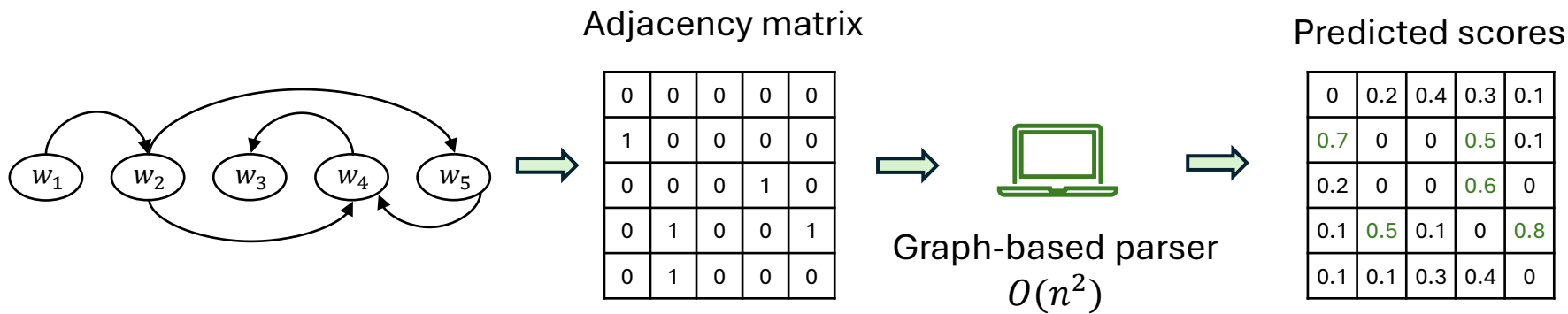
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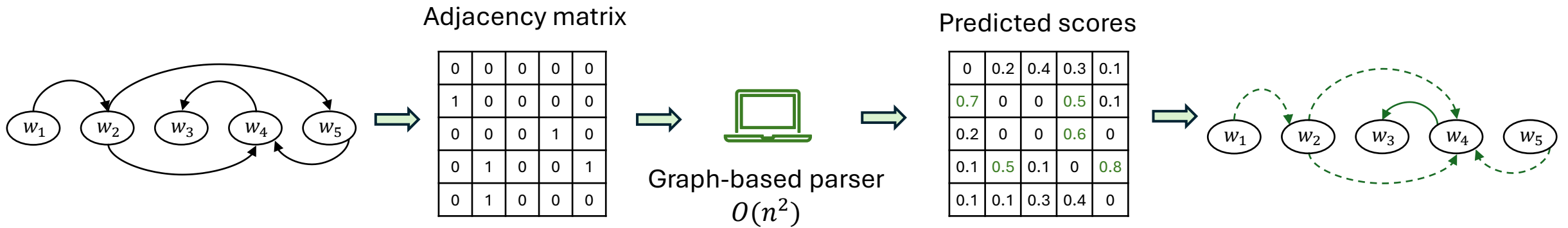
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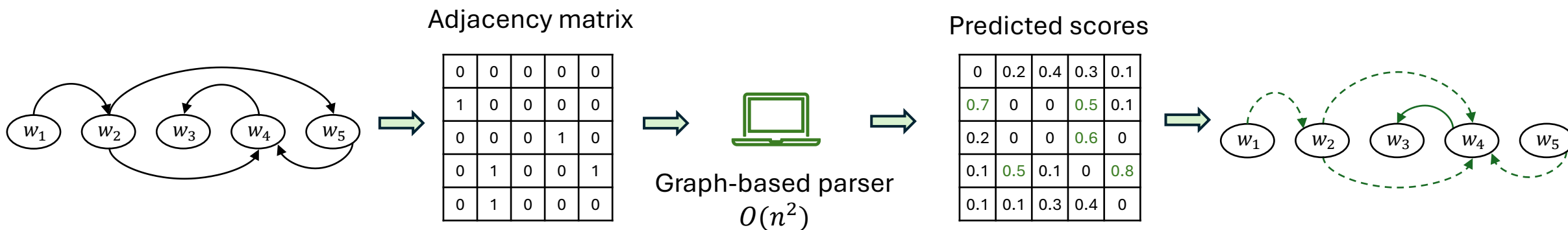


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- [Dozat & Manning \(2018\)](#): ~ 90 UF
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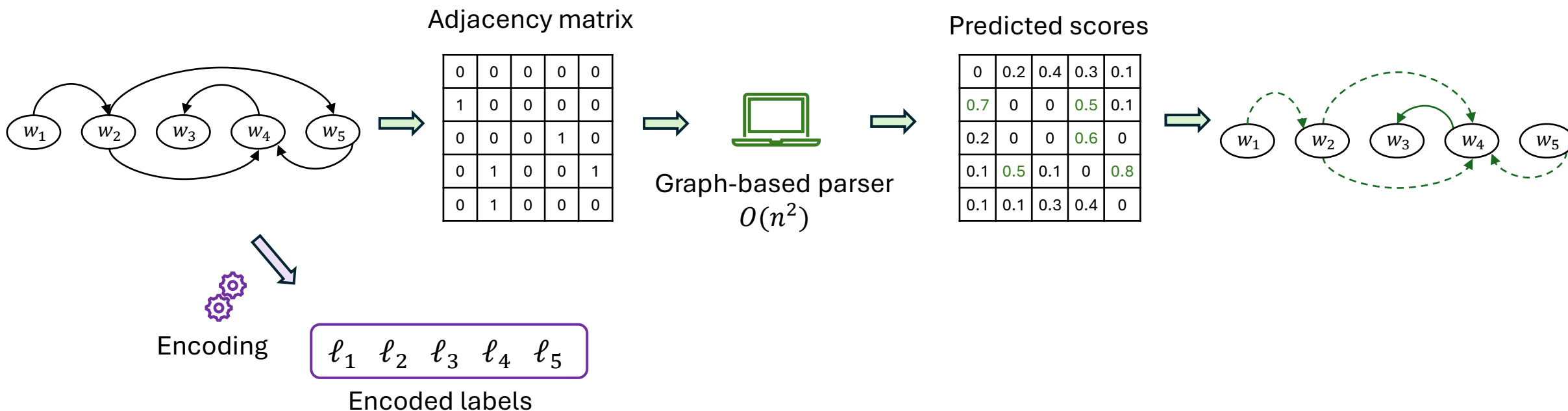


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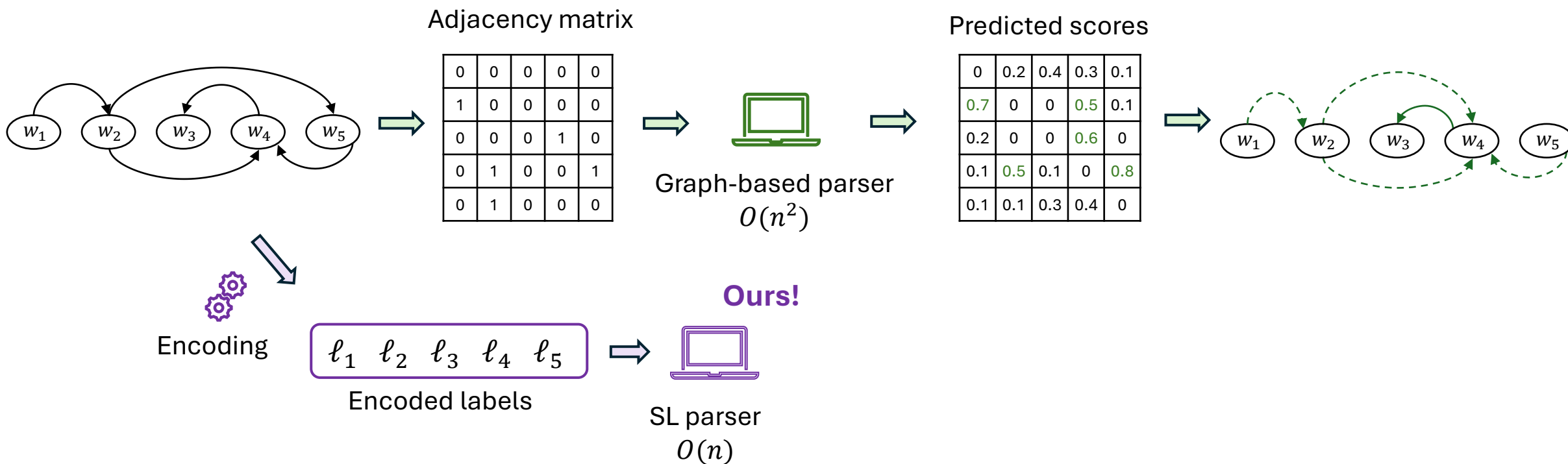


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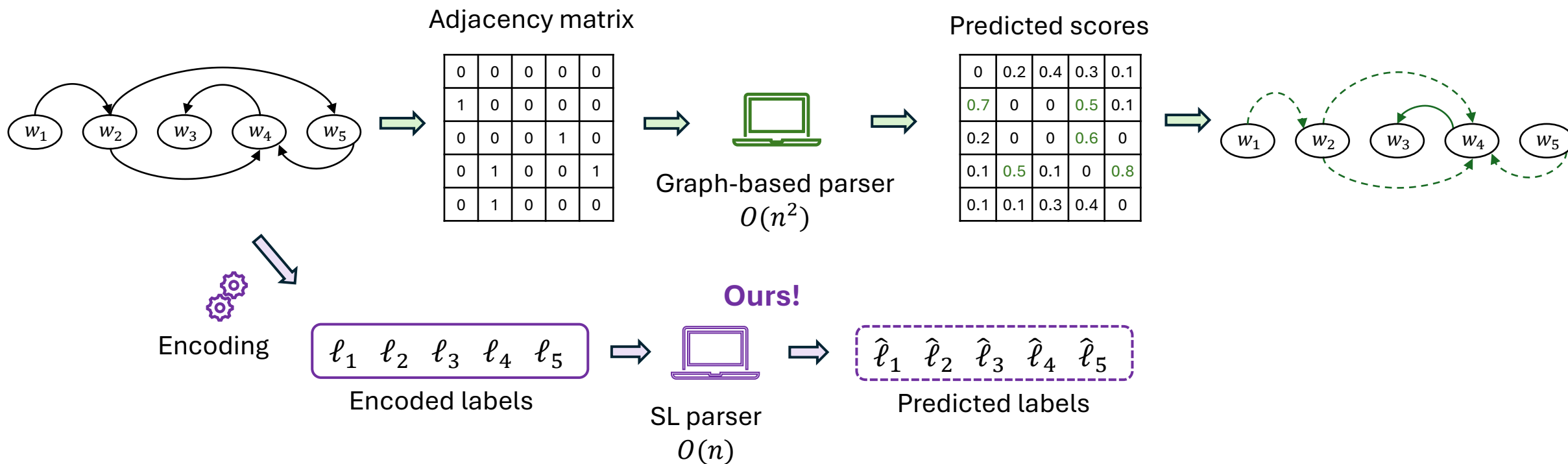
... as **Sequence Labeling**.

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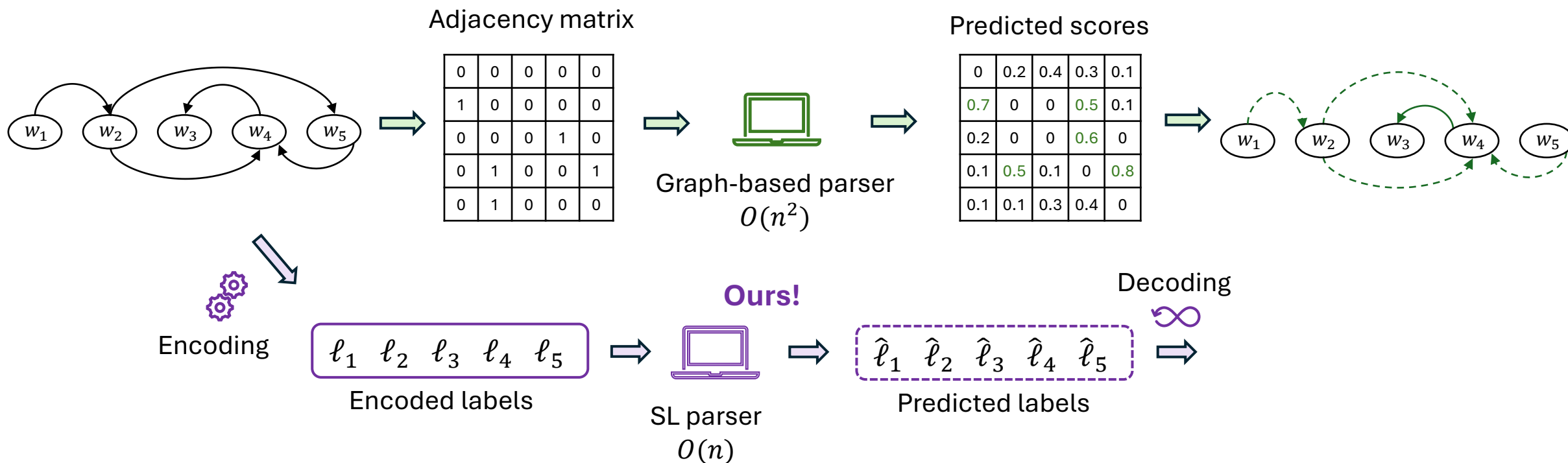
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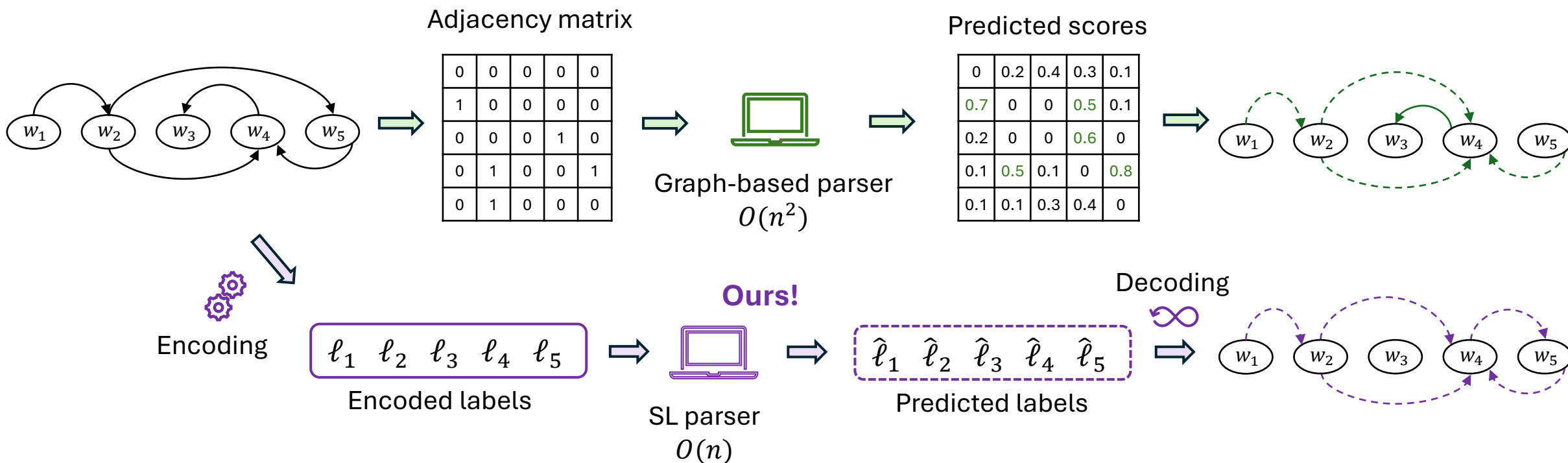
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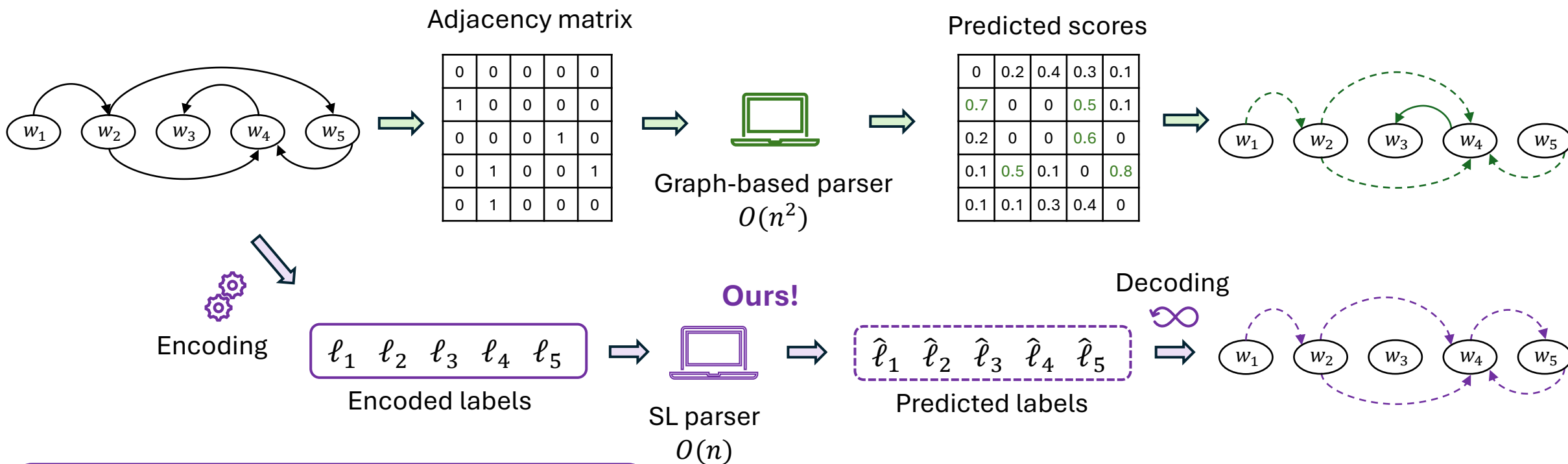
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Motivation: Approaches in Dependency Graph Parsing

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1. Define the encoding and decoding functions.
2. Then tackle as a classical tagging task

... as **Sequence Labeling**.

 We propose various encoding () and decoding () functions

UNBOUNDED ENCODINGS

Positional Encodings

Absolute
Relative

k -planar Bracketing Encoding

$\langle / \backslash \rangle$

Non-fixed number of unique labels.

BOUNDED ENCODINGS

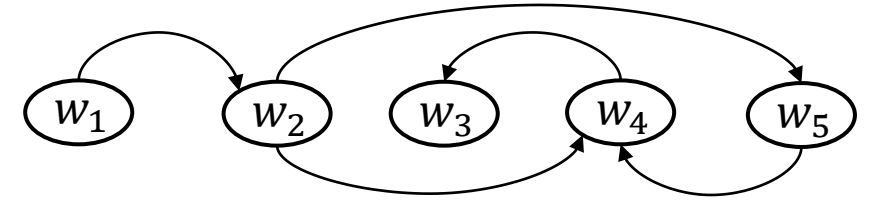
$4k$ -bit Encoding

$6k$ -bit Encoding

Fixed number of unique labels.

Positional encodings: absolute (A) and relative (R)

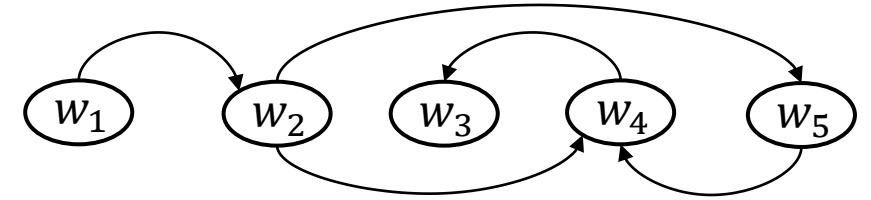
- **Absolute:** Sorted sequence of head positions.



	ℓ_1	ℓ_2	ℓ_3	ℓ_4	ℓ_5
Absolute:	(,)	(1)	(4)	(2,5)	(2)

Positional encodings: absolute (A) and relative (R)

- **Absolute:** Sorted sequence of head positions.
- **Relative:** Sorted sequence of head relative positions.



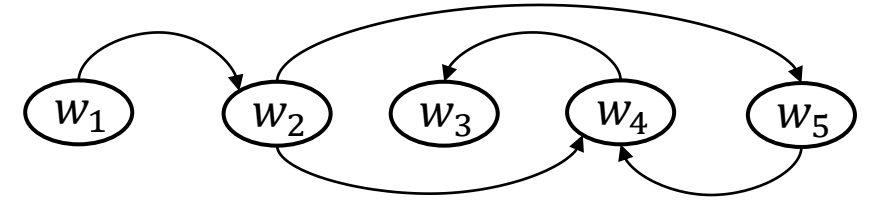
	ℓ_1	ℓ_2	ℓ_3	ℓ_4	ℓ_5
Absolute:	(,)	(1)	(4)	(2,5)	(2)
Relative:	(,)	(-1)	(1)	(-2, 1)	(-3)

Positional encodings: absolute (A) and relative (R)

- **Absolute:** Sorted sequence of head positions.
- **Relative:** Sorted sequence of head relative positions.



∞ Fast and simple decoding (easy to parallelize).



	ℓ_1	ℓ_2	ℓ_3	ℓ_4	ℓ_5
Absolute:	(,)	(1)	(4)	(2,5)	(2)
Relative:	(,)	(-1)	(1)	(-2, 1)	(-3)

k -planar Bracketing Encoding



$\ell_i \sim$

$\begin{matrix} < \\ / \\ \backslash \\ > \end{matrix}$

k -planar Bracketing Encoding



$\ell_i \sim$

$\begin{matrix} < \\ / \\ \backslash \\ > \end{matrix}$

$\ell_i = <</$

k -planar Bracketing Encoding



$\ell_i \sim$

$< : \text{There is a head at the right.}$

$/$
 \backslash
 $>$

k -planar Bracketing Encoding



$\ell_i \sim$

$<$: There is a head at the right.
/
\ : There is a dependent at the right.
 $>$

k -planar Bracketing Encoding



$\ell_i \sim$

- $<$: There is a head at the right.
- $/$: There is a dependent at the right.
- \backslash : There is a dependent at the left.
- $>$

k -planar Bracketing Encoding



$\ell_i \sim$

- \lessdot : There is a head at the right.
- $/$: There is a dependent at the right.
- \backslash : There is a dependent at the left.
- \gtrdot : There is a head is at the left.

k -planar Bracketing Encoding



$\ell_i \sim$

- $<$: There is a head at the right.
- $/$: There is a dependent at the right.
- \backslash : There is a dependent at the left.
- $>$: There is a head is at the left.

- Distribute the arcs in planes (non-crossing arcs).
- Add symbol $*$ for brackets of other planes.
- First plane: $</\backslash>$.
- Second plane: $<*/*\backslash*>*$.
- Third plane: $<**/****>**$.

k -planar Bracketing Encoding

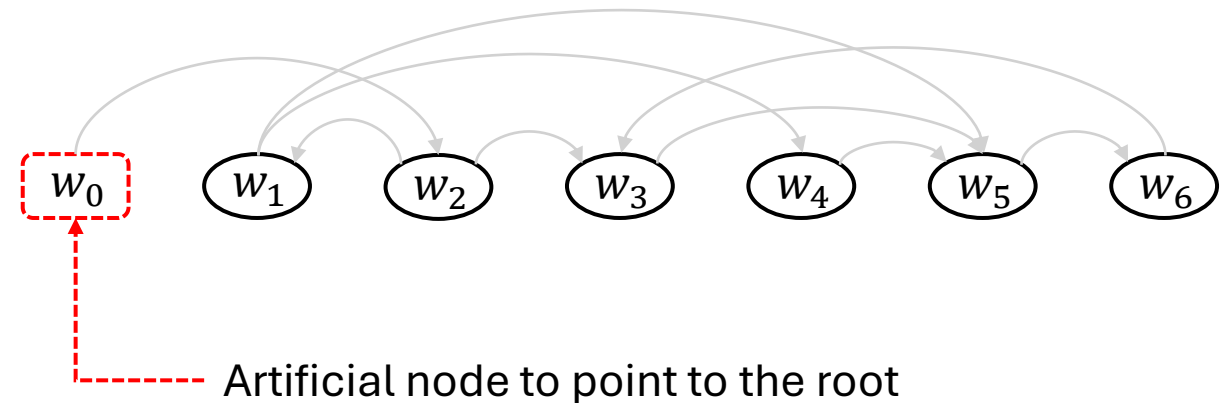


$\ell_i \sim$

- \prec : There is a head at the right.
- $/$: There is a dependent at the right.
- \backslash : There is a dependent at the left.
- \succ : There is a head is at the left.

- Distribute the arcs in planes (non-crossing arcs).
- Add symbol $*$ for brackets of other planes.
- First plane: $\prec / \backslash \succ$.
- Second plane: $\prec * / * \backslash * \succ *$.
- Third plane: $\prec ** / ** \backslash ** \succ **$.

Step 1: Distribute in planes.



k -planar Bracketing Encoding

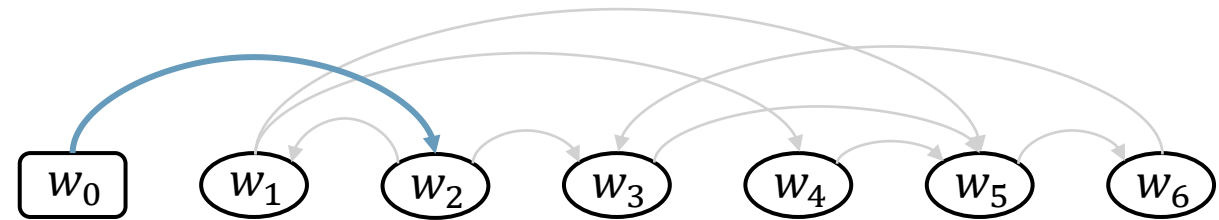


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k -planar Bracketing Encoding

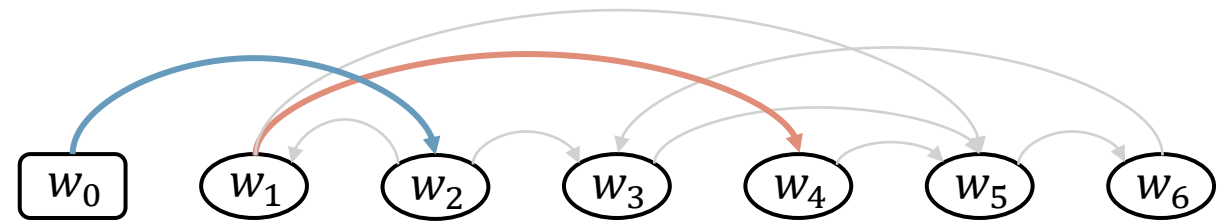


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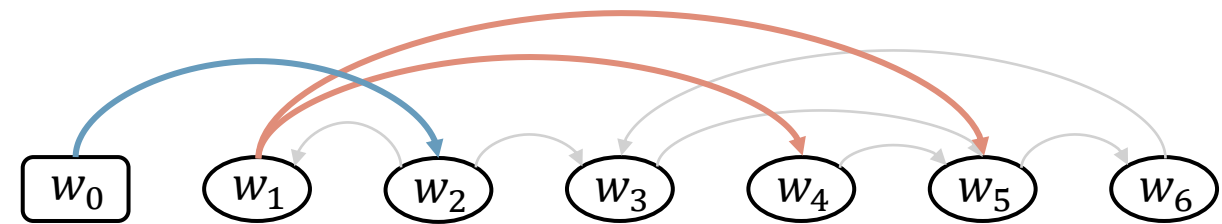


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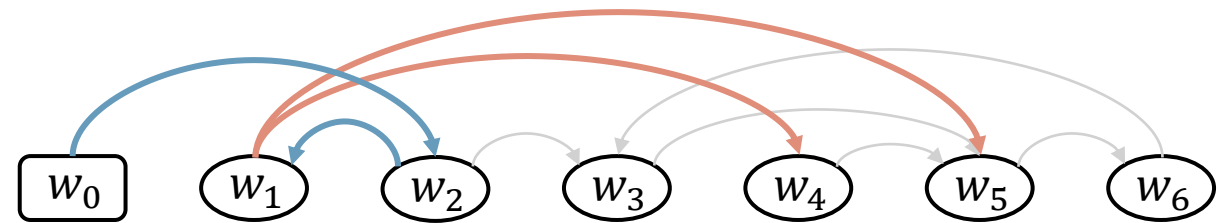


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k -planar Bracketing Encoding

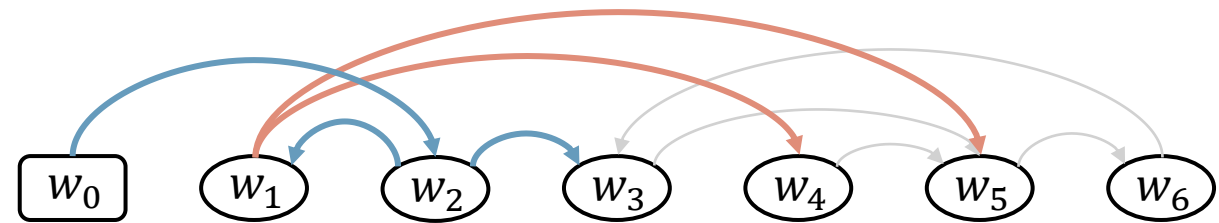


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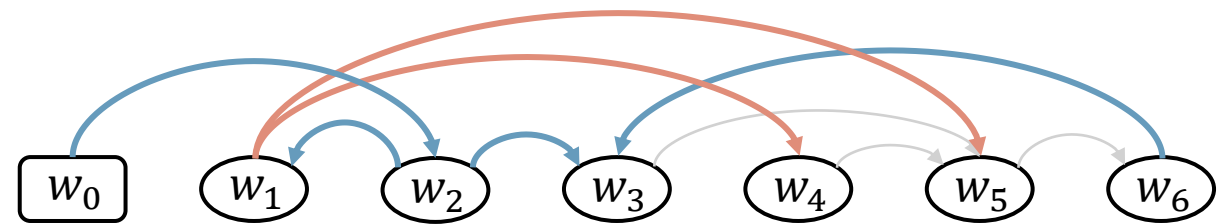


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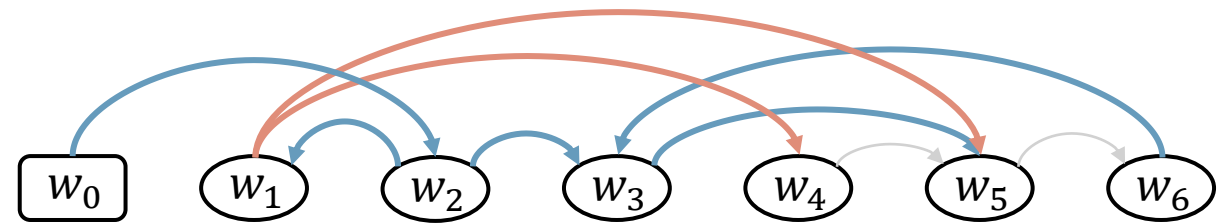


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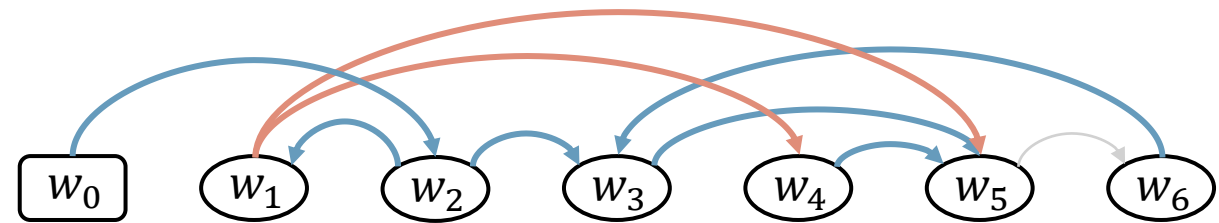


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k -planar Bracketing Encoding

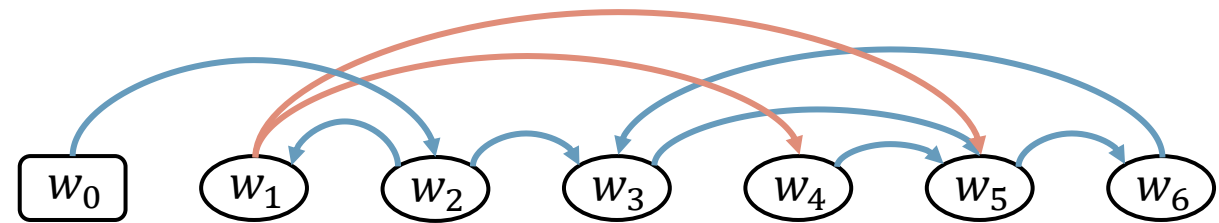


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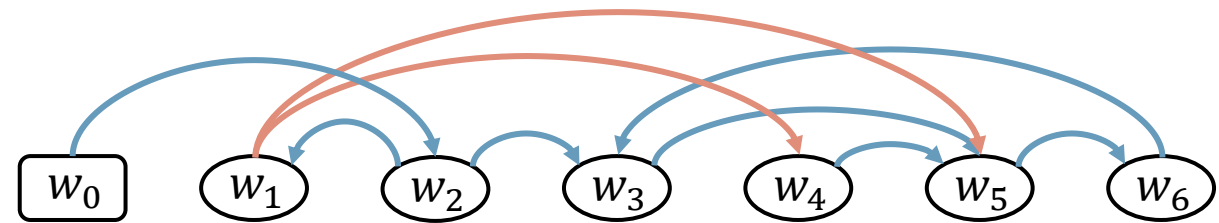


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Step 2: Assign labels to each plane.



k -planar Bracketing Encoding

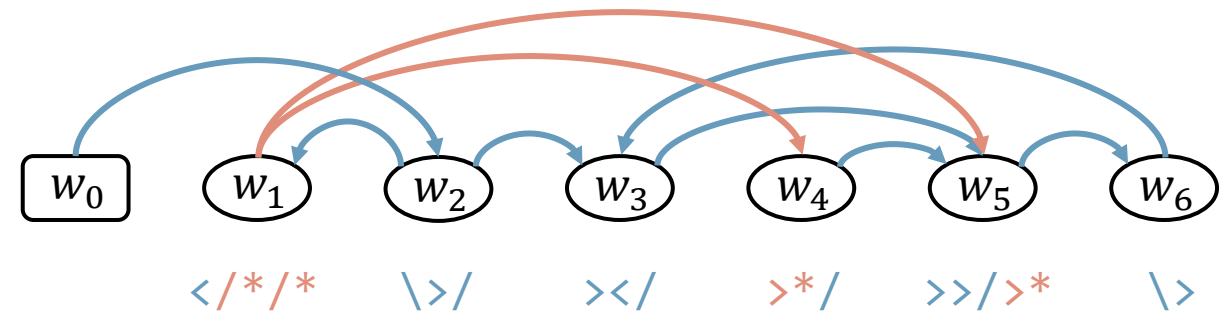


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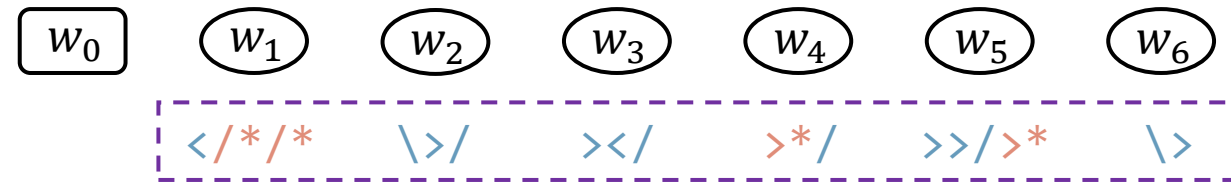


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Step 2: Assign labels to each plane.



We predict this!

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How do we recover the arcs from the labels (ℓ_1, \dots, ℓ_n) ?

k -planar Bracketing Encoding



- Two stacks (**L** and **R**) per plane.
- **L** for left arcs (**<**\) and **R** right arcs (/ **>**).

k -planar Bracketing Encoding



- Two stacks (**L** and **R**) per plane.
- **L** for left arcs ($\langle \backslash$) and **R** right arcs ($/ \rangle$).



L

R

k -planar Bracketing Encoding



- Two stacks (**L** and **R**) per plane.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.

L

R

k -planar Bracketing Encoding



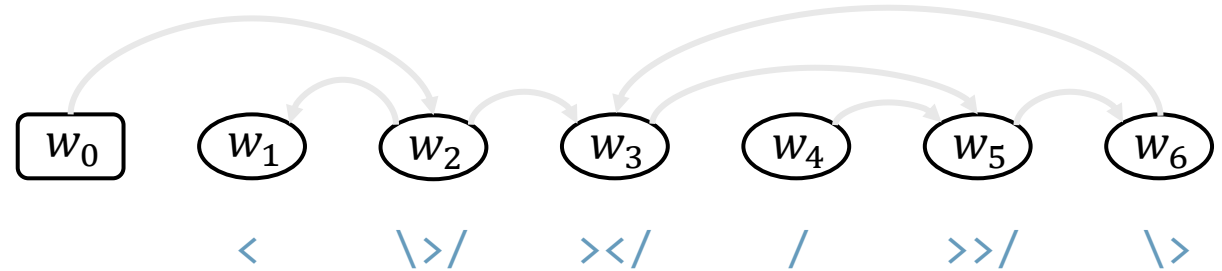
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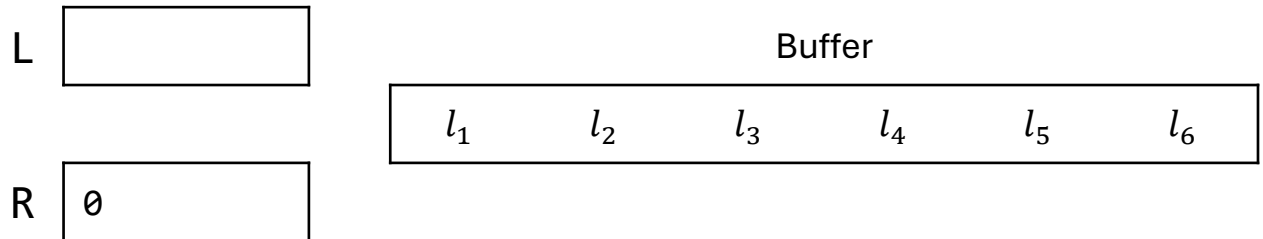
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Initial state: Labels to buffer and **R** with the root dependency.



k -planar Bracketing Encoding



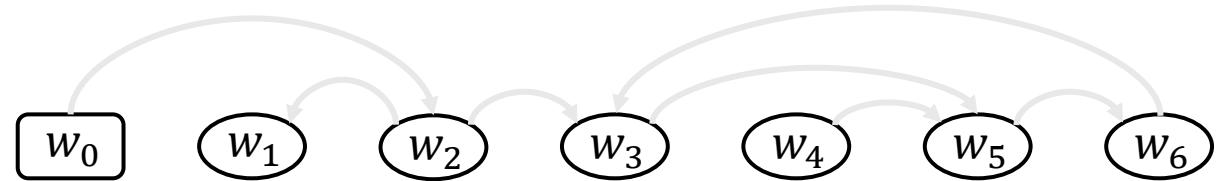
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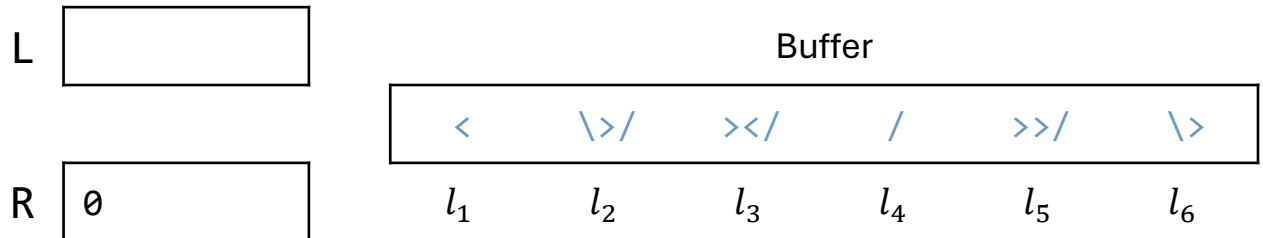
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k -planar Bracketing Encoding



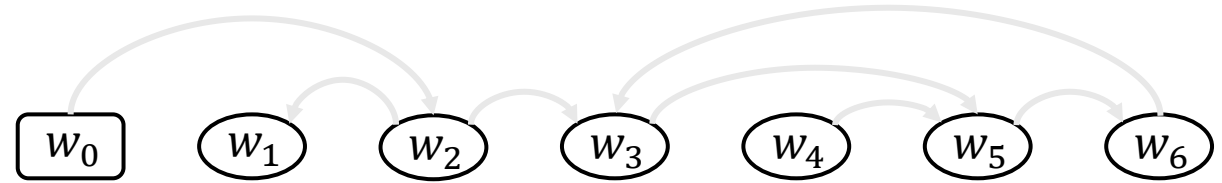
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- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

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$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 1.

L

R

Buffer

$<$	$\backslash>/$	$></$	$/$	$>>/$	$\backslash>$
l_1	l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



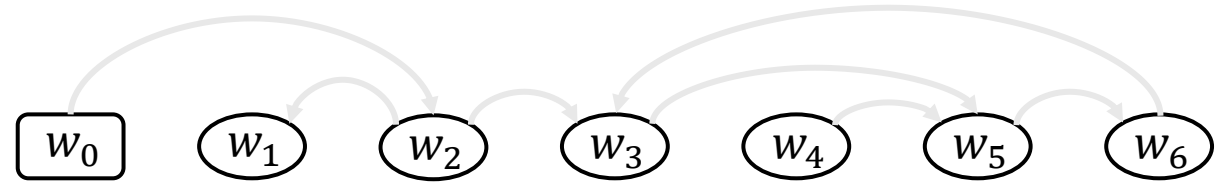
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$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 1.

L

1

R

\emptyset

Buffer

	$\backslash>/$	$></$	$/$	$>>/$	$\backslash>$
l_1	l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



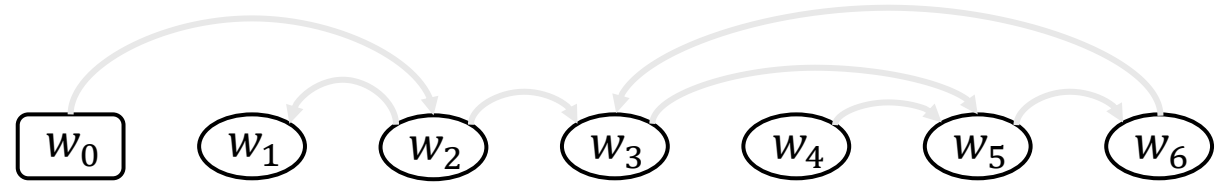
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($\langle \backslash$) and **R** right arcs ($/ >$).

\langle : Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 1.

L

1

R

\emptyset

Buffer

	$\backslash > /$	$> \langle /$	$/$	$> > /$	$\backslash >$
l_1	l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



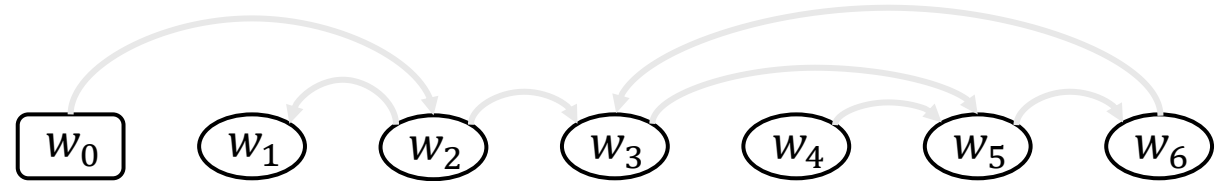
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($\langle \backslash$) and **R** right arcs ($/ >$).

\langle : Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



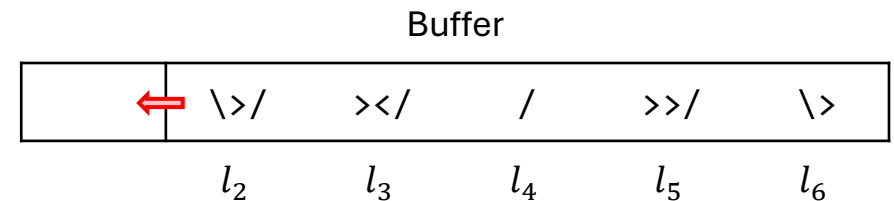
 **Step 1.**

L

1

R

\emptyset



k -planar Bracketing Encoding



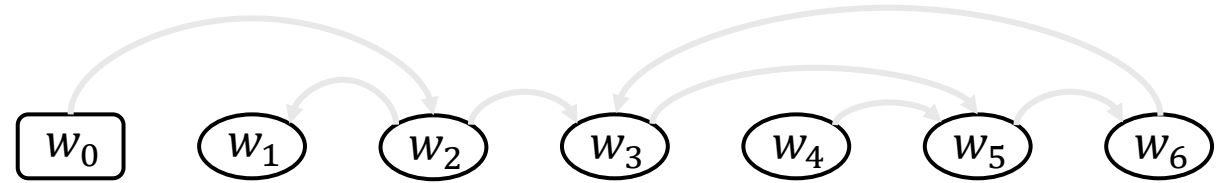
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

1

R

\emptyset

Buffer

$\backslash>/$	$></$	$/$	$>>/$	$\backslash>$
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



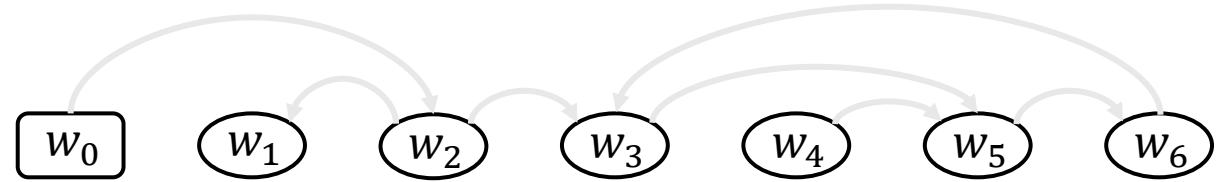
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

1

R

\emptyset

Buffer

$\backslash > /$	$> < /$	$/$	$> > /$	$\backslash >$
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



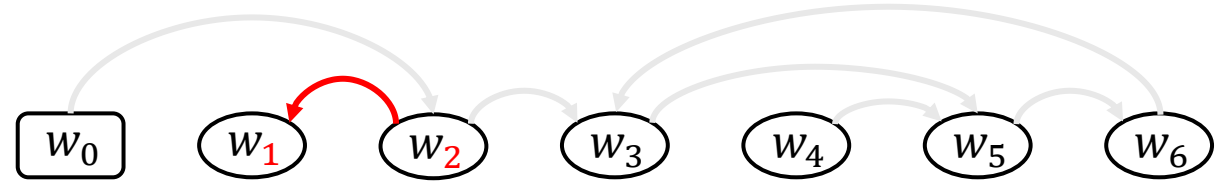
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

1

R

\emptyset

Buffer

$\backslash>/$	$></$	$/$	$>>/$	$\backslash>$
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



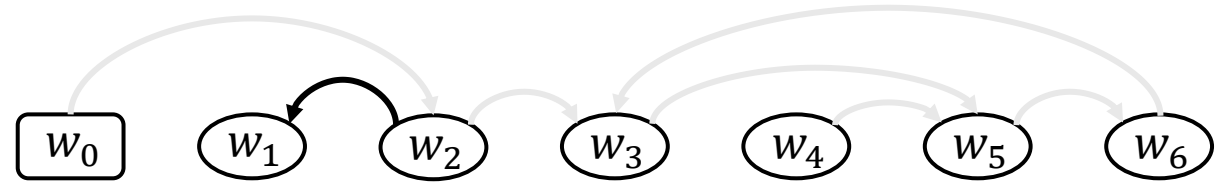
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

R

Buffer

$>/$	$></$	$/$	$>>/$	$\backslash>$
------	-------	-----	-------	---------------

l_2

l_3

l_4

l_5

l_6

k -planar Bracketing Encoding



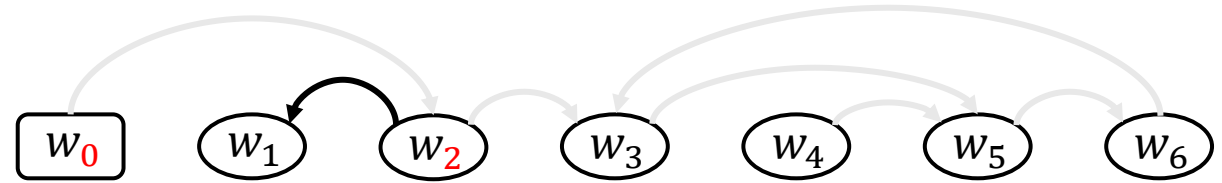
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

R

\emptyset

Buffer

$>/$	$></$	$/$	$>>/$	$\backslash>$
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



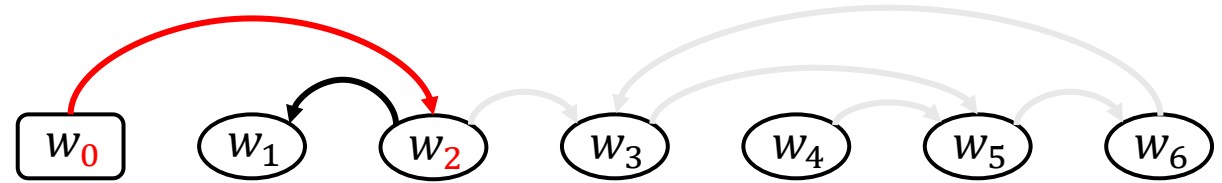
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

R

\emptyset

Buffer

$>/$	$></$	$/$	$>>/$	$\backslash>$
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



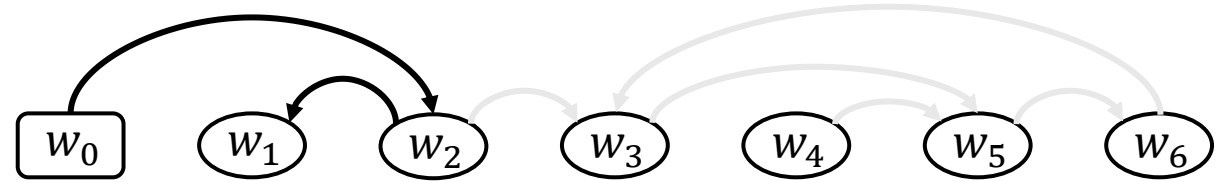
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

R

Buffer

$/$	$></$			
	$/$	$>>/$	$\backslash>$	
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



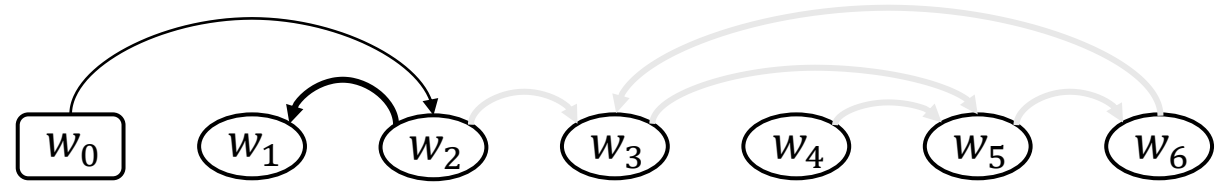
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



 Step 2.

L

R

Buffer

$/$	$></$	$/$	$>>/$	$\backslash>$
-----	-------	-----	-------	---------------

l_2

l_3

l_4

l_5

l_6

k -planar Bracketing Encoding



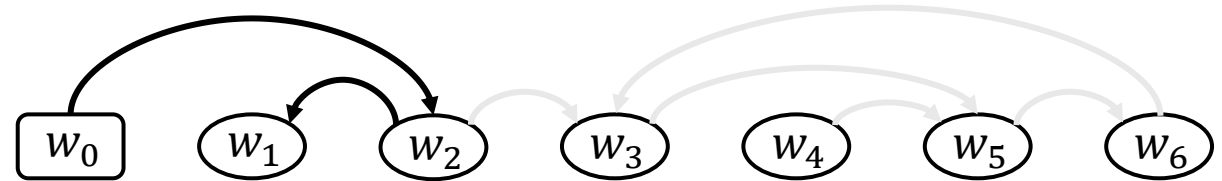
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 2.

L

R

2

Buffer

	$></$	$/$	$>>/$	$\backslash>$
l_2	l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



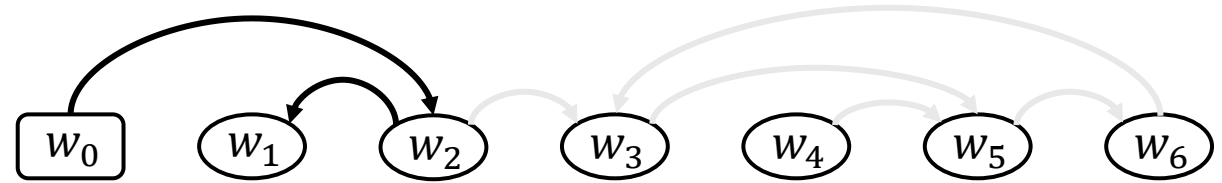
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.

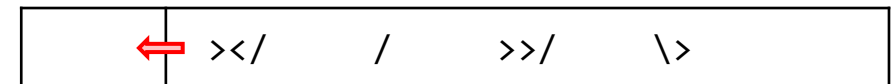


Step 2.

L

R

Buffer



l_3 l_4 l_5 l_6

k -planar Bracketing Encoding



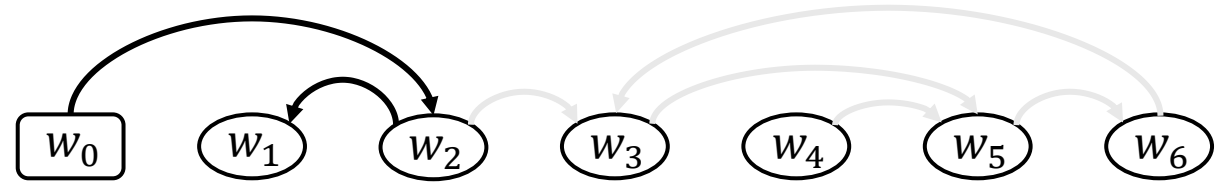
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



 Step 3.

L

R

2

Buffer

$></$	$/$	$>>/$	$\backslash>$
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l_3

l_4

l_5

l_6

k -planar Bracketing Encoding



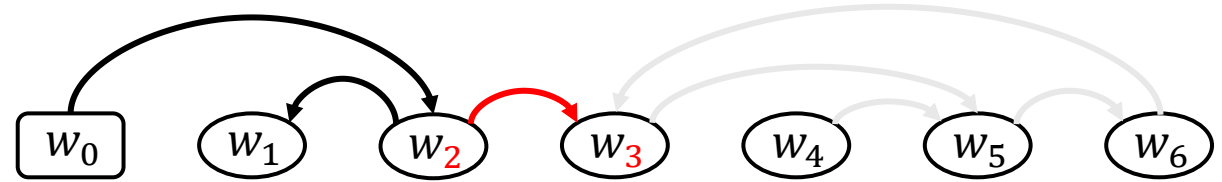
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 3.

L

R

2

Buffer

$></$	$/$	$>>/$	$\backslash>$
-------	-----	-------	---------------

l_3

l_4

l_5

l_6

k -planar Bracketing Encoding



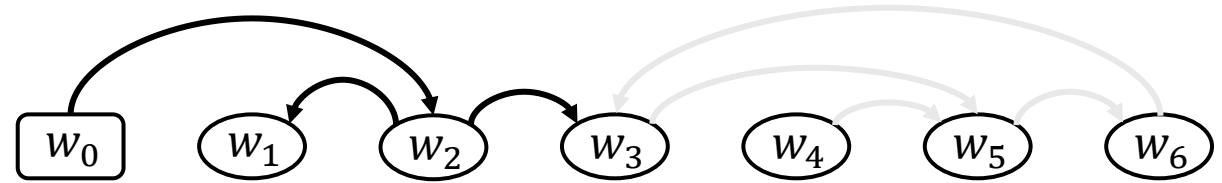
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 3.

L

R

Buffer

$</$	$/$	$>>/$	$\backslash>$
------	-----	-------	---------------

l_3

l_4

l_5

l_6

k -planar Bracketing Encoding



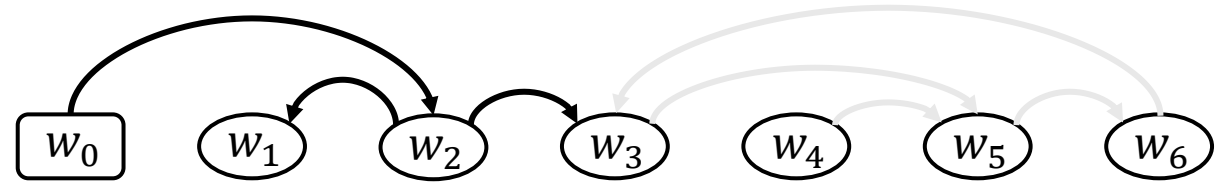
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 3.

L

3

R

--

Buffer

$</$	$/$	$>>/$	$\backslash>$
------	-----	-------	---------------

l_3

l_4

l_5

l_6

k -planar Bracketing Encoding



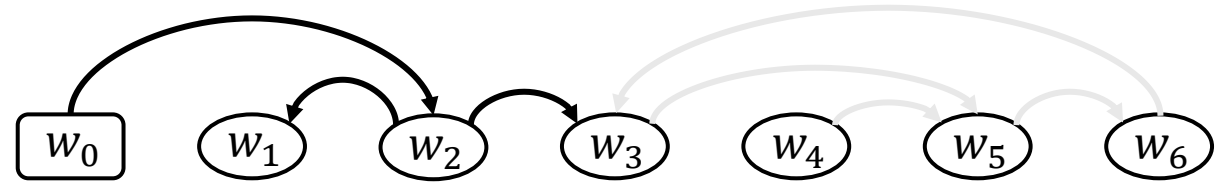
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 3.

L

3

R

3

Buffer

$/$	$/$	$>>/$	$\backslash>$
l_3	l_4	l_5	l_6

k -planar Bracketing Encoding



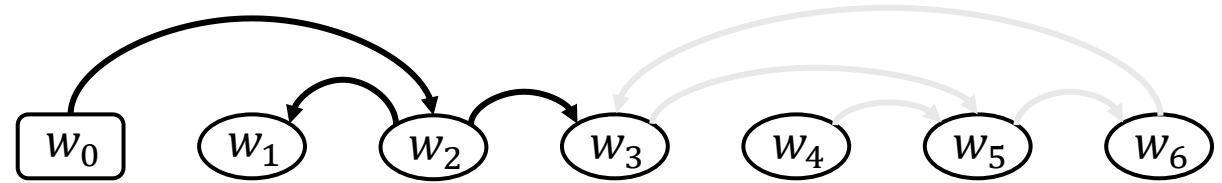
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



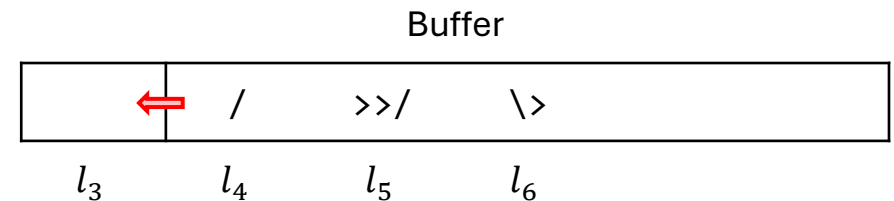
Step 3.

L

3

R

3



k -planar Bracketing Encoding



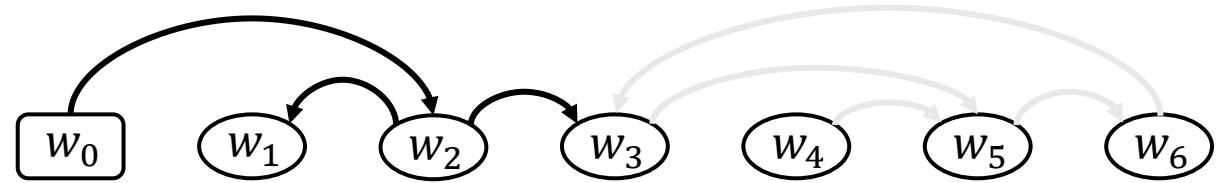
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

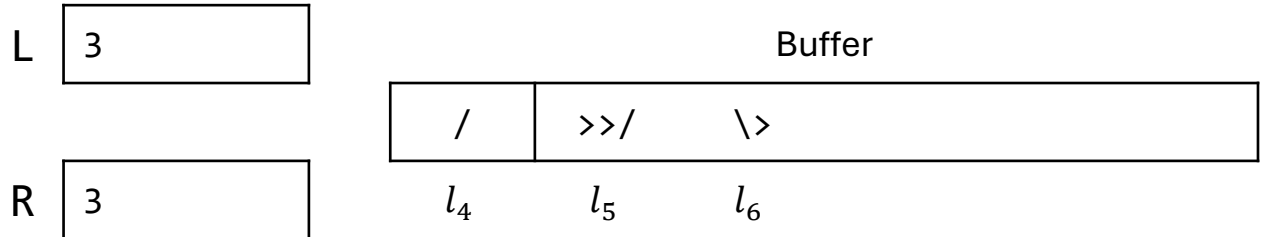
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 4.



k -planar Bracketing Encoding



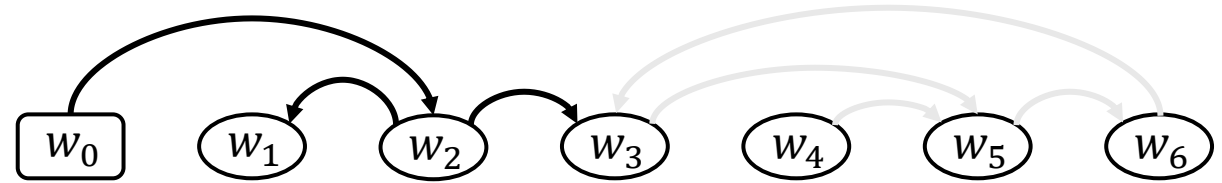
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 4.

L

3

R

3	4
---	---

Buffer

$/$	$>>/$	$\backslash>$
l_4	l_5	l_6

k -planar Bracketing Encoding



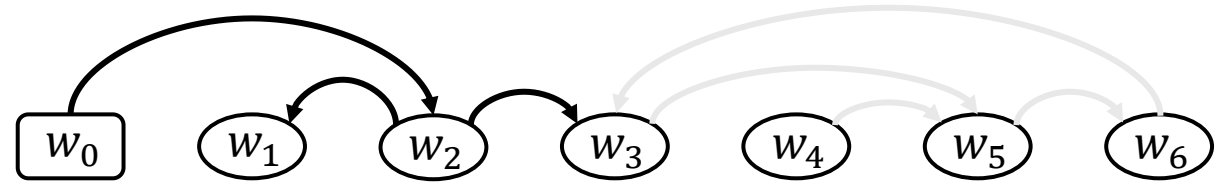
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



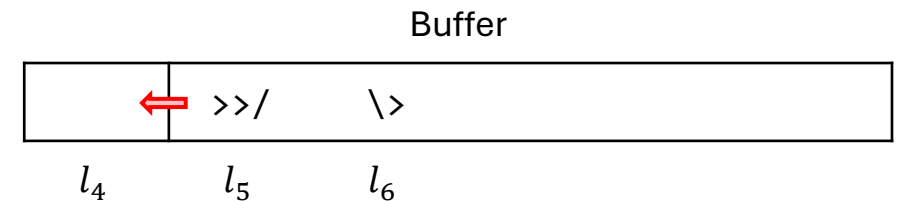
Step 4.

L

3

R

3	4
---	---



k -planar Bracketing Encoding



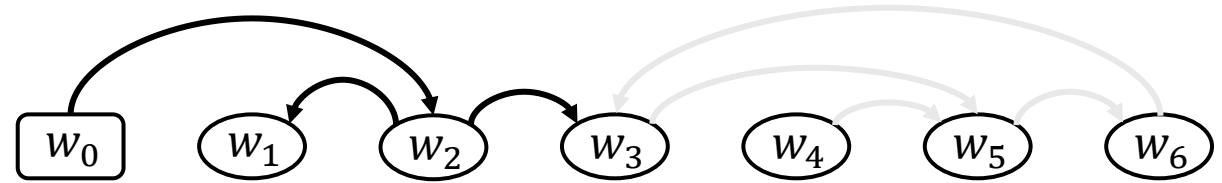
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

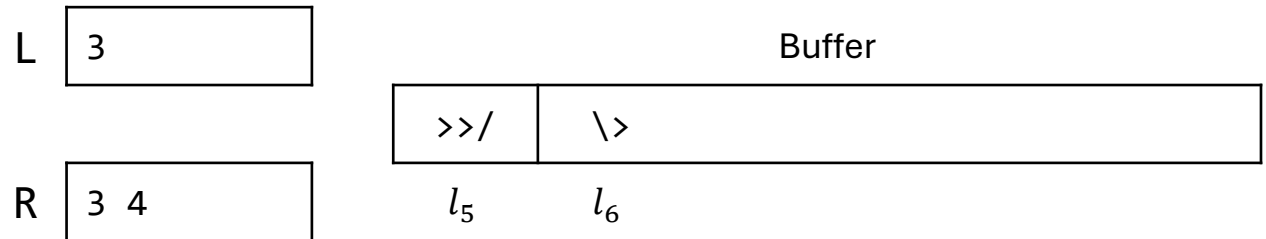
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 5.



k -planar Bracketing Encoding



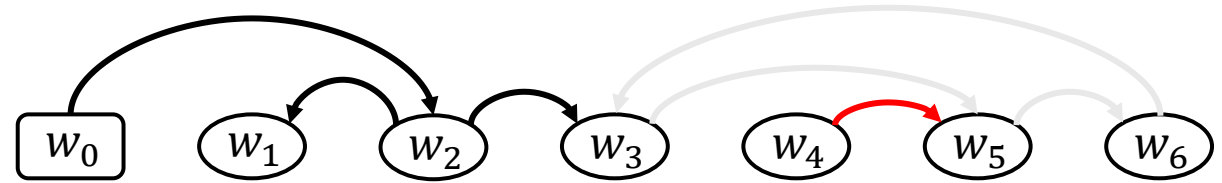
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

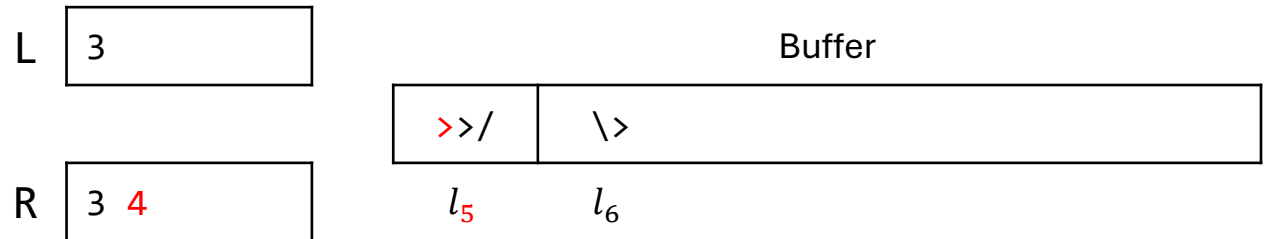
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 5.



k -planar Bracketing Encoding



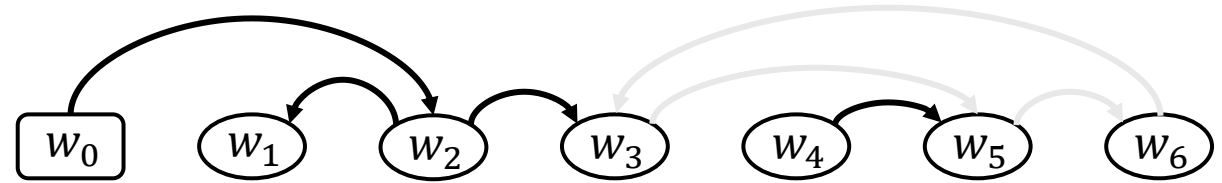
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

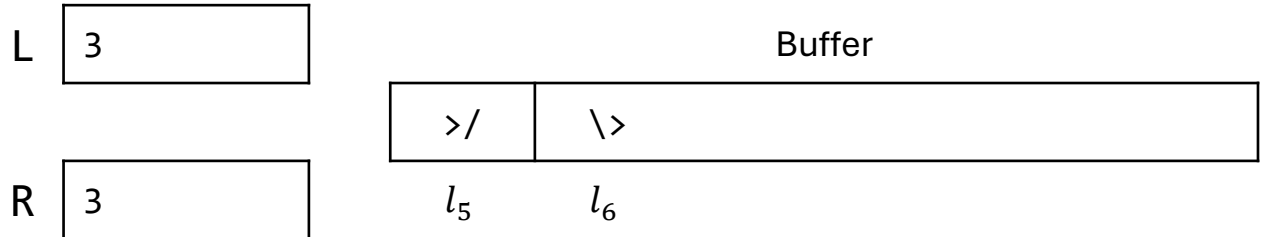
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 5.



k -planar Bracketing Encoding



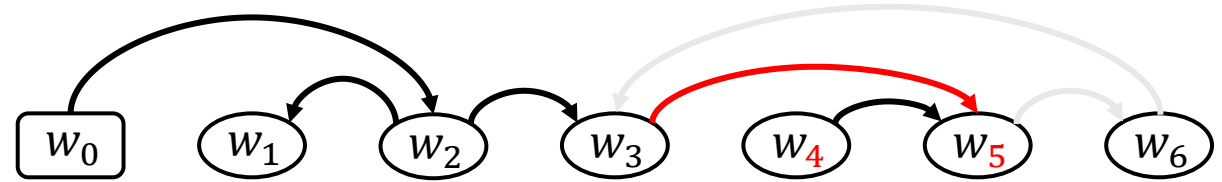
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

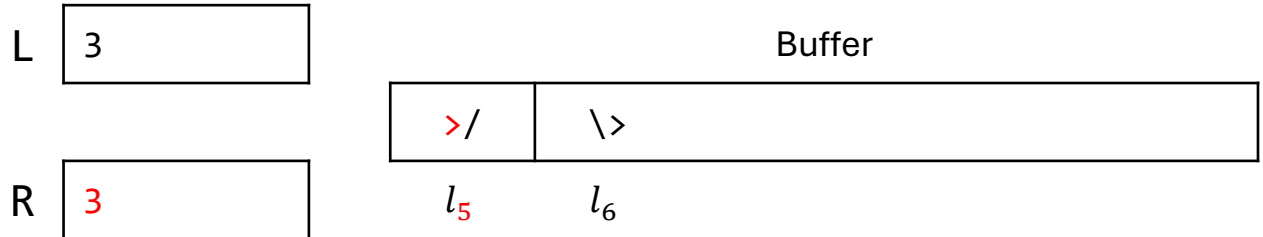
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 5.



k -planar Bracketing Encoding



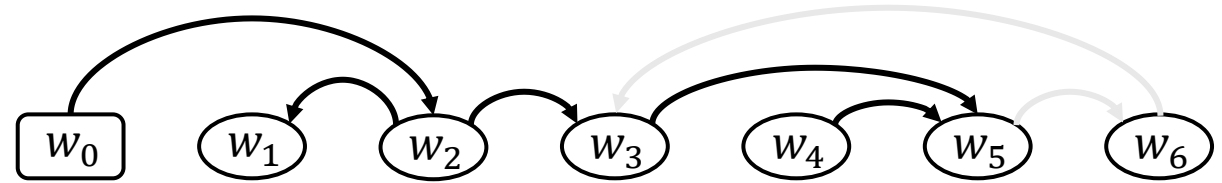
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

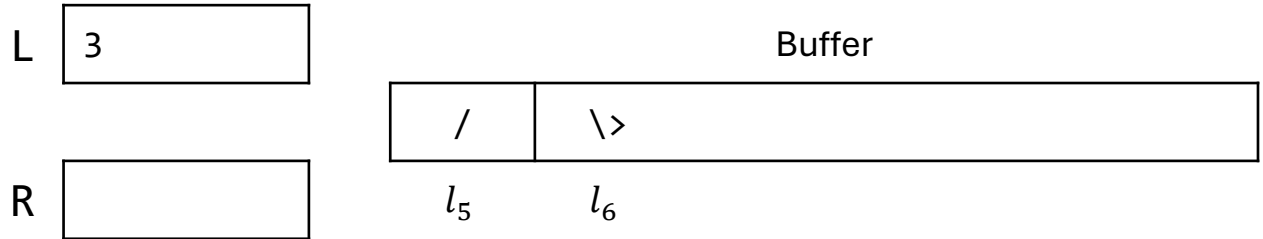
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 5.



k -planar Bracketing Encoding



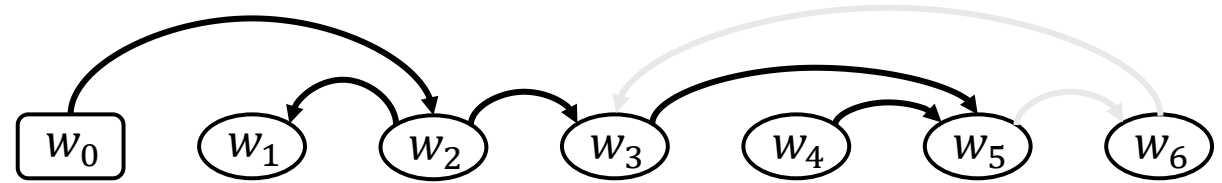
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 5.

L

3	5
---	---

R

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Buffer

$/$	$\backslash>$
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l_5

l_6

k -planar Bracketing Encoding



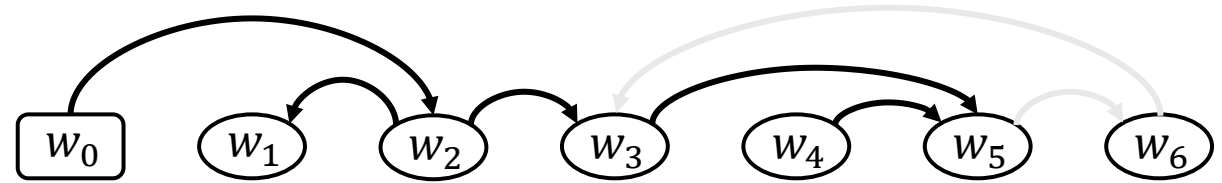
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



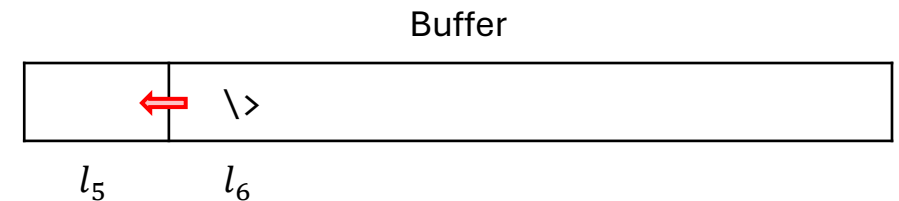
Step 5.

L

3	5
---	---

R

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k -planar Bracketing Encoding



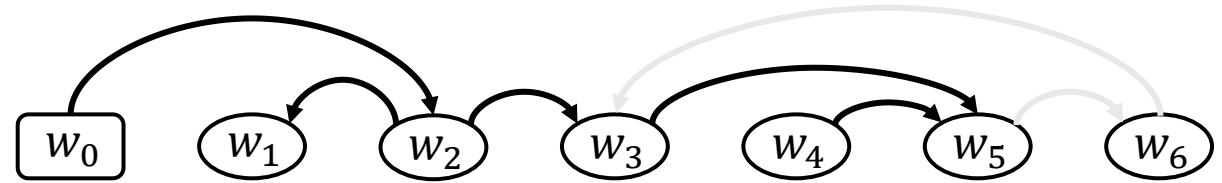
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 6.

L

3	5
---	---

R

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Buffer

$\backslash >$	
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l_6

k -planar Bracketing Encoding



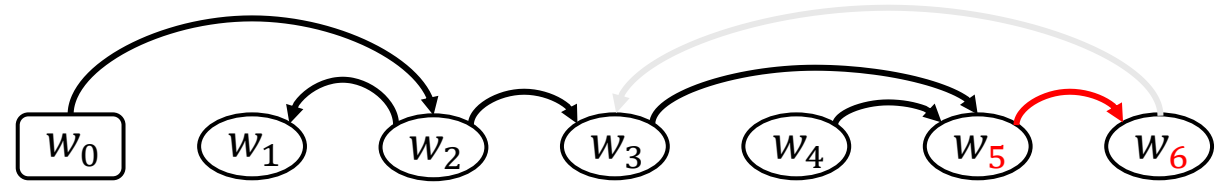
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 6.

L

3	5
---	---

R

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Buffer

$\backslash >$	
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l_6

k -planar Bracketing Encoding



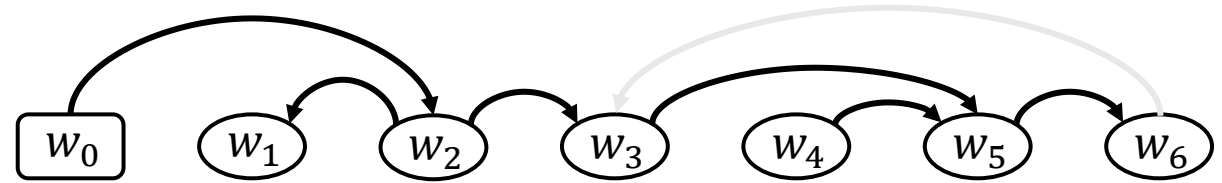
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

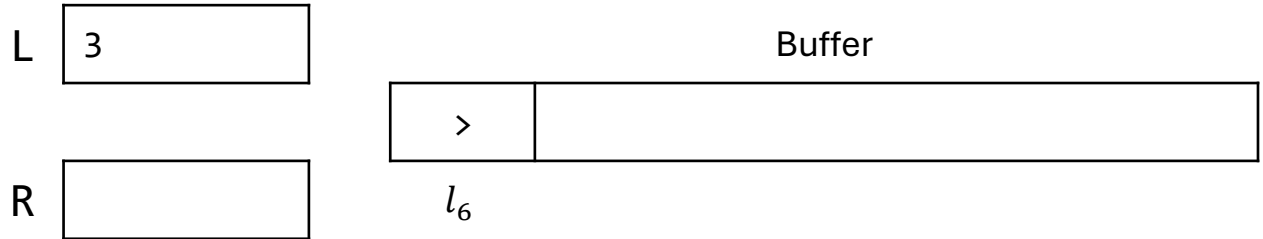
$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 6.



k -planar Bracketing Encoding



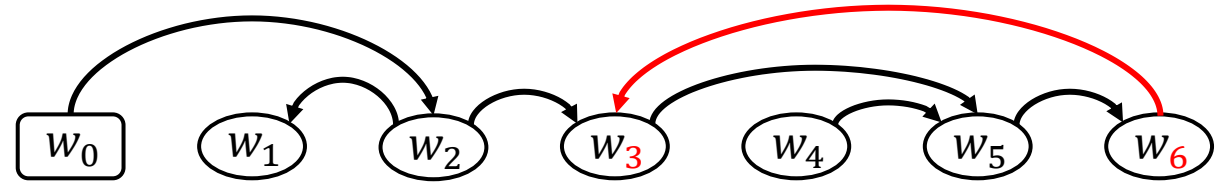
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.



Step 6.

L

3

R

--

Buffer

$>$	
-----	--

l_6

k -planar Bracketing Encoding



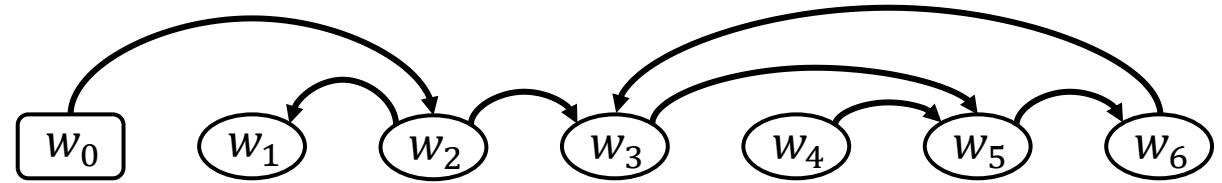
- Two stacks (**L** and **R**) per plane.
- L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).

$<$: Add i to **L**.

$/$: Add i to **R**.

\backslash : Resolve **L** $\leftarrow w_i$.

$>$: Resolve **R** $\rightarrow w_i$.

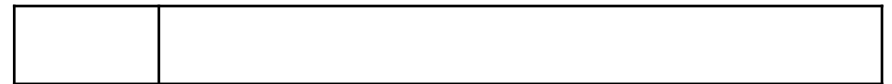


Finished!

L

R

Buffer



Some terminology...

1-planar graph

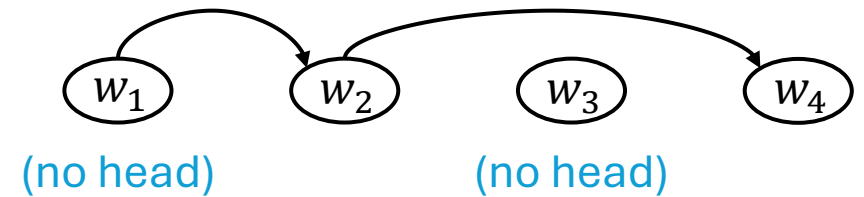
- No crossing arcs.

Relaxed 1-planar *graph*

- No crossing arcs *in the same direction*.

Relaxed 1-planar *tree*

- No crossing arcs *in the same direction*.
- *Only one* head per node.



1-planar



Relaxed 1-planar graph



Relaxed 1-planar tree

Some terminology...

1-planar graph

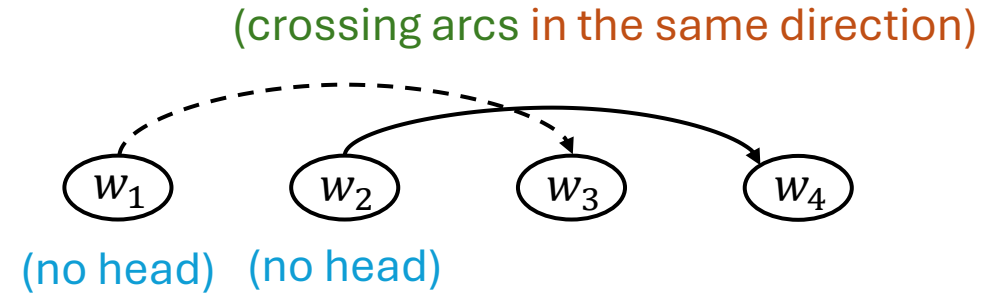
- No crossing arcs.

Relaxed 1-planar *graph*

- No crossing arcs *in the same direction*.

Relaxed 1-planar *tree*

- No crossing arcs *in the same direction*.
- *Only one* head per node.



1-planar



Relaxed 1-planar graph



Relaxed 1-planar tree

Some terminology...

1-planar graph

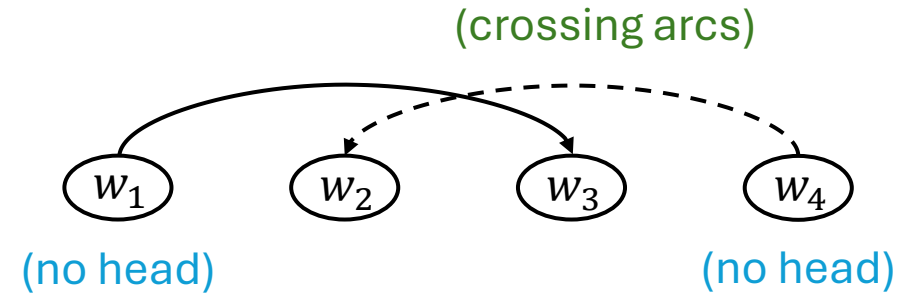
- No crossing arcs.

Relaxed 1-planar *graph*

- No crossing arcs *in the same direction*.

Relaxed 1-planar *tree*

- No crossing arcs *in the same direction*.
- *Only one* head per node.



1-planar



Relaxed 1-planar graph



Relaxed 1-planar tree

Some terminology...

1-planar graph

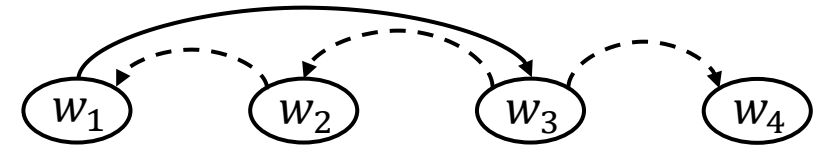
- No crossing arcs.

Relaxed 1-planar *graph*

- No crossing arcs *in the same direction*.

Relaxed 1-planar *tree*

- No crossing arcs *in the same direction*.
- *Only one* head per node.



1-planar



Relaxed 1-planar graph



Relaxed 1-planar tree

4k-bit Encoding

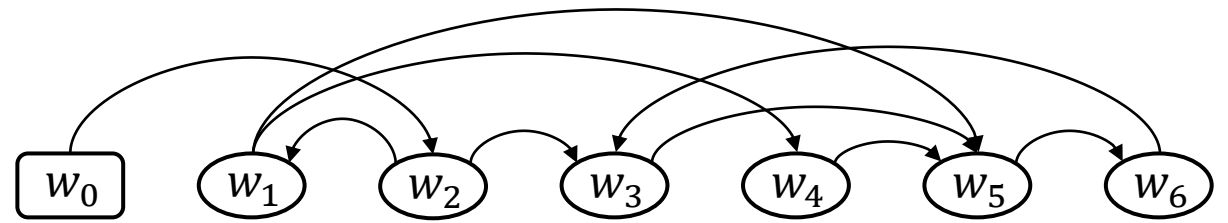
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 1: Distribute the arcs in **relaxed 1-planar trees**.



Sort and incrementally assign!

4k-bit Encoding

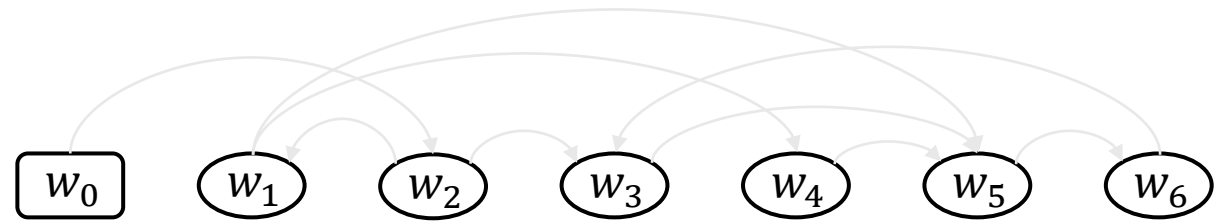
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 1: Distribute the arcs in **relaxed 1-planar trees**.



Sort and incrementally assign!

4k-bit Encoding

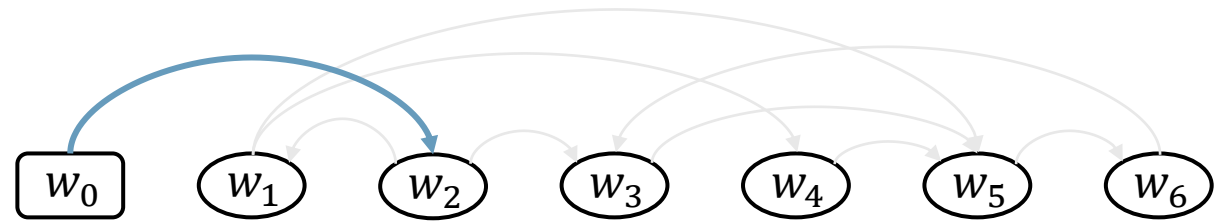
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 1: Distribute the arcs in **relaxed 1-planar trees**.



Sort and incrementally assign!

4k-bit Encoding

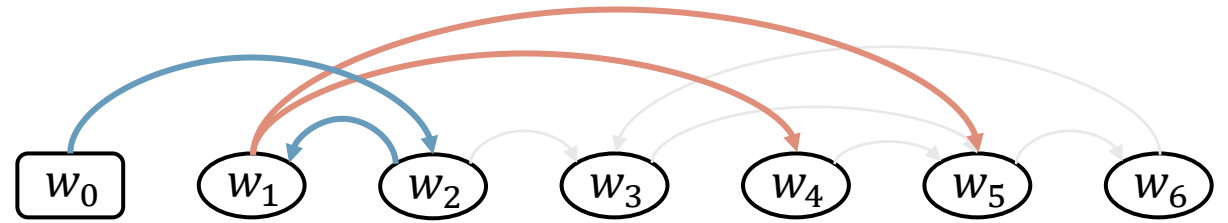
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 1: Distribute the arcs in **relaxed 1-planar trees**.



Sort and incrementally assign!

4k-bit Encoding

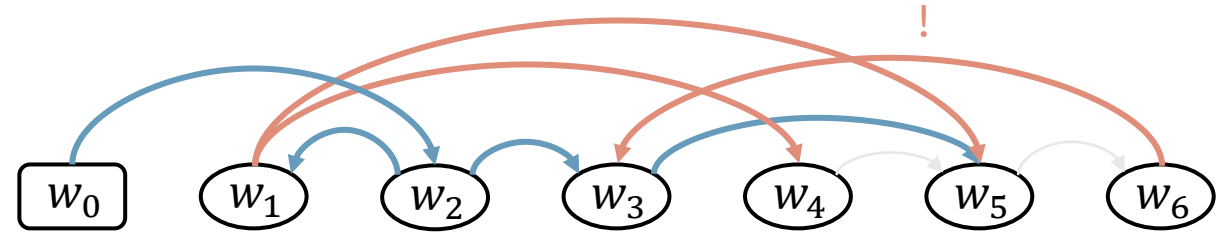
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 1: Distribute the arcs in **relaxed 1-planar trees**.



Sort and incrementally assign!

4k-bit Encoding

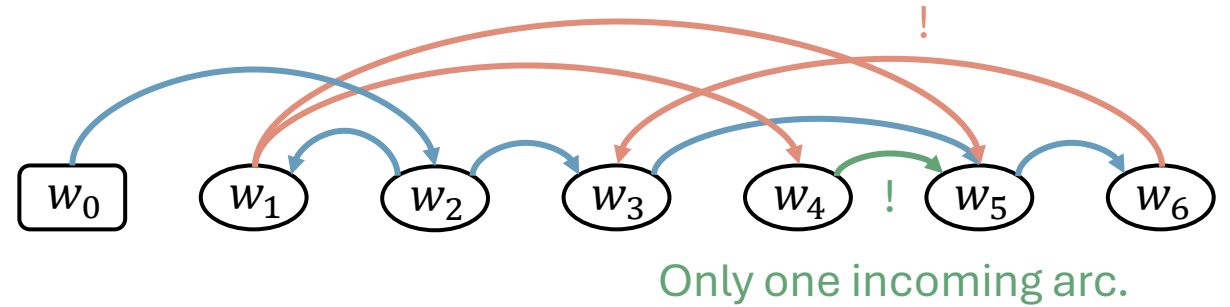
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 1: Distribute the arcs in **relaxed 1-planar trees**.



Sort and incrementally assign!

4k-bit Encoding

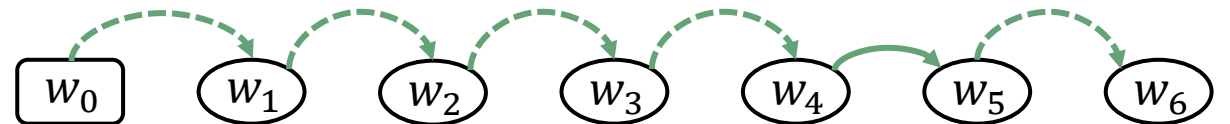
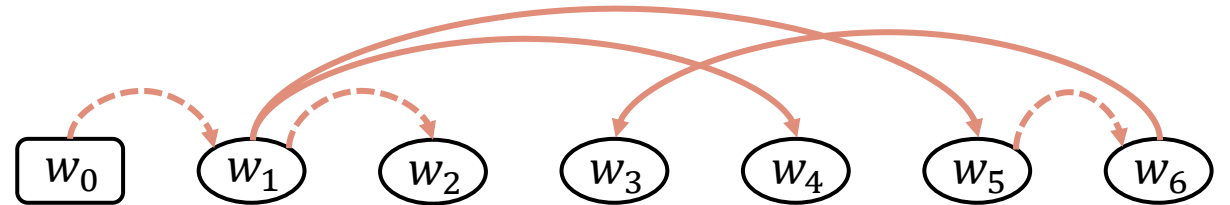
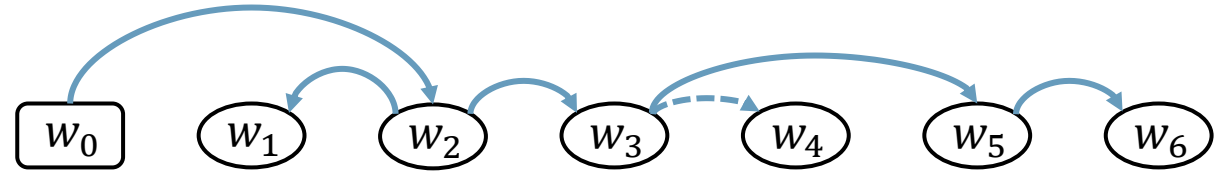
Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.



Step 2: Each plane is a tree. Create artificial heads to those nodes without incoming arcs.



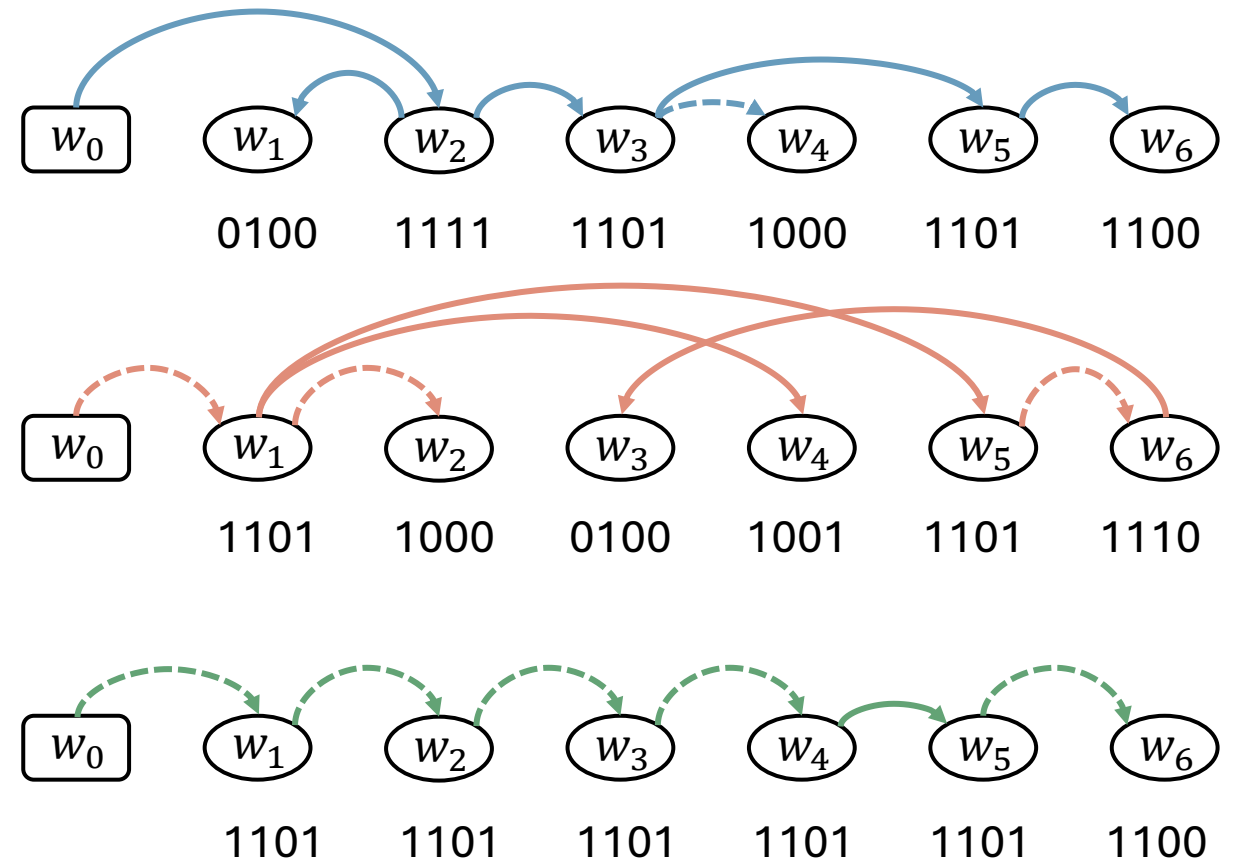
4k-bit Encoding

Assumptions:

- Distribute the arcs in **relaxed 1-planar trees**.
- Encode each label ℓ_i with 4 bits: $b_0b_1b_2b_3$.

b_0	0	w_i has a right head.
	1	w_i has a left head.
b_1		w_i is the outermost dependent.
b_2		w_i has left dependents.
b_3		w_i has right dependents.

Step 3. Assign labels.



4k-bit Encoding



What about decoding?

4k-bit Encoding



- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

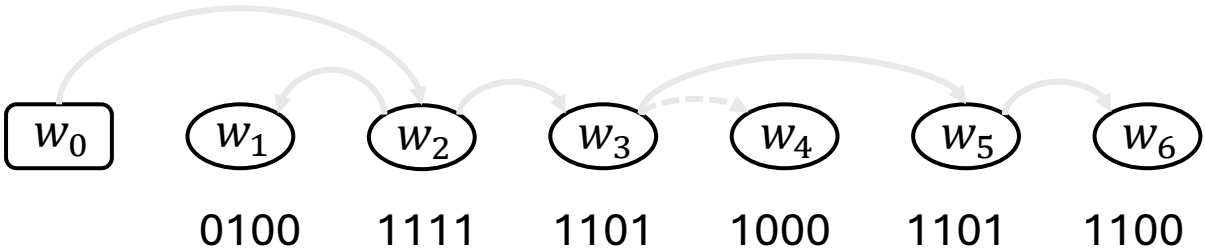
b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve L _p $\leftarrow w_i$ and pop if p .
b_3	1	Add i to R .

4k-bit Encoding

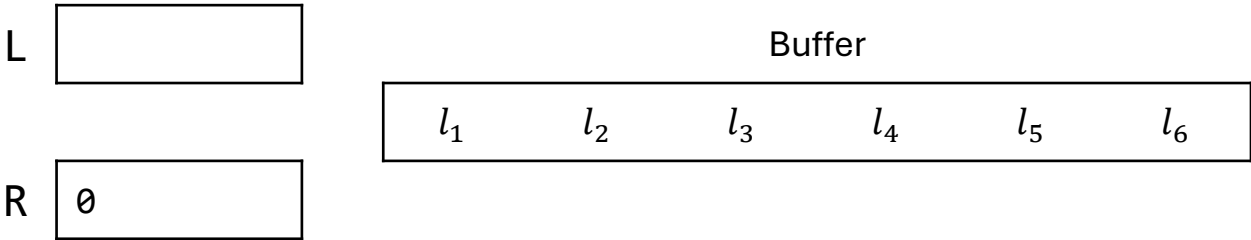


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

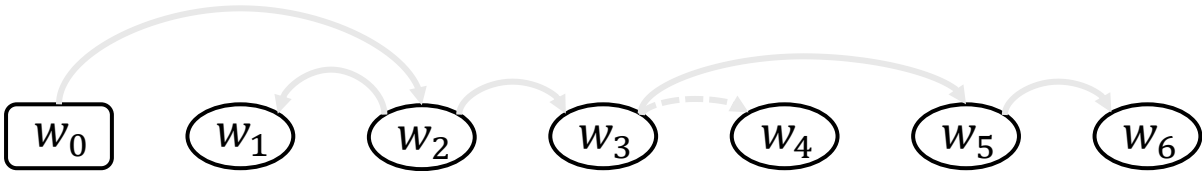


4k-bit Encoding

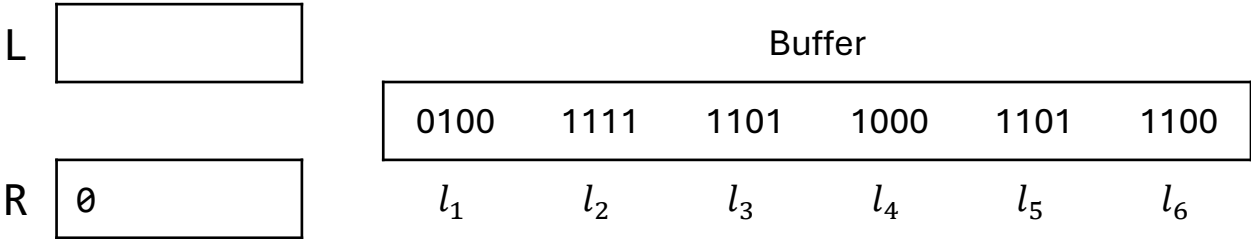


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

$b_0 b_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

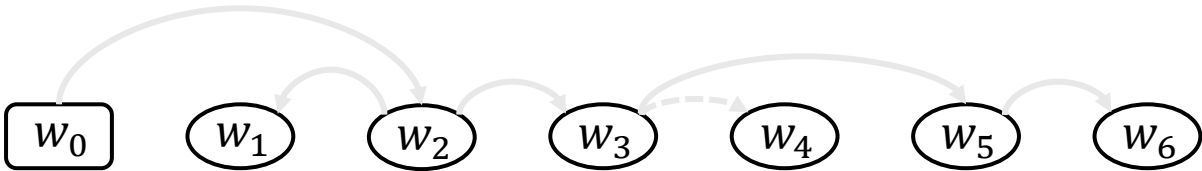


4k-bit Encoding

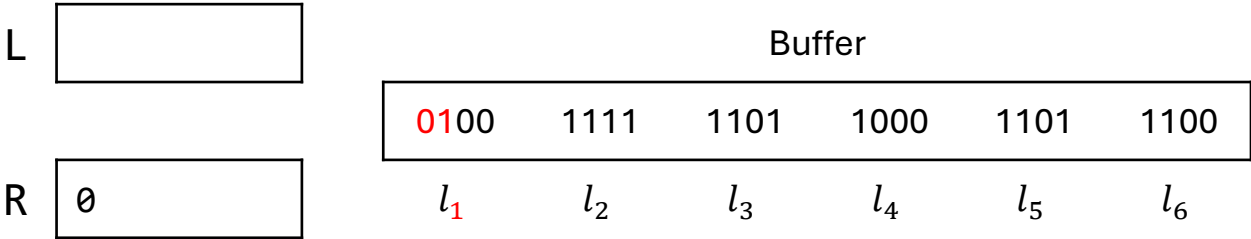


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve $\mathbf{R} \rightarrow w_i$.
	11	Resolve $\mathbf{R} \rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

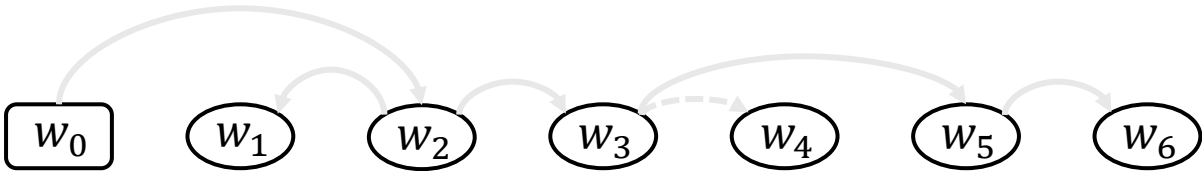


4k-bit Encoding

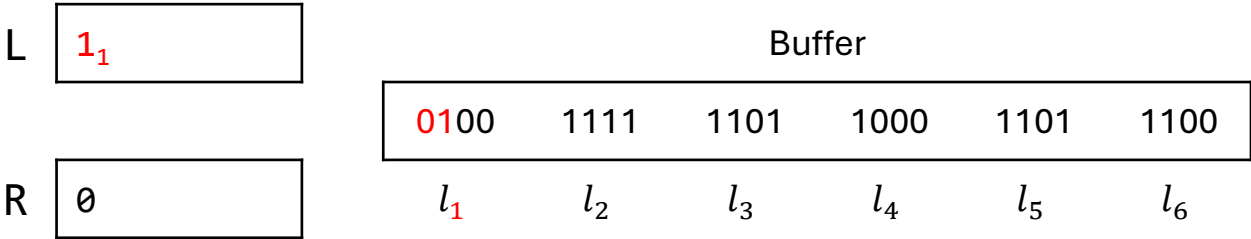


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

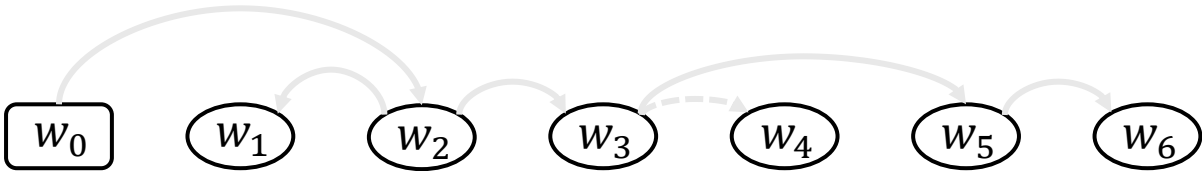


4k-bit Encoding

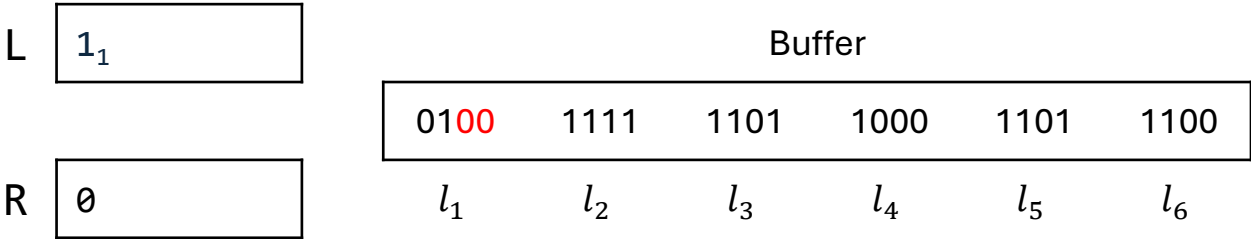


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

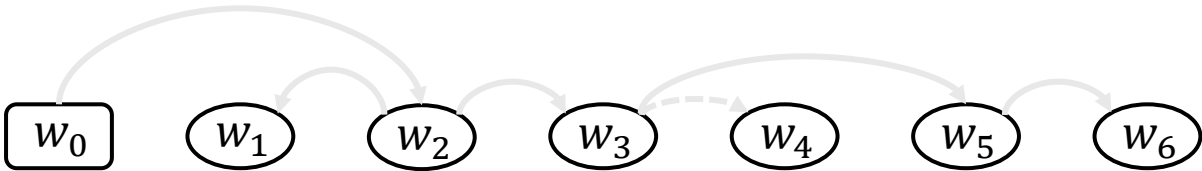


4k-bit Encoding

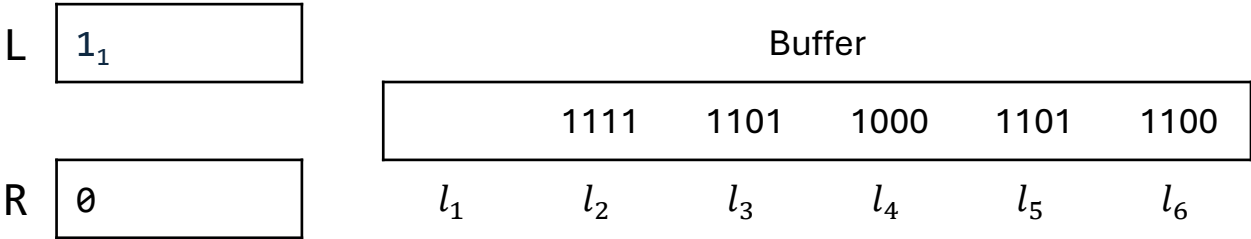


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

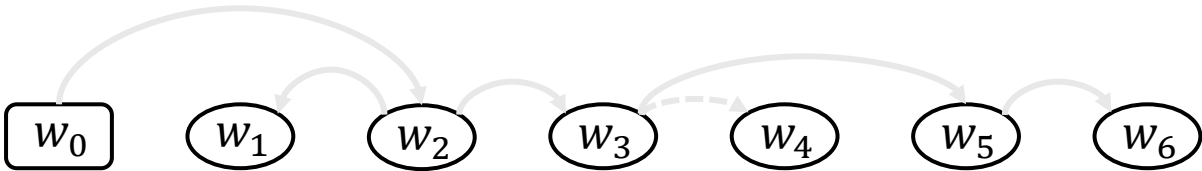


4k-bit Encoding

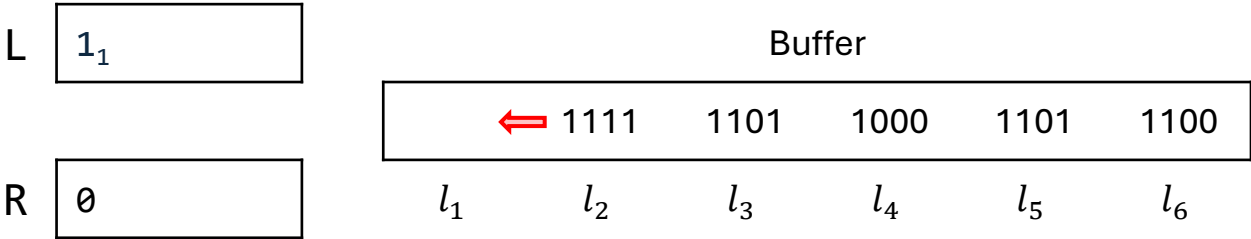


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

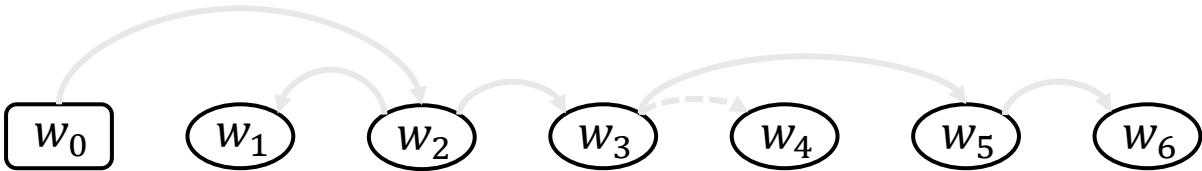


4k-bit Encoding

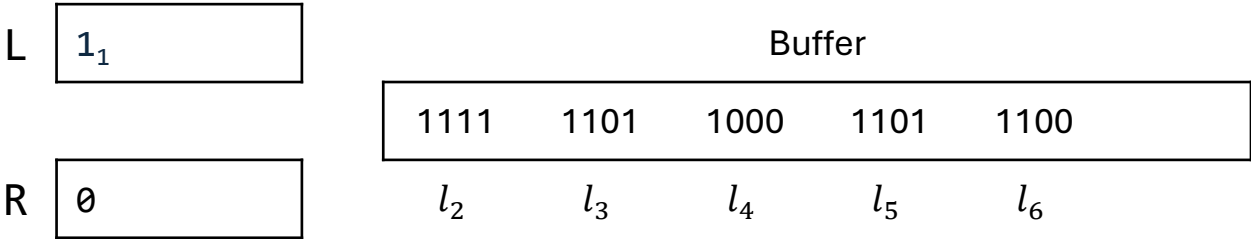


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

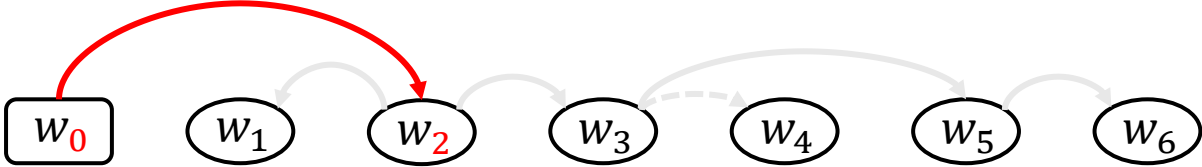


4k-bit Encoding

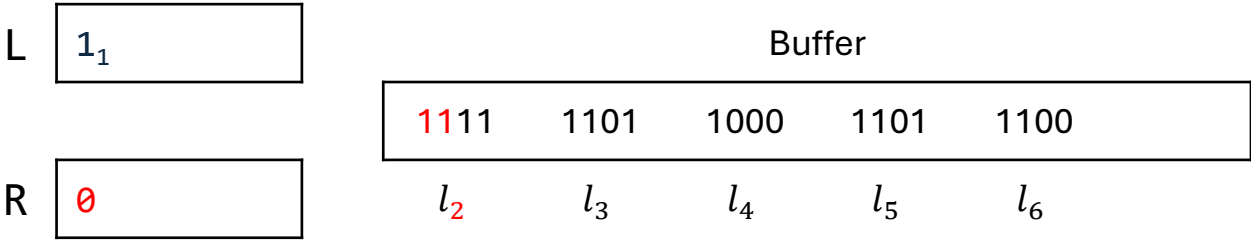


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

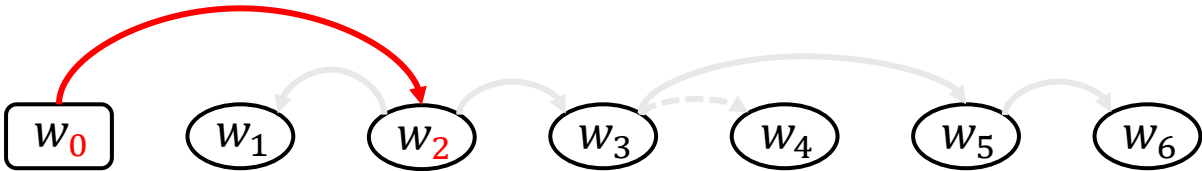


4k-bit Encoding

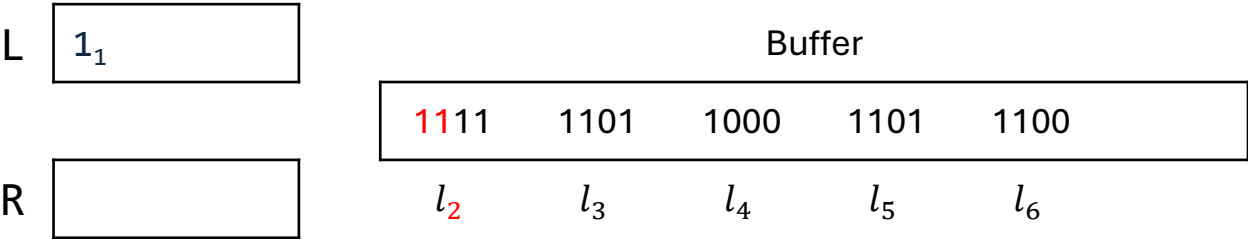


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

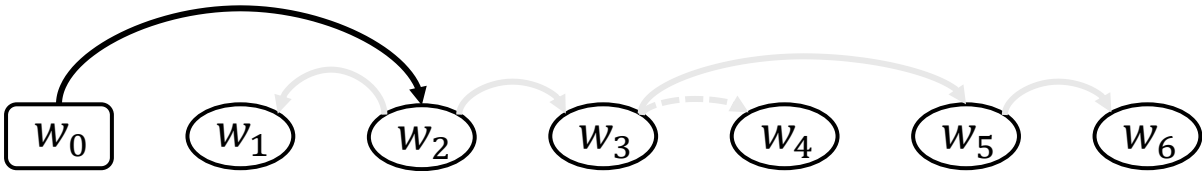


4k-bit Encoding

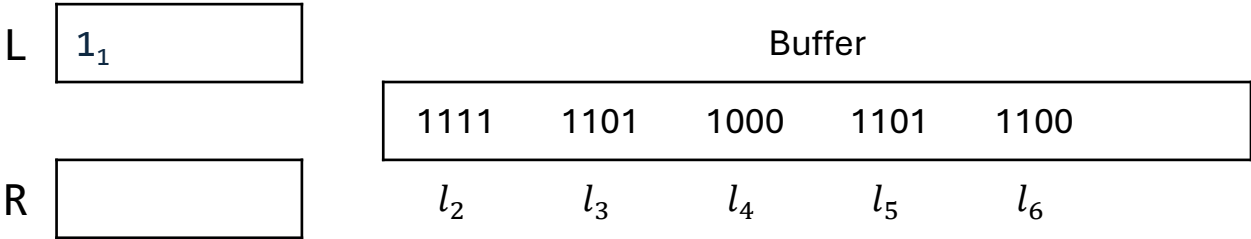


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

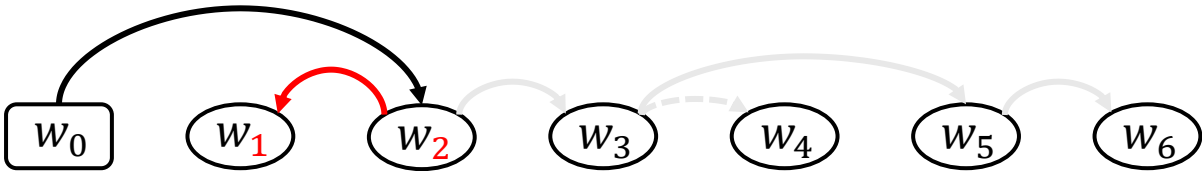


4k-bit Encoding

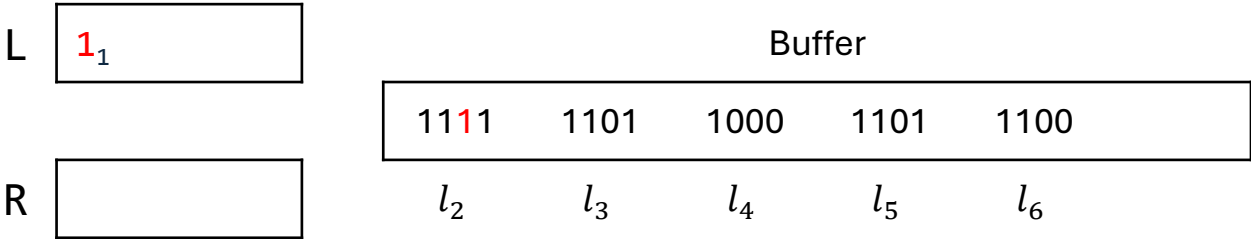


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve L _p $\leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

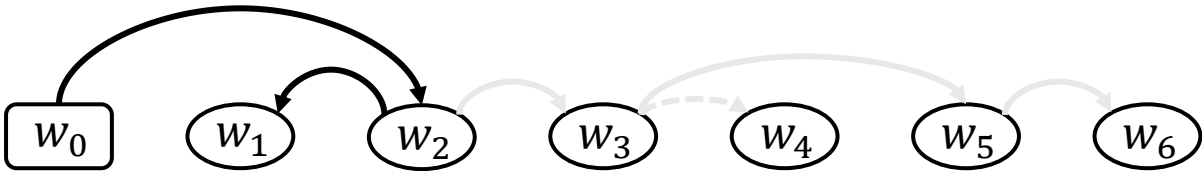


4k-bit Encoding

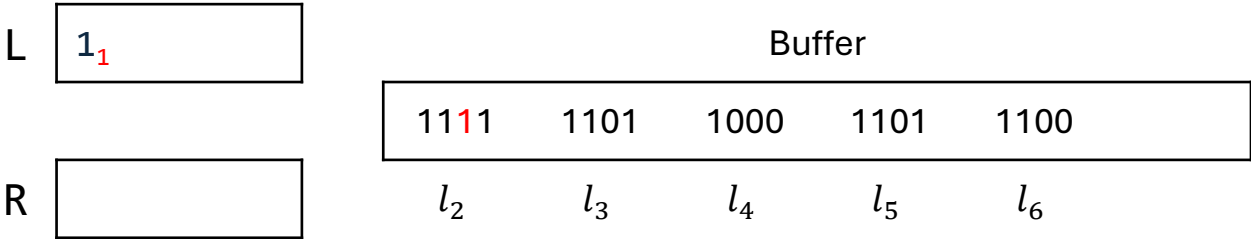


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

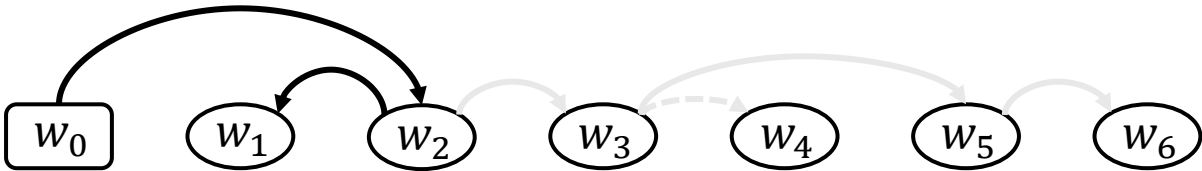


4k-bit Encoding

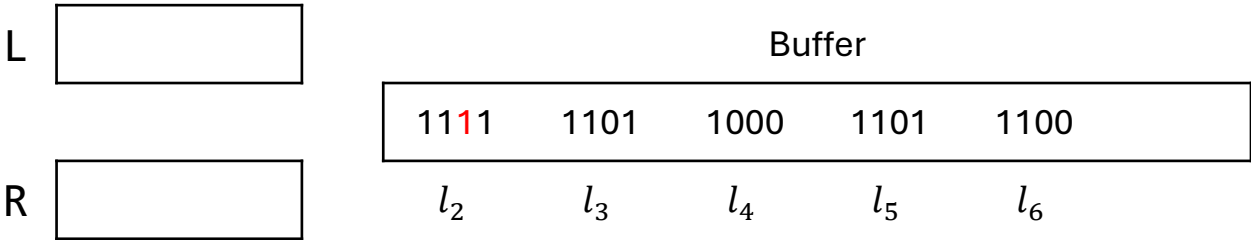


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

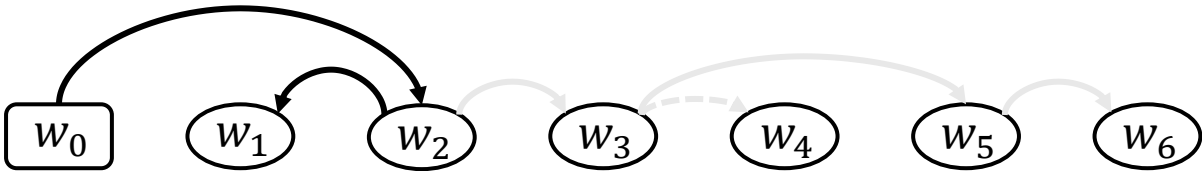


4k-bit Encoding

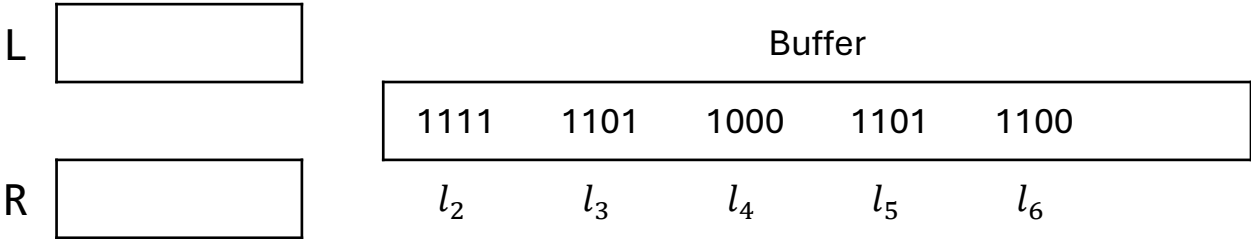


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

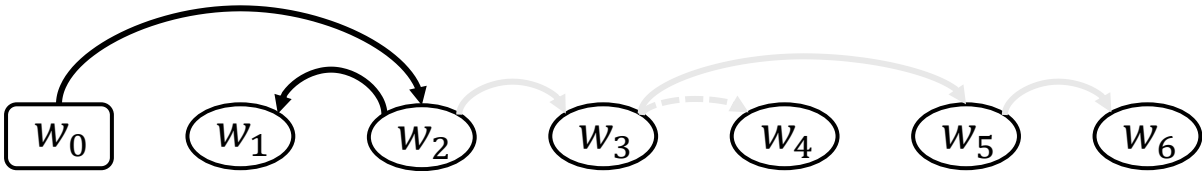


4k-bit Encoding

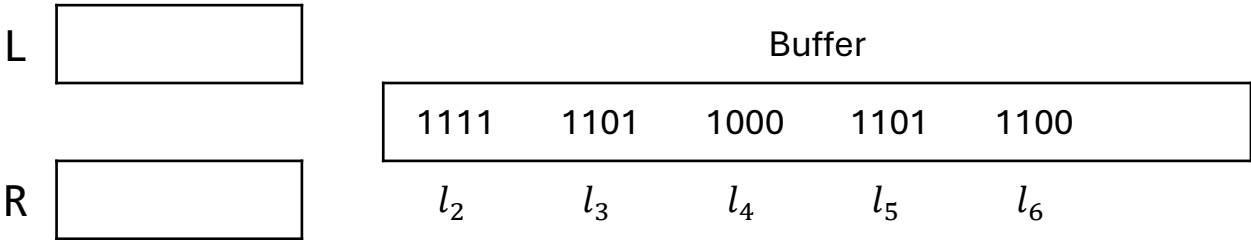


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

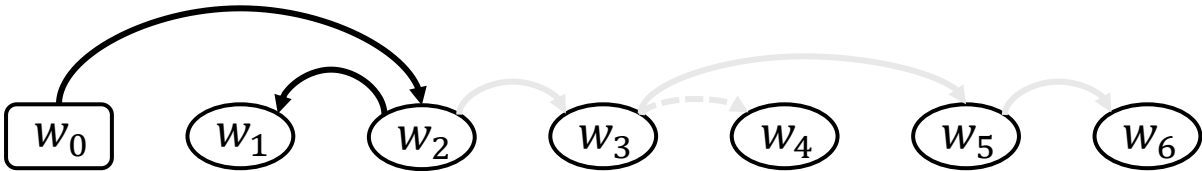


4k-bit Encoding

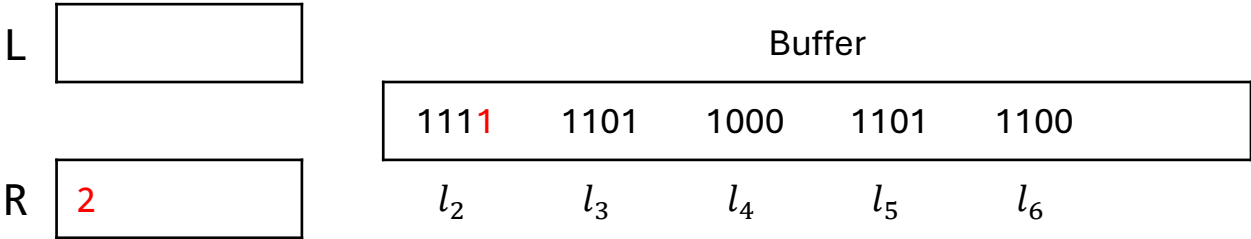


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

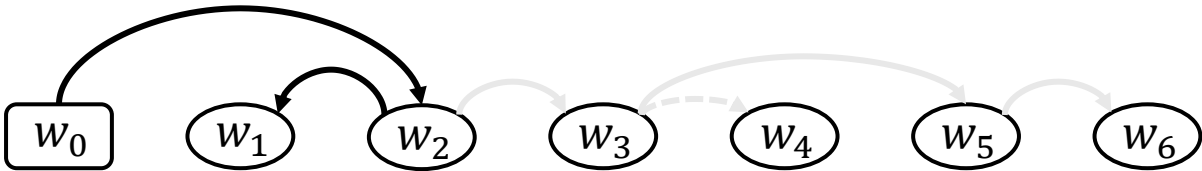


4k-bit Encoding

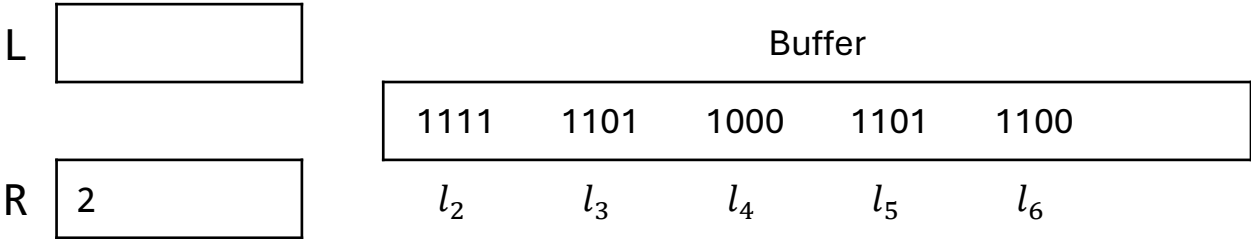


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

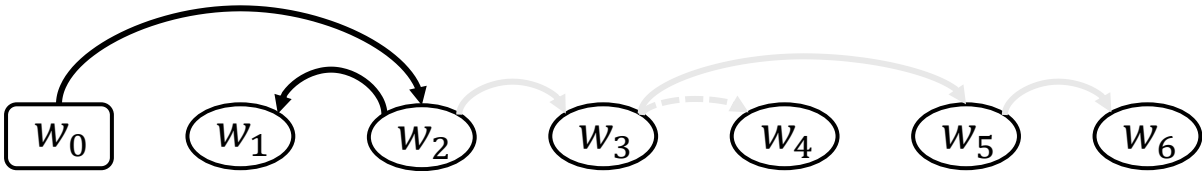


4k-bit Encoding

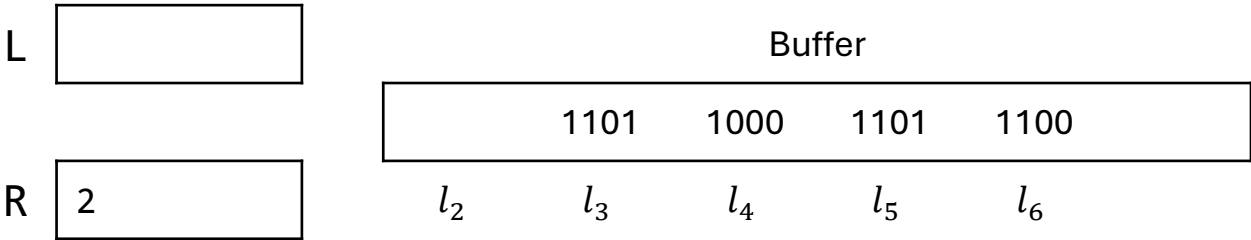


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

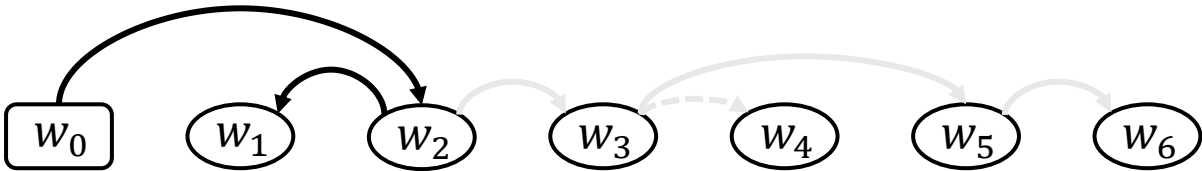


4k-bit Encoding

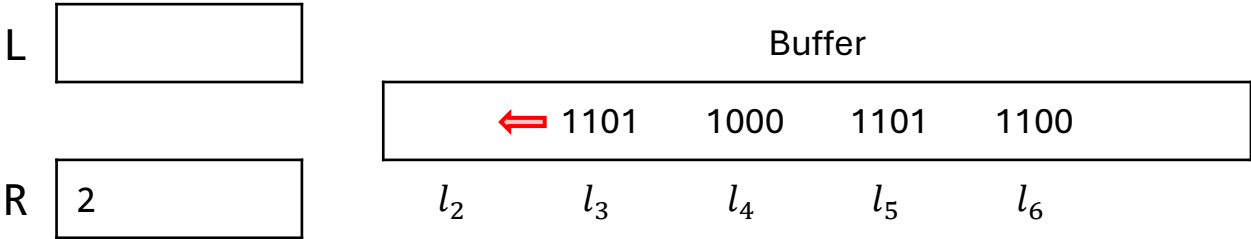


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

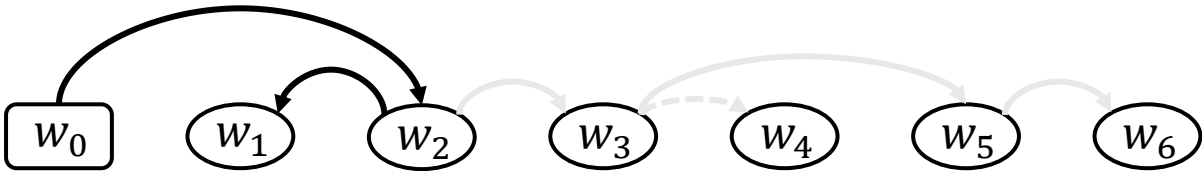


4k-bit Encoding

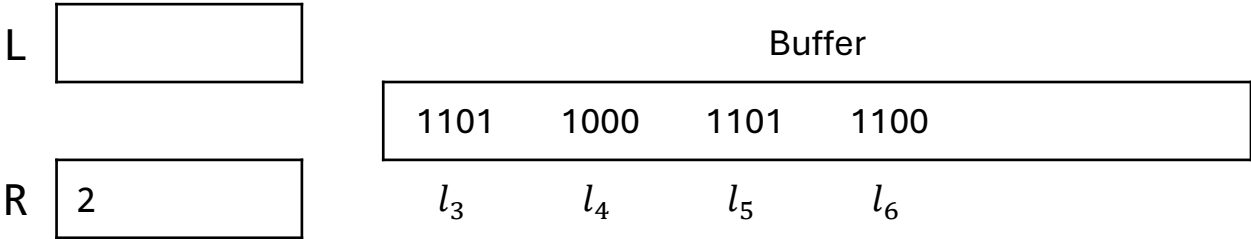


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

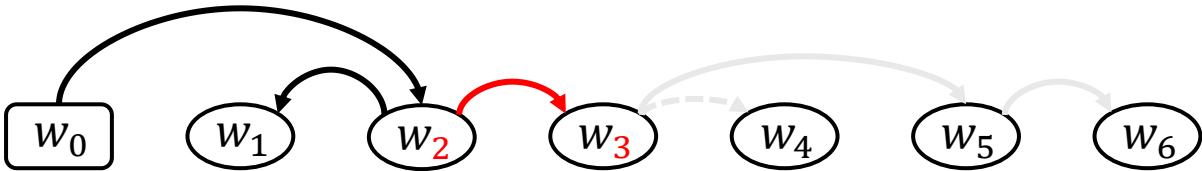


4k-bit Encoding

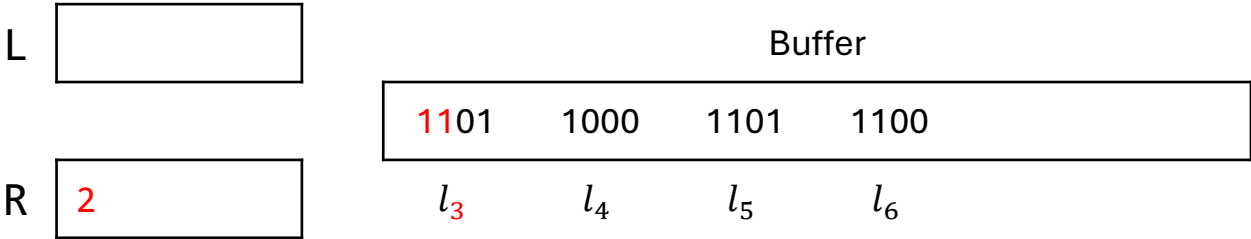


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

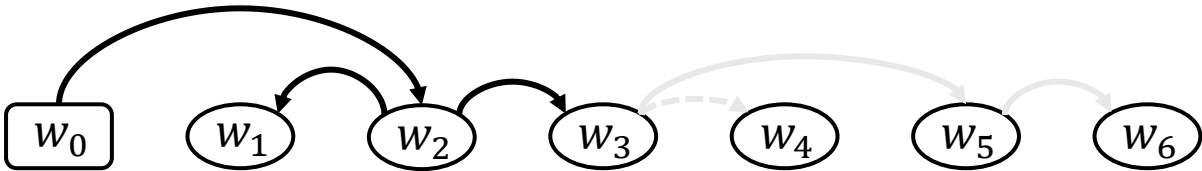


4k-bit Encoding

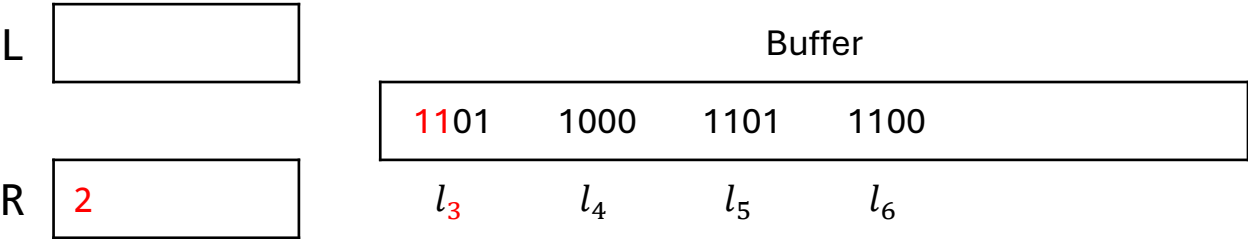


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

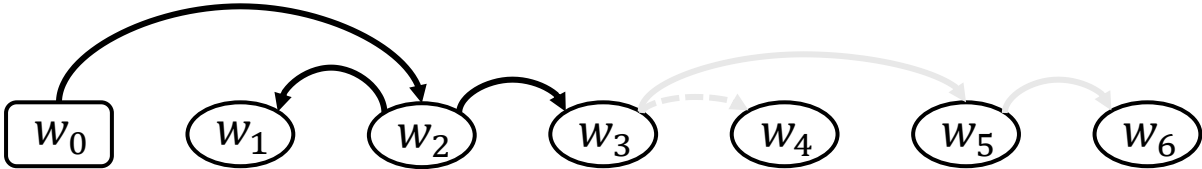


4k-bit Encoding

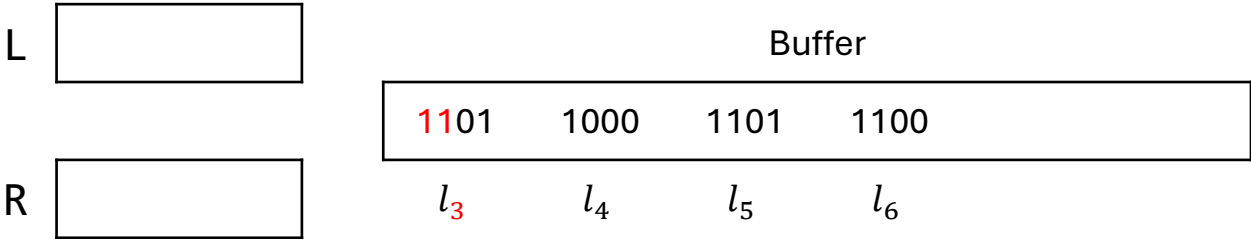


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve L _p $\leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

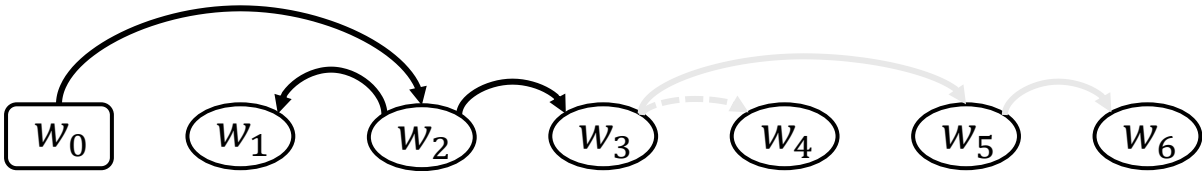


4k-bit Encoding

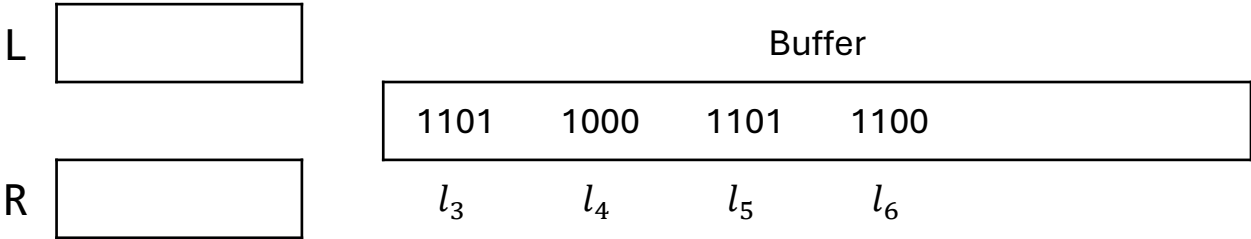


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

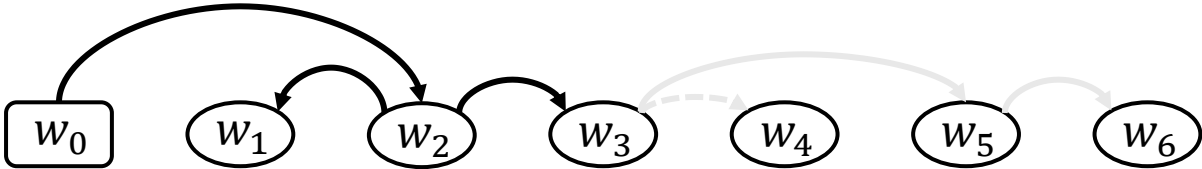


4k-bit Encoding

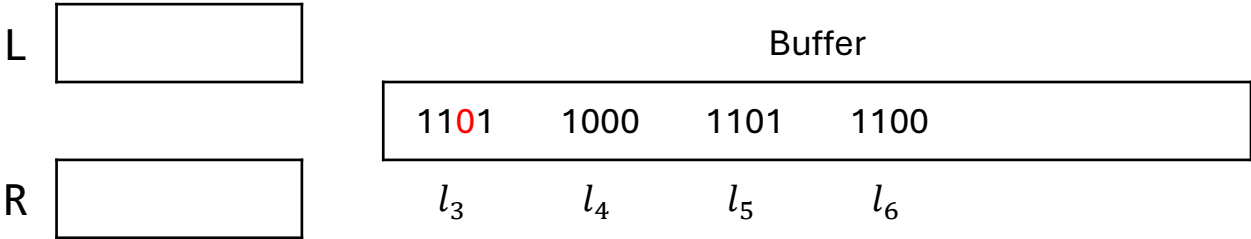


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

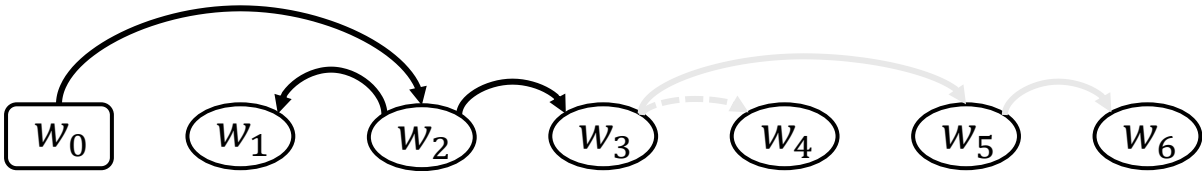


4k-bit Encoding

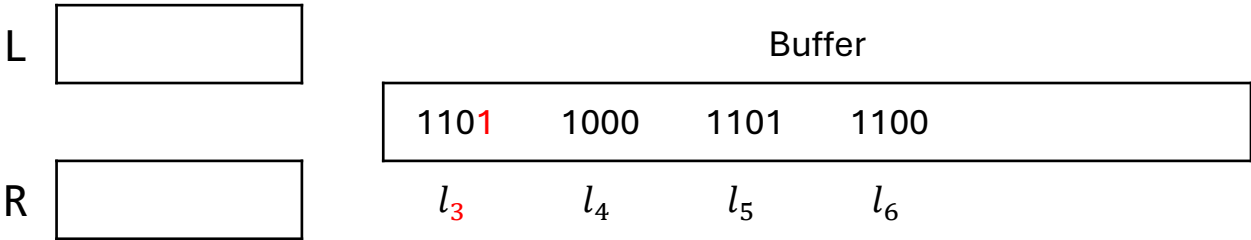


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

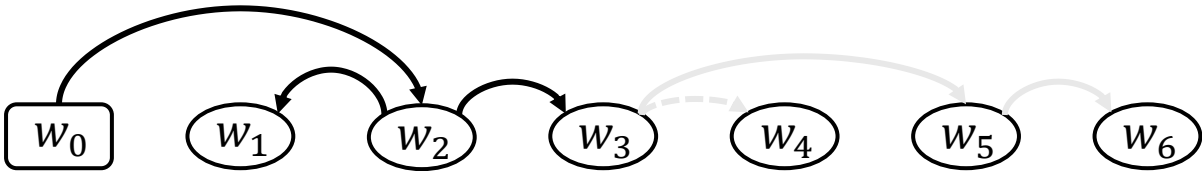


4k-bit Encoding

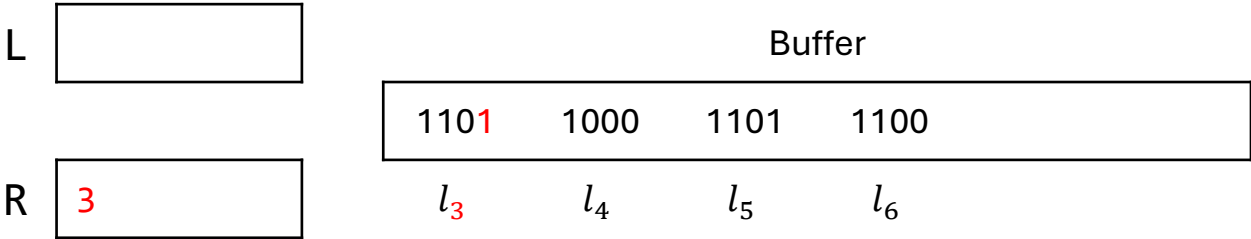


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

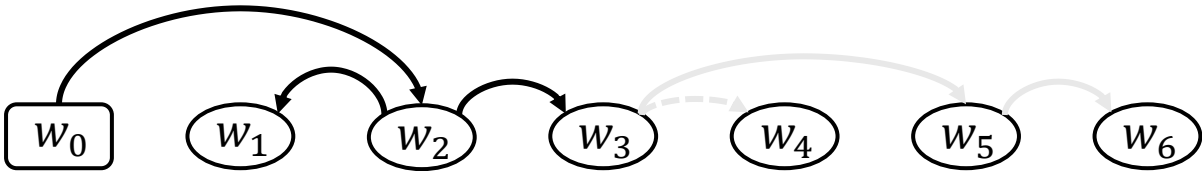


4k-bit Encoding

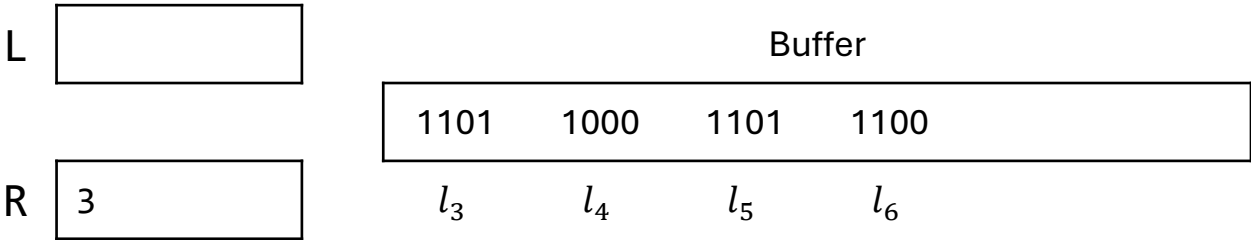


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

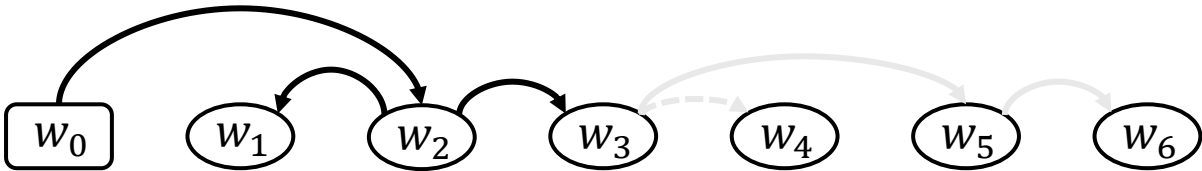


4k-bit Encoding

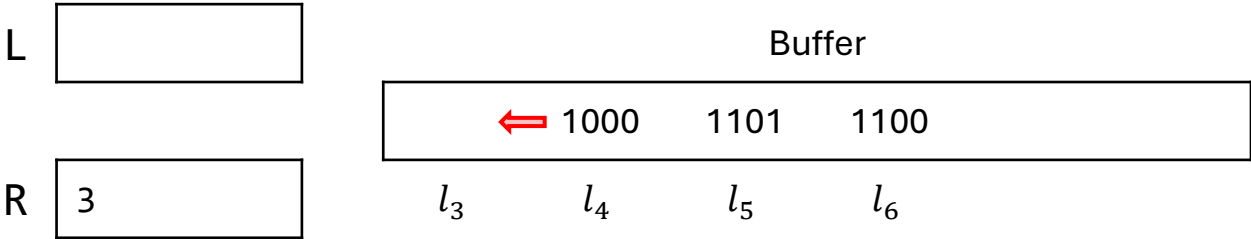


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

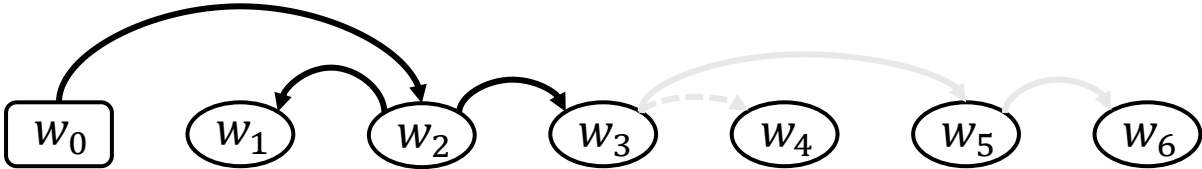


4k-bit Encoding

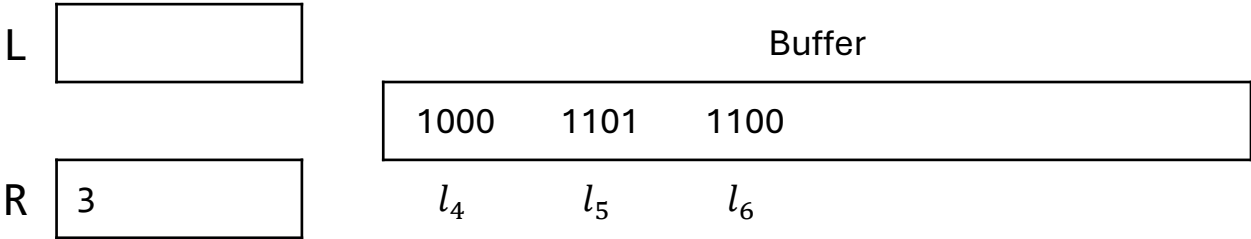


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

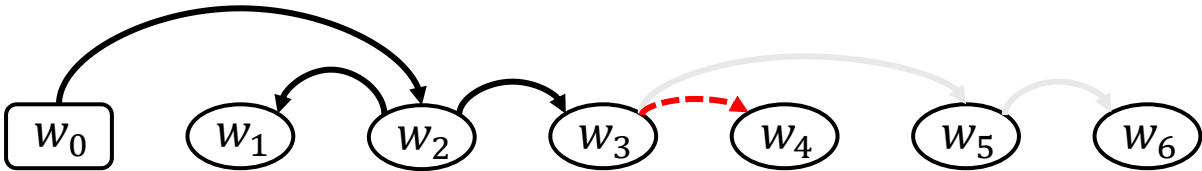


4k-bit Encoding

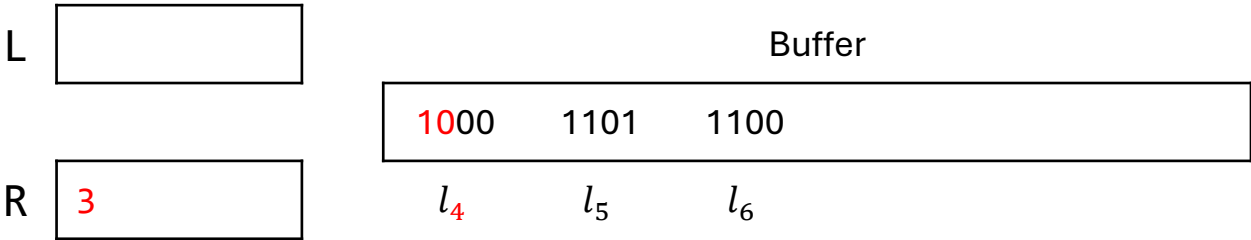


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

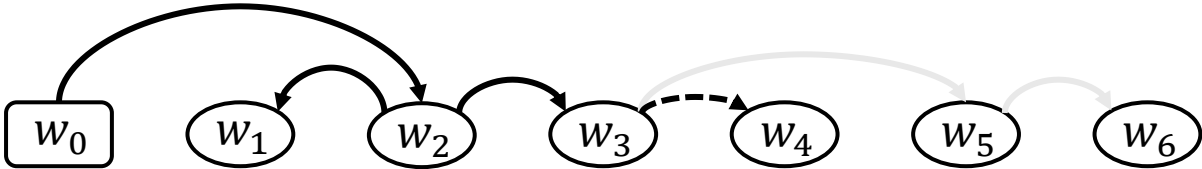


4k-bit Encoding

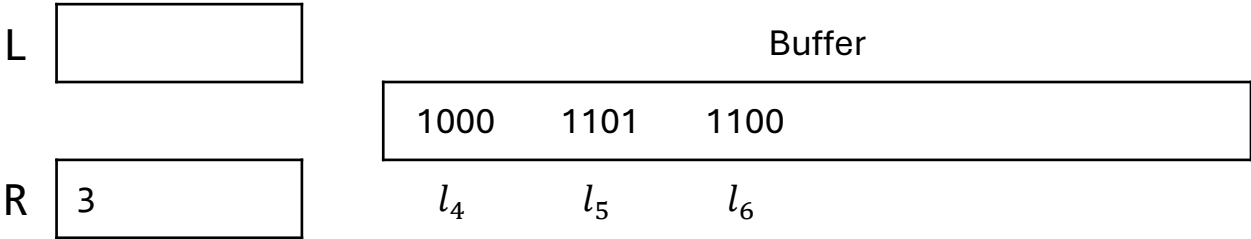


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

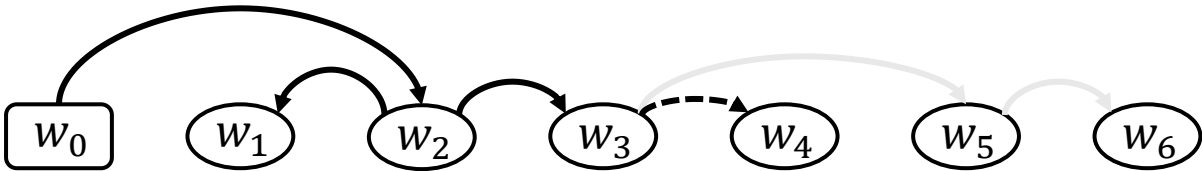


4*k*-bit Encoding

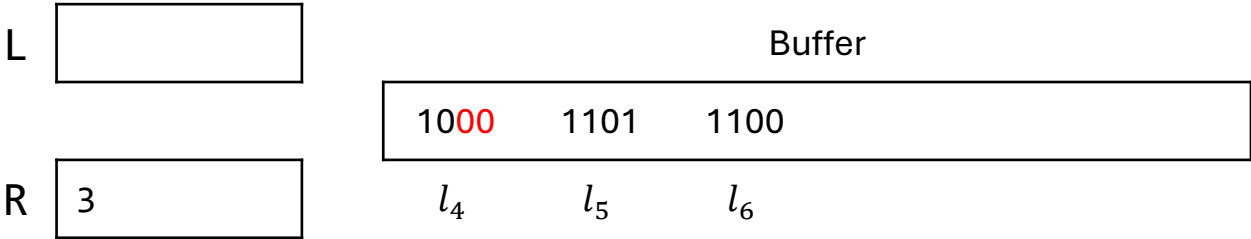


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

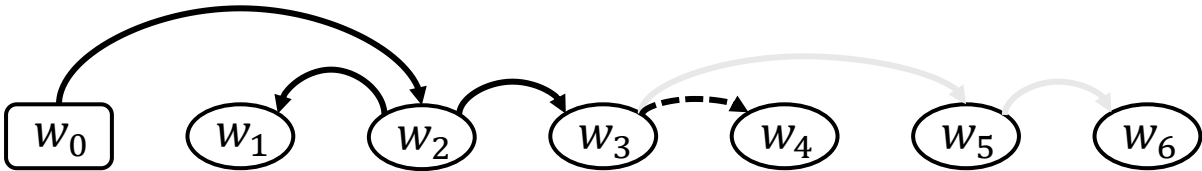


4k-bit Encoding

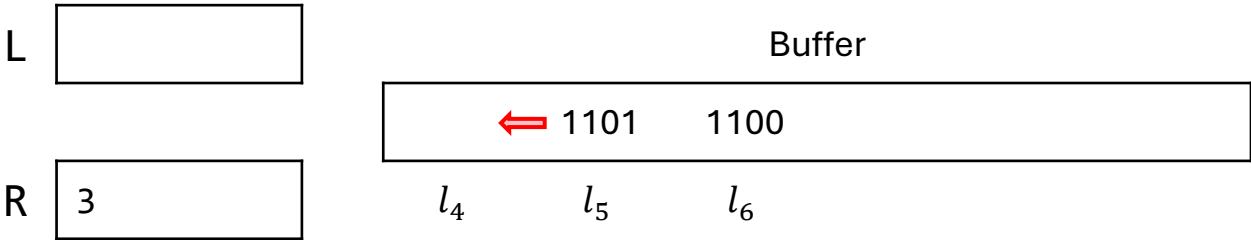


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

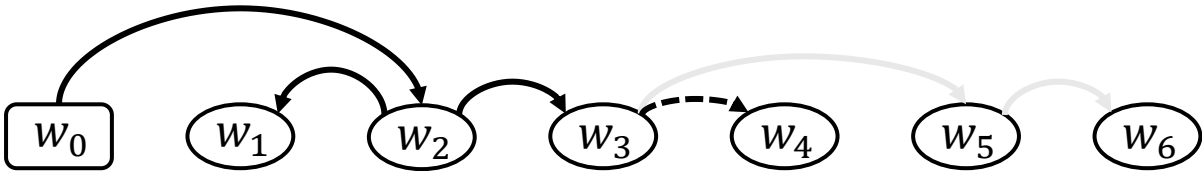


4k-bit Encoding

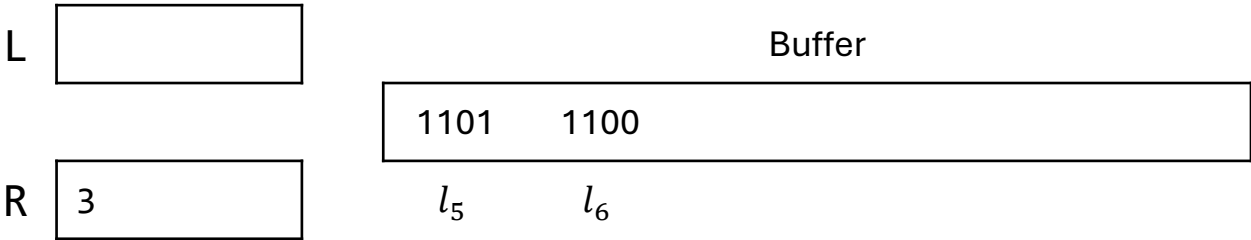


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

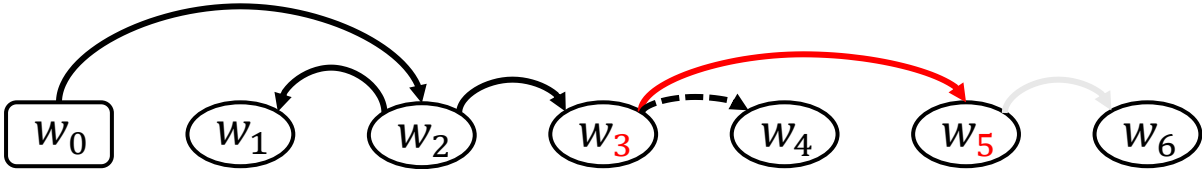


4k-bit Encoding

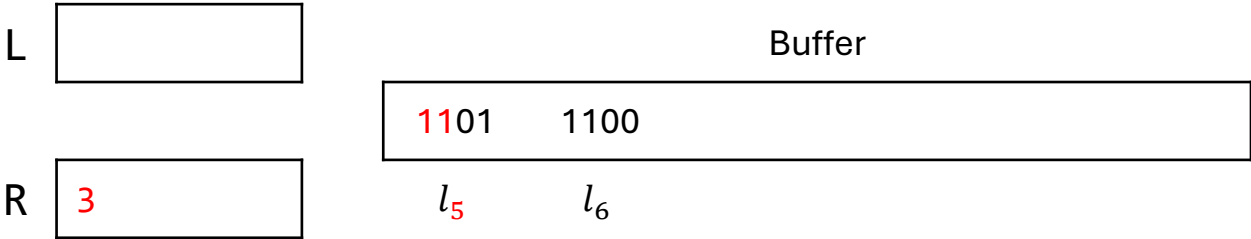


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

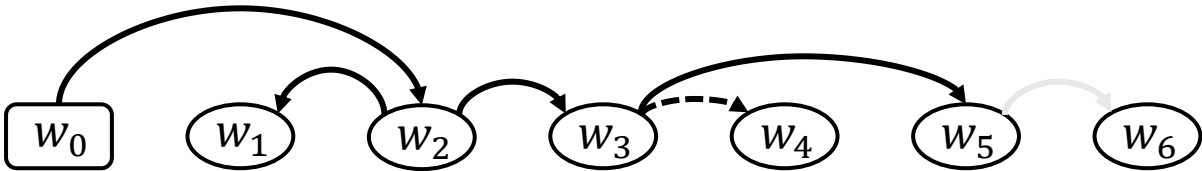


4k-bit Encoding

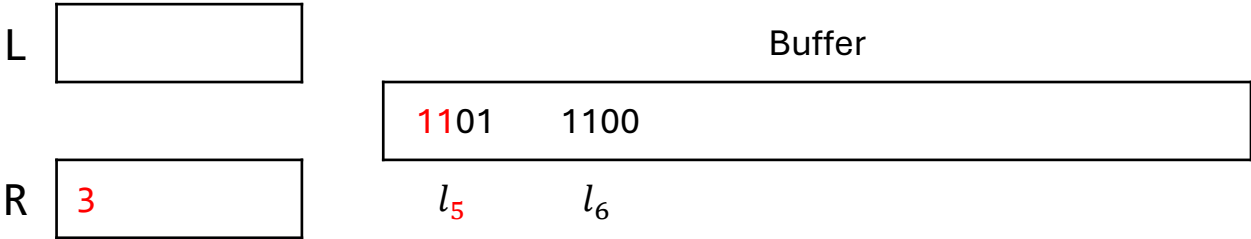


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b_2	1	Resolve L _p $\leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

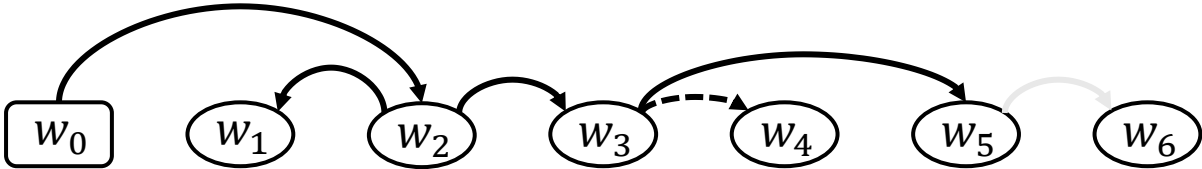


4k-bit Encoding

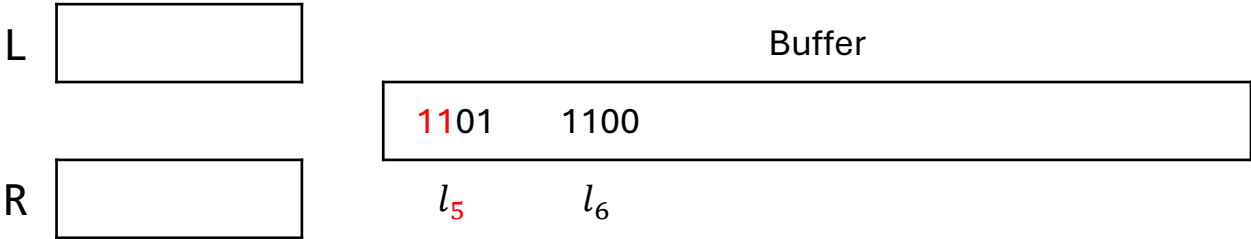


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
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Initial state: Labels to buffer and **R** with the root dependency.

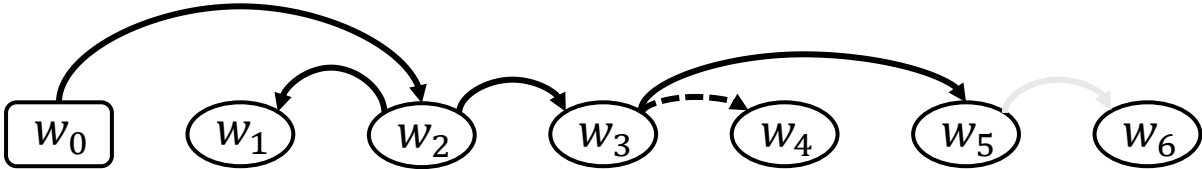


4k-bit Encoding

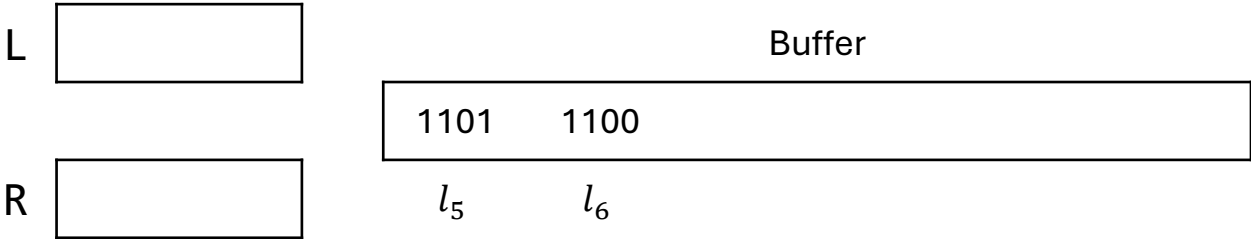


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
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b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

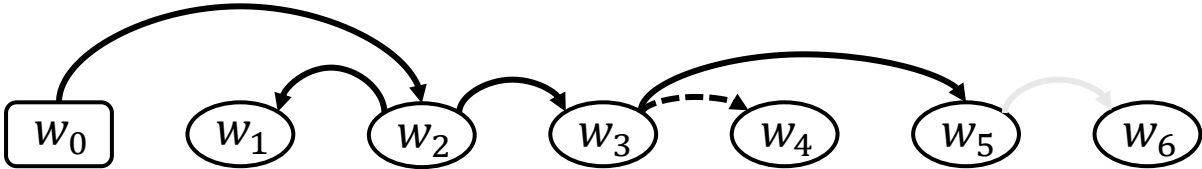


4k-bit Encoding

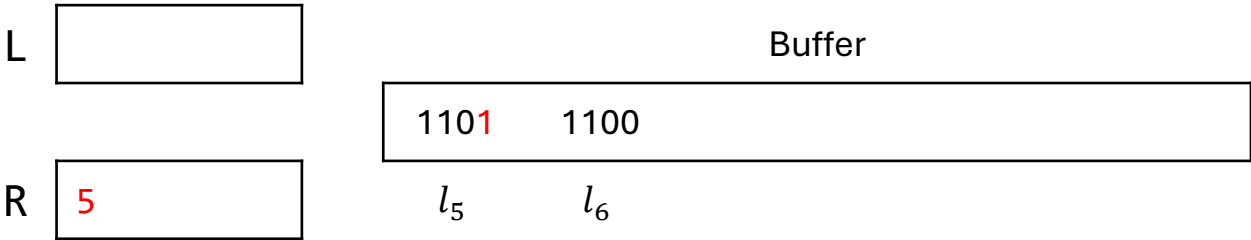


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
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	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

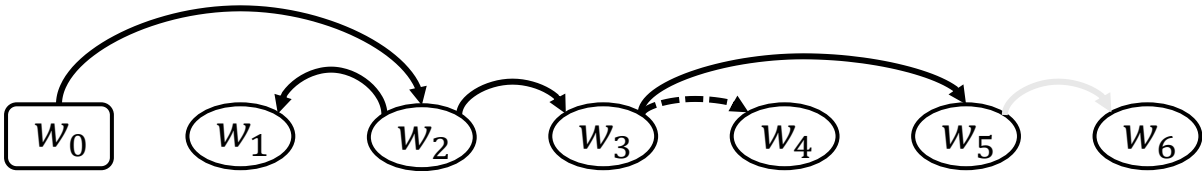


4k-bit Encoding

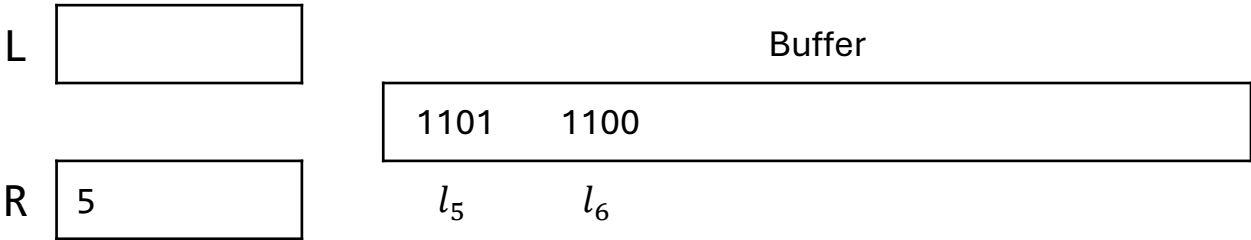


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
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	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
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Initial state: Labels to buffer and **R** with the root dependency.

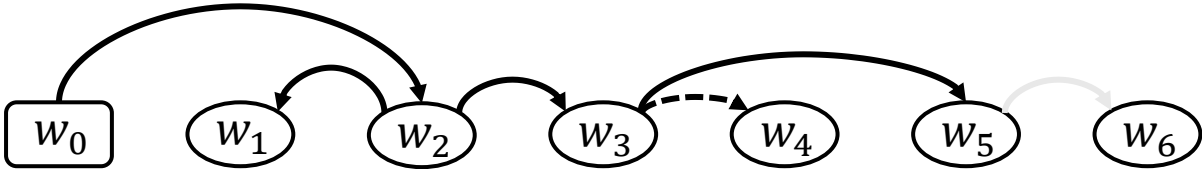


4k-bit Encoding

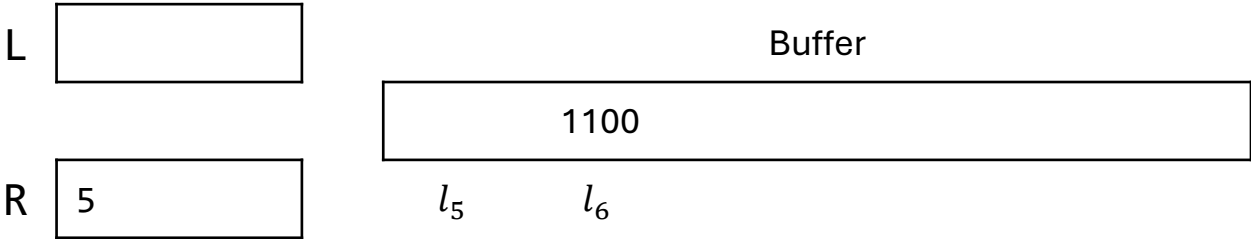


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Initial state: Labels to buffer and **R** with the root dependency.

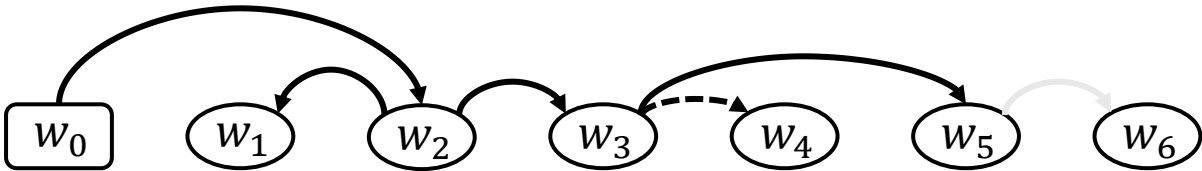


4k-bit Encoding

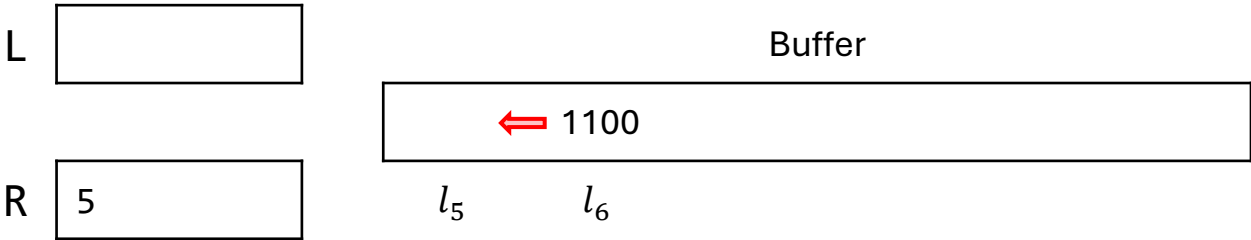


- Same as bracketing-encoding.
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$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
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	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

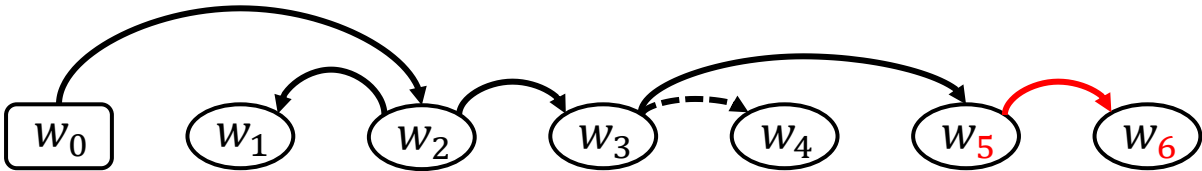


4k-bit Encoding



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- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and b_1 .

b_0b_1	00	Add i_0 to L .
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Initial state: Labels to buffer and **R** with the root dependency.

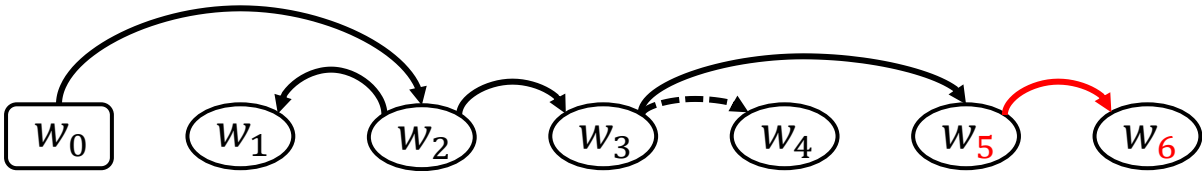


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Initial state: Labels to buffer and **R** with the root dependency.

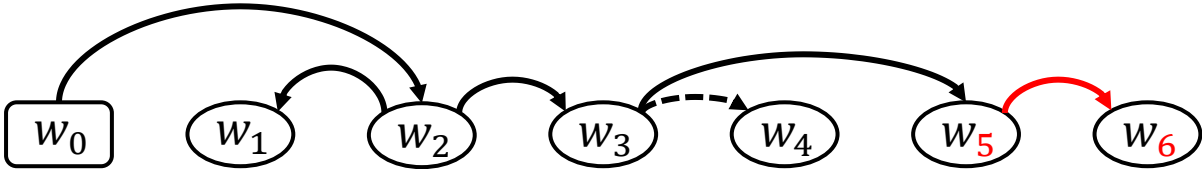


4k-bit Encoding



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- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
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b_2	1	Resolve $L_p \leftarrow w_i$ and pop if p .
b_3	1	Add i to R .



Initial state: Labels to buffer and **R** with the root dependency.

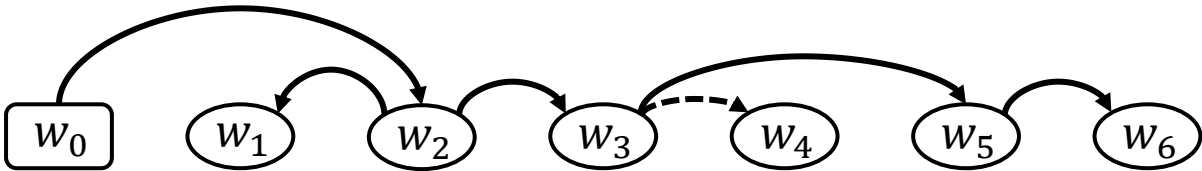


4k-bit Encoding

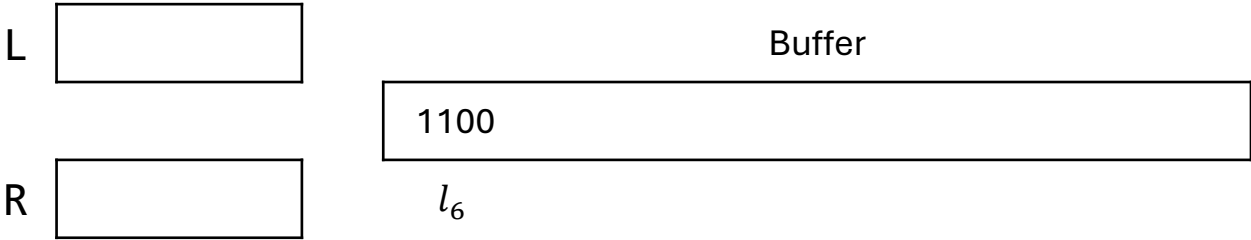


- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

$\mathbf{b}_0\mathbf{b}_1$	00	Add i_0 to L .
	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
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Initial state: Labels to buffer and **R** with the root dependency.

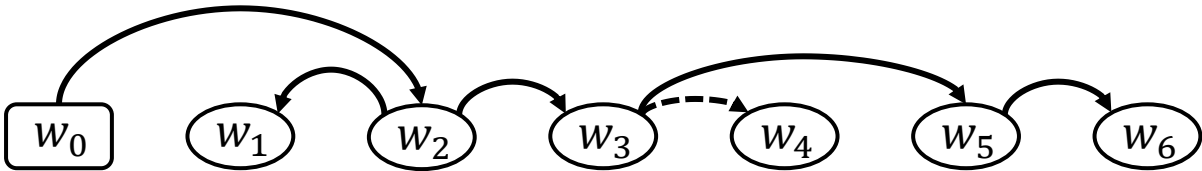


4k-bit Encoding



- Same as bracketing-encoding.
- **L** for left arcs ($<\backslash$) and **R** right arcs ($/>$).
- Store i and \mathbf{b}_1 .

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	01	Add i_1 to L .
	10	Resolve R $\rightarrow w_i$.
	11	Resolve R $\rightarrow w_i$ and pop R .
\mathbf{b}_2	1	Resolve $\mathbf{L}_p \leftarrow w_i$ and pop if p .
\mathbf{b}_3	1	Add i to R .



Finished!

L

R

Buffer

 l_6

6k-bit Encoding

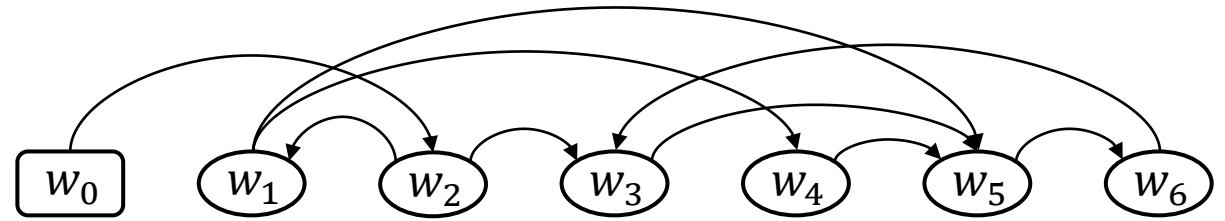


Assumptions:

- Distribute the arcs in **relaxed 1-planar graphs**.
- For each node limit one head per direction.
- Encode each label ℓ_i with 6 bits: $b_0b_1b_2b_3b_4b_5$.

b_0	w_i has a left head.
b_1	w_i is the outermost right dependent.
b_2	w_i has right dependents.
b_3	w_i has a right head.
b_4	w_i is the outermost left dependent.
b_5	w_i has left dependents.

Step 1: Distribute the arcs in **relaxed 1-planar graphs**.



6k-bit Encoding

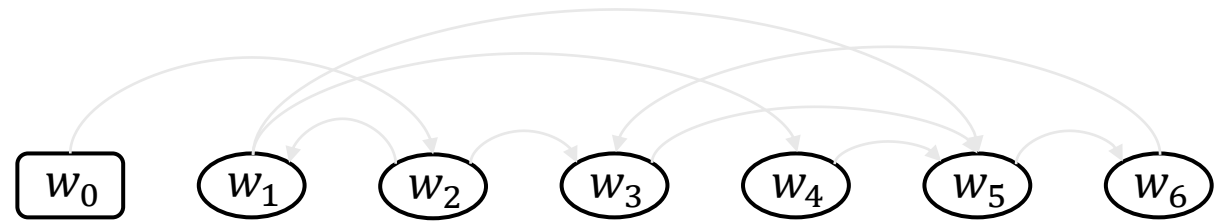


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- Encode each label ℓ_i with 6 bits: $b_0b_1b_2b_3b_4b_5$.

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b_4	w_i is the outermost left dependent.
b_5	w_i has left dependents.

Step 1: Distribute the arcs in **relaxed 1-planar graphs**.



6k-bit Encoding

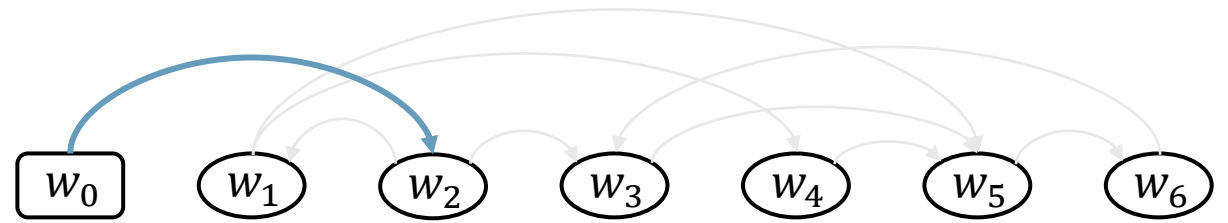


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Step 1: Distribute the arcs in **relaxed 1-planar graphs**.



6k-bit Encoding

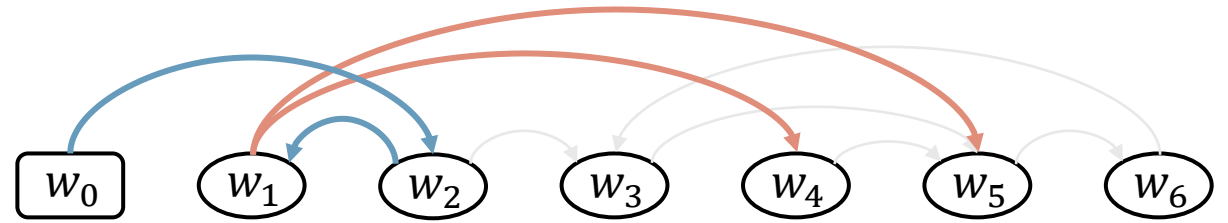


Assumptions:

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Step 1: Distribute the arcs in **relaxed 1-planar graphs**.



6k-bit Encoding



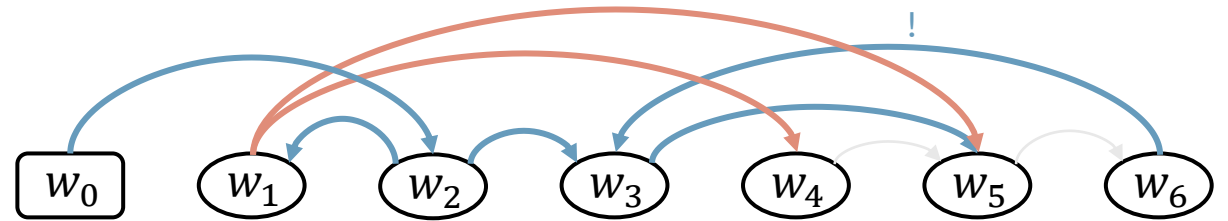
Assumptions:

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Step 1: Distribute the arcs in **relaxed 1-planar graphs**.

Allow one head per direction



6k-bit Encoding



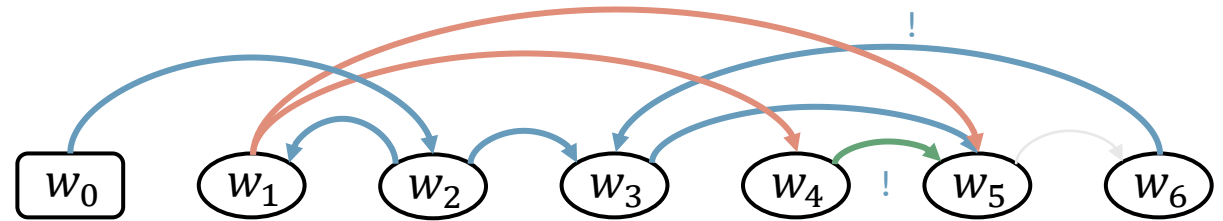
Assumptions:

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Step 1: Distribute the arcs in **relaxed 1-planar graphs**.

Allow one head per direction



6k-bit Encoding

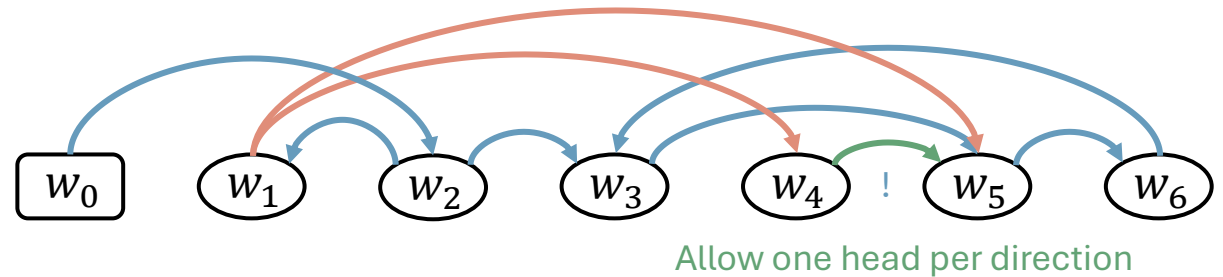


Assumptions:

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Step 1: Distribute the arcs in **relaxed 1-planar graphs**.



6k-bit Encoding

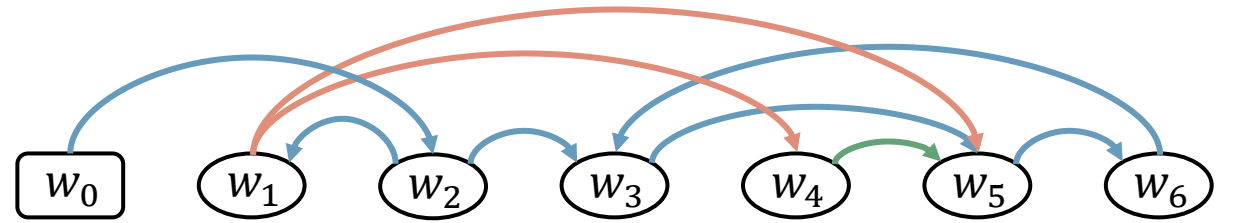


Assumptions:

- Distribute the arcs in **relaxed 1-planar graphs**.
- For each node limit one head per direction.
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b_3	w_i has a right head.
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b_5	w_i has left dependents.

Step 2: Assign labels.



P1	000110	001001	111110	000000	111000	110001
P2	001000	000000	000000	100000	110000	000000
P3	000000	000000	000000	001000	110000	000000

6k-bit Encoding

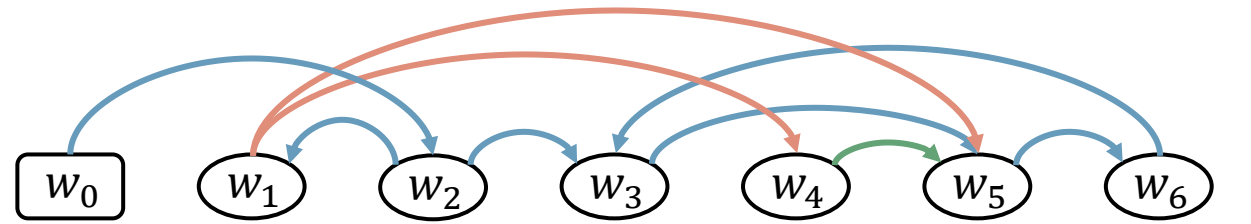
Assumptions:

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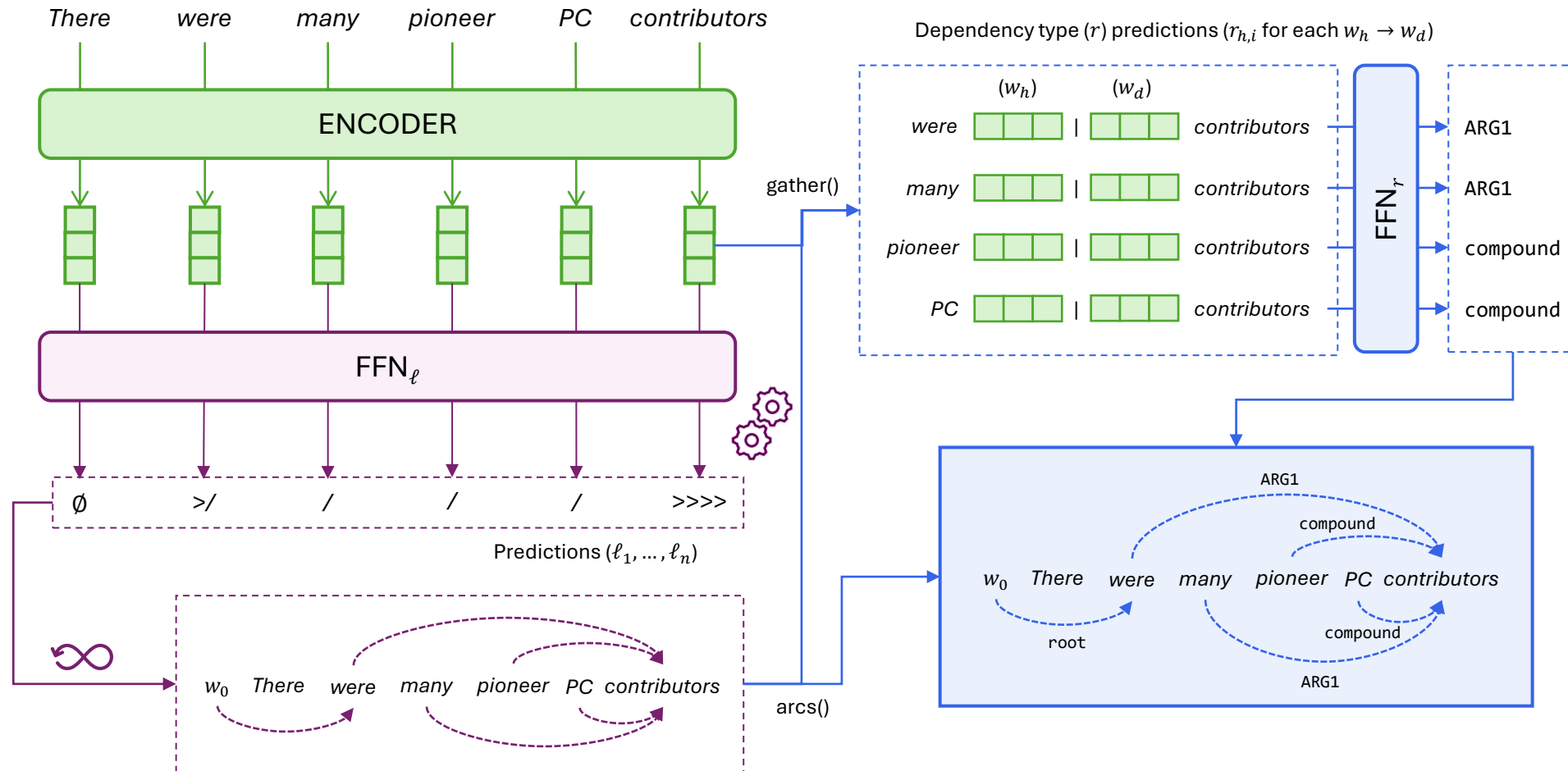
Step 2: Assign labels.



P1	000110	001001	111110	000000	111000	110001
P2	001000	000000	000000	100000	110000	000000
P3	000000	000000	000000	001000	110000	000000

∞ Same as 4k-bit encoding!

Encoder-Decoder Architecture



Experiments

Encoders:

- 4-layered BiLSTM.
- XLM-RoBERTa ([Conneau et al., 2020](#)).
- XLNet ([Yang et al., 2019](#)).

Decoders:

- Absolute (A) and relative (R).
- Bracketing (B) with $k \in \{2,3\}$.
- $4k$ -bit (B4) with $k \in \{2,3,4\}$.
- $6k$ -bit (B6) with $k \in \{2,3,4\}$.

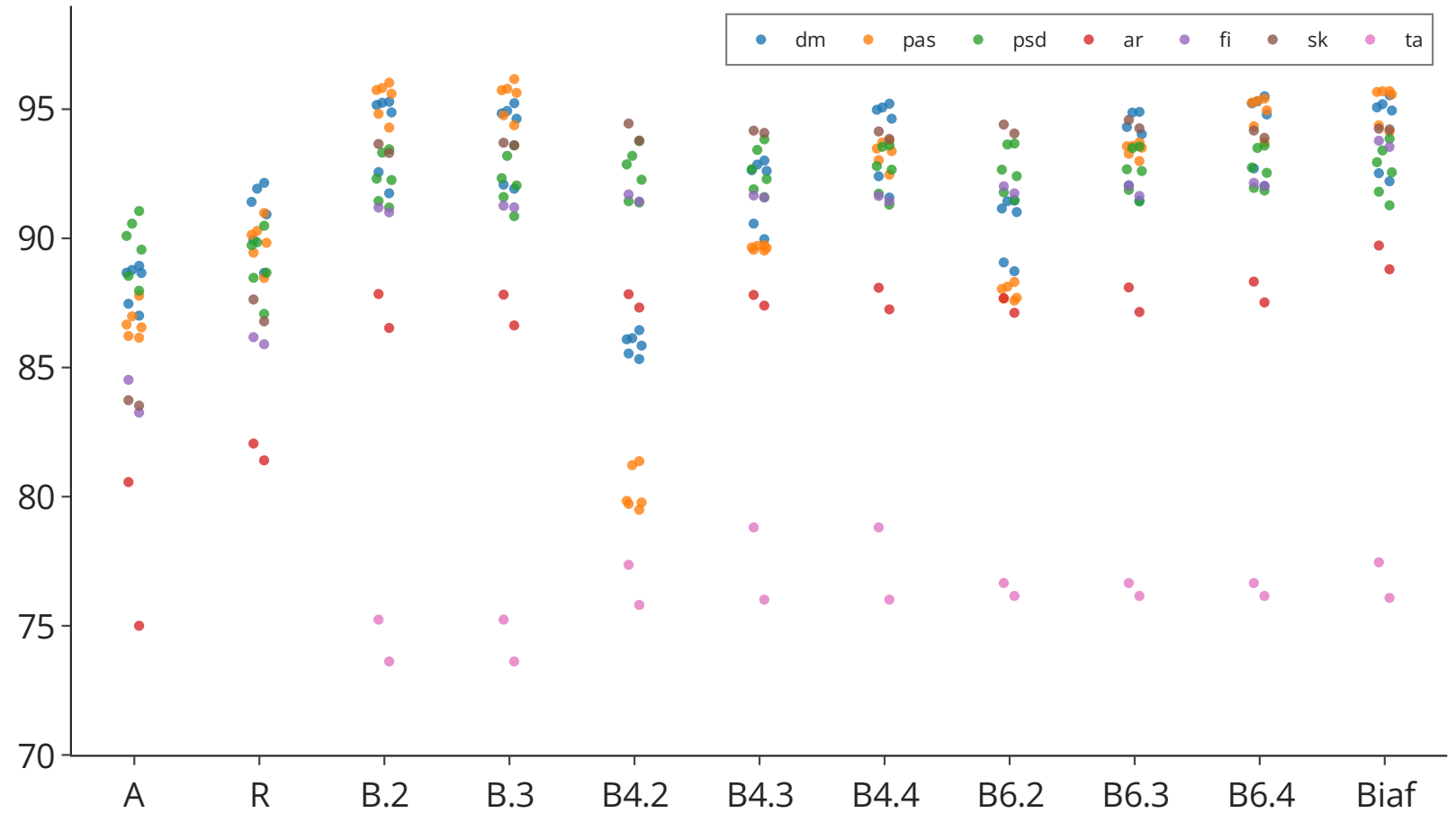
Evaluation

- [SemEval 2015 Task 18](#) and [IWPT-2021 Shared Task](#).
- UF and LF score.
- **Biaffine** baseline ([Dozat & Manning, 2018](#)).
- **Coverage** (score with gold labels).

Results

Performance

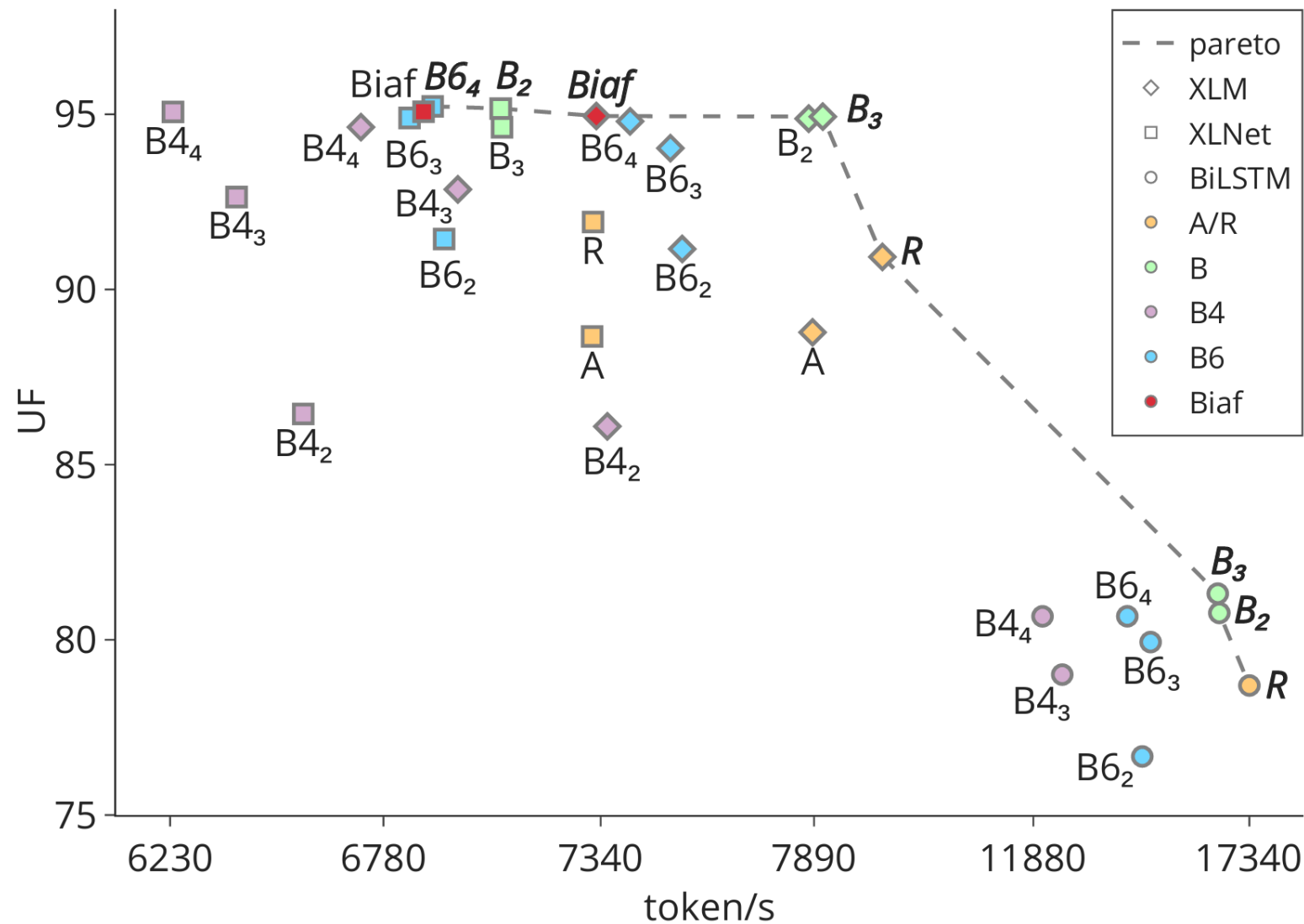
- Worst performance with **A** and **B**.
- **B2** and **B3** reach a similar performance to **Biaf**.
- Clear improvement increasing k .



Results

Speed

- Faster decoding for **A** and **R**.
- **B4.k** is slower than **Biaf** (likely due to the extra arcs generated).
- **B6.k** and **B.k** offer a trade-off between accuracy and speed.



Conclusions

- Several bounded and unbounded linearizations are proposed for graph parsing.
- Multilingual benchmark.
- SL algorithms and graph-based parsers have similar performance.
- SL is a faster and more efficient alternative to graph-based models.
- Hyperparameter k must be configured for bracket and bit encodings.

Thanks for listening!



Ana Ezquerro



David Vilares



Carlos Gómez-Rodríguez

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