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This version (28 Mar 2017 13:31) was **approved** by rgetz.
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ADALM-PLUTO Detailed Specifications

Features List:

- ADI AD9363, RF Agile Transceiver [<http://www.analog.com/AD9363>]:
 - 1 Transmit, 1 Receive channel (with  rate tuning frequencies)
 - Tuning range: 300 MHz - 3.8 GHz
 - 2.4 Hz LO step size
 - Tunable channel bandwidth: 200 kHz - 20 MHz
 - Integrated 12-bit DACs (Tx) and ADCs (Rx)
 - Variable output data rates: 61.44 MSPS - 65.1 kSPS
 - Modulation Accuracy (EVM): ≤ -40 dB (typical, not measured on every unit)
 - Internal I/Q correction and calibration
 - Tx to Rx Isolation:
 - Tx Specifications
 - Maximum Output Power:
 - Rx Specifications
 - Min sensitivity:
 - RX gain control: 0 to +74.5dB (800 MHz)
 - Manual Modes
 - Slow Attack
 - Fast Attack
 - Received Signal Strength Indicator: 100 dB (± 2 dB)
- Xilinx Zynq [<http://www.xilinx.com/XilinxZynq>] XC7Z010-1CLG225C
 - Zynq-7000 All Programmable SoC Overview [http://www.xilinx.com/support/documentation/data_sheets/ds190-Zynq-7000-Overview.pdf]
 - Datasheet [http://www.xilinx.com/support/documentation/data_sheets/ds187-XC7Z010-XC7Z020-Data-Sheet.pdf]
 - FPGA
 - Logic Cells: 28k
 - Block RAM: 2.1Mb
 - DSP Slices 80
 - ARM Processing System
 - Single-core ARM® Cortex™-A9 MPCore™
 - 667 MHz
 - USB 2.0 (included in the Zynq)
 - streams up to 4MSPS with no dropped samples
- Memory
 - Micron DDR3L
 - MT41K256M16TW-107 [<https://www.micron.com/parts/dram/ddr3-sdram/mt41k256m16tw-107>]
 - 1066 Mbps (16-bit interface)
 - 512 Mbytes
 - Micron Serial Flash
 - N25Q256A11E1240F [<https://www.micron.com/parts/nor-flash/serial-nor-flash/n25q256a11e1240f>]
 - 32 Mbyte
 - Quad I/O provides throughput up to 54 MBps
 - Minimum 100,000 ERASE cycles (don't update the firmware more times than this)



- Power
 - Completely self powered from [USB](#)
 - Optional power connector

university/tools/pluto/devs/specs.txt · Last modified: 01 Nov 2016 15:46 by rgetz

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