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# Analog Devices Wiki

This version (02 May 2017 08:33) was **approved** by mhennerich.  
 The Previously approved version (21 Apr 2017 13:21) is available.  
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## ADALM-PLUTO for Developers

The people who typical read these pages are those who write custom software or HDL (for the FPGA) that run directly on the Pluto device. This may put the Pluto in different modes, and support different external USB devices (including USB/LAB, or USB/WiFi), extending the capabilities of the device, or completely changing the data that is transferred to the host. Since the goal of the project is to keep things as open as possible, the details on how to compile kernels, create bit files, assemble FIT files and load them into the device, should be found here.

While we do have a few examples, and show how to re-create the default software loads, since this the hardware can be nearly a blank slate for your project, you can do anything you want.

## Content

- 1. Introduction
- 2. Hardware
  - a. Detailed Specifications
  - b. Schematics
  - c. Detailed Performance
  - d. Accessing the Console with the ADALM-JTAGUART adapter
  - e. Accessing FPGA JTAG with the ADALM-JTAGUART adapter
- 3. ADI Reference Designs HDL User Guide
  - a. AD9361 HDL reference design
  - b. AXI\_AD9361
- 4. Device Drivers
  - a. AD9361 high performance, highly integrated RF Agile Transceiver™ Linux device driver
  - b. AXI ADC HDL Linux Driver
  - c. AXI DAC HDL Linux Driver
  - d. AXI DMAC Linux Driver
  - e. ADM1177 [http://www.analog.com/ADM1177] Digital Power Monitor Linux Driver
  - f. etc.
- 5. Building the Firmware image from source
  - a. Obtaining the Sources
  - b. Building the Image
- 6. Controlling the transceiver and transferring data
- 7. Accessing the AD9363 inside Pluto from userspace
  - a. libio local mode
- 8. USB OTG HOST function Support
- 9. Using U-Boot's DFU modes
- 10. Reboot Modes

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