Understanding Op Amp Noise in Audio Circuits



Tyler Noyes, Tamara Alani

ABSTRACT

One of the greatest design challenges when building a high fidelity audio circuit is how to reduce the total system noise. This noise is most often compared to the system signal and represented as a signal-to-noise ratio (SNR). Noise can be defined as an unwanted signal that interferes with the desired signal causing an error. The overall noise of an audio circuit can come from multiple sources, some intrinsic and some extrinsic.

The three largest contributors to intrinsic noise in an amplifier circuit are the thermal noise, voltage noise, and current noise. In this article, we will demonstrate the importance of calculating total noise before selecting an amplifier for an audio application. We will be using two different amplifier architectures, CMOS, and Bipolar, to demonstrate the impact the different noise sources will have on the audio circuit.

Table of Contents

1 Thermal Noise	
2 Operational Amplifier Voltage Noise	3
2.1 Flicker Noise	3
2.2 Broadband Noise	3
3 Operational Amplifier Current Noise	
3.1 Taking a Deeper Look at Noise Sources	4
4 Calculating Voltage Noise at the Output	
5 Summary	8
List of Figures	
, , , , , , , , , , , , , , , , , , ,	
List of Figures	
Figure 1-1. Voltage Noise Density Curve	
Figure 1-1. Voltage Noise Density Curve	3
Figure 1-1. Voltage Noise Density Curve	3
Figure 1-1. Voltage Noise Density Curve	3 2
Figure 1-1. Voltage Noise Density Curve	
Figure 1-1. Voltage Noise Density Curve	
Figure 1-1. Voltage Noise Density Curve	

Trademarks

All trademarks are the property of their respective owners.

Thermal Noise www.ti.com

1 Thermal Noise

Resistors can be a major contribution to the overall noise of an audio circuit. The noise generated from a resistor, also known as thermal noise, is noise generated by the random motion of charges within the resistor. We can calculate the noise generated by an ideal resistor using Equation 1.

$$e_{n(R)} = \sqrt{4 \times k \times T_K \times R}$$
 (1)

Where

- $e_{n(R)}$ = Noise spectral density from resistance in nV/ \sqrt{Hz}
- k = Boltzmann's constant 1.38 x 10-23 J/K
- T_k = Temperature in Kelvin
- R = The input resistance referred to the non-inverting terminal of the amplifier

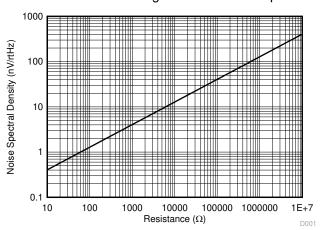


Figure 1-1. Voltage Noise Density Curve

Figure 1-1 displays the relationship between noise spectral density (in nV/√Hz) and resistance (in ohms) plotted for T = 25°C (298K) with varying source resistance values. At just 1k ohms of source resistance the voltage noise is already at 4nV/√Hz.

It's important to note that an ideal resistor will exhibit predictable noise density that is flat across the frequency spectrum. Multiplying Equation 1 by the noise bandwidth yields RMS. The noise bandwidth is the bandwidth of your circuit. This bandwidth can either be set by the operational amplifier internal circuitry or by using a filter. This RMS noise calculation is shown in Equation 2.

$$E_{n(R)} = \sqrt{4 \times k \times T_k \times R \times BW_n}$$
(2)



2 Operational Amplifier Voltage Noise

An operational amplifier has a voltage noise and current noise source. The magnitude of the noise sources inside the amplifier is given in the amplifier's data sheet. When considering the voltage noise of an amplifier, it is important to realize the architecture of the amplifier. Typically, a bipolar input amplifier will have much lower voltage noise than a CMOS input amplifier for the same amount of quiescent current. For more information on the difference between amplifier architectures see this technical article: Trade-offs Between CMOS, JFET, and Bipolar Input Stage Technology. Before discussing the different types of voltage noise, it's important to understand what this noise looks like in an amplifier circuit. Amplifier noise can be simplified by modeling it as an external voltage noise $e_{n(v)}$ on the positive terminal of the amplifier as shown in Figure 2-1.

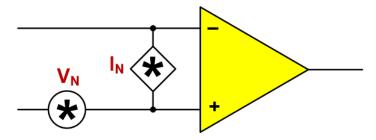


Figure 2-1. Voltage Noise

Amplifier voltage noise can be broken down into two main components: flicker noise and broadband noise. Figure 1-1 displays these noise regions.

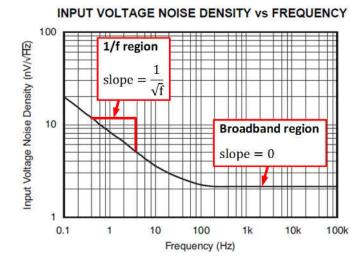


Figure 2-2. Voltage Noise Density Curve

2.1 Flicker Noise

Flicker noise, or 1/f noise, is considered to be in the low frequency range; that is, frequencies less than 1kHz. 1/f noise has a slope of one divided by the square root of frequency. For low-frequency focused circuits, such as a woofer or bass control stage, 1/f noise can be critical. However for a circuit that covers the full audio bandwidth, the 1/f noise will not be a dominant noise contribution.

2.2 Broadband Noise

Broadband noise, also called wide-band noise, is considered to be in the middle-to-high frequency range; for example, frequencies greater than 1kHz. In most amplifier data sheets, this noise spectral density specification is shown at frequencies of 1kHz and 10kHz.

3 Operational Amplifier Current Noise

As previously mentioned, amplifiers have a current noise contribution shown as I_N in Figure 2-2. Current noise is represented as a noise source between the inverting and non-inverting inputs. Input current noise density (i_n) is most commonly shown in the amplifier data sheet in units of. For current noise calculations, it is often necessary to calculate Req, the equivalent resistance seen by the input, shown in Figure 3-1.

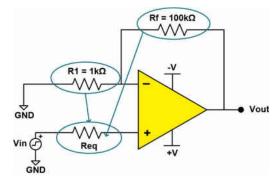


Figure 3-1. Req Equivalent Circuit

The parallel combination of Rf and R1 act like a resistance on the amplifier's non-inverting input, so Req in this example has a value of approximately $1k\Omega$.

This Req value can be multiplied by the input current noise density specification from the amplifier's data sheet to yield the noise contribution due to current noise, in units of V/\sqrt{Hz} . This calculation is shown in Equation 3.

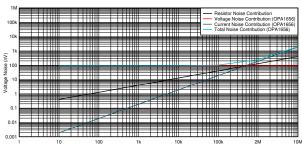
$$\mathbf{e}_{\mathsf{n}(\mathsf{I})} = \mathsf{in} \times \mathsf{R}_{\mathsf{eq}} \tag{3}$$

Multiplying the spectral density by the square root of the noise bandwidth gives RMS voltage noise. This is shown in Equation 4.

$$e_{n(l)} = i_n \times R_{eq} \times \sqrt{BW_n}$$
(4)

3.1 Taking a Deeper Look at Noise Sources

Using the OPA1656 and OPA1612 we can see how the different noise sources can affect the overall noise. Using excel we can plot out the voltage noise, current noise, and resistance noise vs resistance values. For this example, the frequency is set to 1kHz.





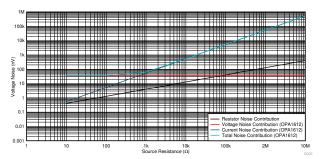


Figure 3-3. OPA1612 Noise Curves

As the source resistance increases, the overall noise of the OPA1612 will eventually dominate that of the OPA1656 due to the current noise. For this reason, it is paramount to understand the system and all its noise contributions before choosing an amplifier.

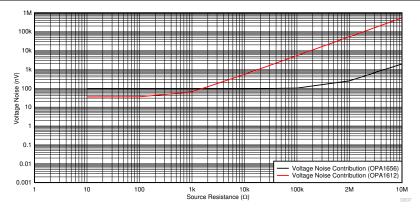


Figure 3-4. Total Noise of OPA1656 vs OPA1612

4 Calculating Voltage Noise at the Output

Now, we run through a hand calculation to find the total noise of a simplified audio system. For this example, we are using the OPA1656 in a non-inverting configuration with a gain of 40 dB or 100V/V. We are also using filtering as described in Section 1 to limit the BW. Figure 4-1 is the circuit configuration we are using for this example.

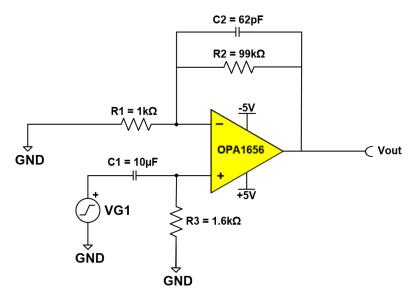


Figure 4-1. Circuit Configuration

The first step is to calculate the noise BW of the circuit. The cutoff frequency of the circuit in the example is set at 25.9K. The bandwidth noise of our circuit can be calculated using Equation 5.

$$BW_{n} = k_{n} \times f_{c}$$
(5)

Where:

- BWn = Noise BW
- Kn Brick wall factor (1.57 for a first order filter)
- Fc = Cutoff frequency

Equation 6 is the noise bandwidth for the circuit shown in Figure 4-1.

$$BW_n = 1.57 \times 25.9 \text{ k} = 40.7 \text{ kHz}$$
 (6)



We start by looking at the thermal noise for the circuit. The thermal noise a circuit that can be calculated by first calculating the Req of the gain network. This is completed by putting R1 and R2 in parallel. For the circuit shown in Circuit 1 this is done using the following:

$$R_{eq} = R_1 \| R_2 = 1 k \| 99 k = 990 \Omega$$
 (7)

At higher speeds the DC blocking cap will act like a short. For this reason, the R4 resistor is considered in parallel with the source resistance. This value is very low and for that reason is not included in the Req calculation. Using Equation 1, the 2RMS voltage noise contribution from the resistors can be calculated.

$$E_{V} = 6 \times 990 \times \sqrt{40.7 \text{ k}} = 1.2 \text{ nV}$$
 (8)

Next, we look at the voltage noise of the circuit. When evaluating the voltage noise contribution from the operational amplifier, the first step is to take the input voltage noise shown in the data sheet and multiply it by the square root of the frequency. If we look at the voltage noise from the *OPA1656 SoundPlus Ultra-low Noise and Distortion, Burr-Brown Audio Operational Amplifier* data sheet we see that the value is 2.9 nV/rtHz at 10kHz. As we have shown in Figure 1-1 the broadband noise will remain flat across frequency, so we can use this number for any frequencies past 10kHz. Taking the voltage noise of the amplifier times our cutoff frequency will yield the RMS voltage noise of the amplifier.

$$E_I = 2.9 * \sqrt{40.7k} = 585 \, nV \tag{9}$$

The next step is to calculate the current noise contribution of the amplifier. Using Equation 4 it is possible to calculate the RMS current noise contribution of the amplifier. For this example, the current noise of the OPA1656 is 6 fA/rtHz.

$$E_{V} = 6 \times 990 \times \sqrt{40.7 \text{ k}} = 1.2 \text{ nV}$$
 (10)

Next is the calculation for the total noise at the input of the amplifier.

Total Noise =
$$\sqrt{\left(\text{Thermal Noise}\right)^2 + \left(\text{Current Noise}\right)^2 + \left(\text{Voltage Noise}\right)^2}$$
 (11)

Total Noise =
$$\sqrt{813n^2 + 585n^2 + 1.2n^2} = 1.00 \text{ uV}$$
 (12)

This will give what is known as the total input refereed voltage noise. Next multiply the input referred voltage noise by the voltage noise gain, which for our circuit is 100V/V. Remember this is the gain at the non-inverting terminal of the amplifier.

$$V_{os,N} = Total \ Noise \times Noise \ Gain = 1.00 \ uV \times 100 = 100 \ uV$$
(13)

Based on our calculations we have a value of 100uVpp. This is very close to the simulated value of the circuit shown in Figure 4-2. Note that for a more accurate calculation, the 1/f noise may be calculated separately. For more information on voltage noise and how to calculate this error, follow these training videos: *TI Precision Labs - Op Amps: Noise*.

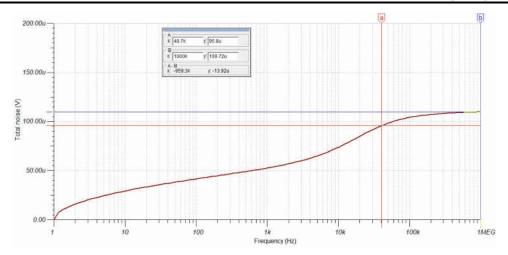


Figure 4-2. Simulation Circuit

One of the key reasons for doing the calculations manually, is that it allows you to evaluate the individual noise sources. In our example, we actually see that the resistor noise is our highest noise source.

Summary www.ti.com

5 Summary

When attempting to get the lowest overall noise performance out of your audio op amp there are a lot of factors to consider. It is very important to always consider all three noise sources, thermal, voltage, and current noise when building your Hifi circuit. Table 5-1 shows a list of our latest low noise audio amplifiers for your consideration.

Table 5-1. Low Noise Audio Amplifiers

Devices	Voltage noise	Current noise
OPA1656 – Ultra low noise CMOS	4.3 nV/rtHz	6 fA/rtHz
OPA1637 – Low power FDA	3.7 nV/rtHz	300 fA/rtHz
INA1620 – Lowest noise THD+N	2.8 nV/rtHz	800 fA/rtHz
OPA1612 – Ultra low noise Bipolar	1.1 nV/rtHz	1700 fA/rtHz

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2022, Texas Instruments Incorporated