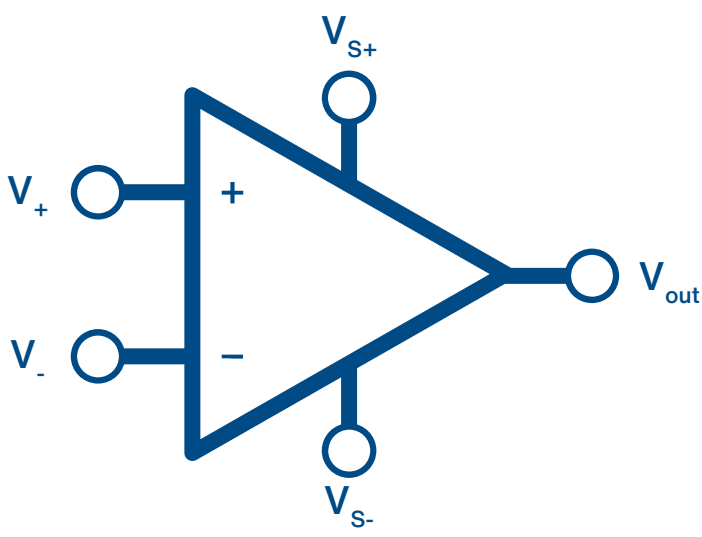
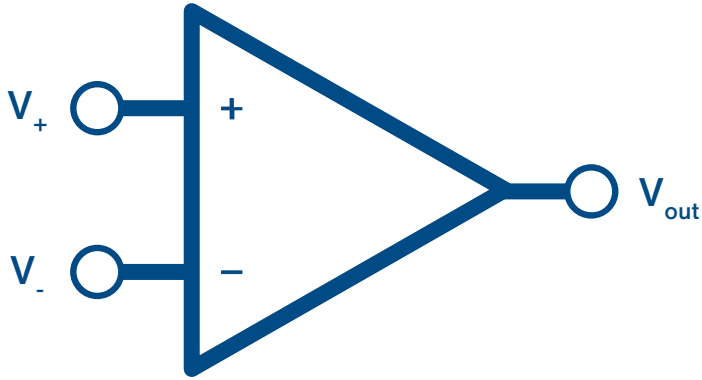
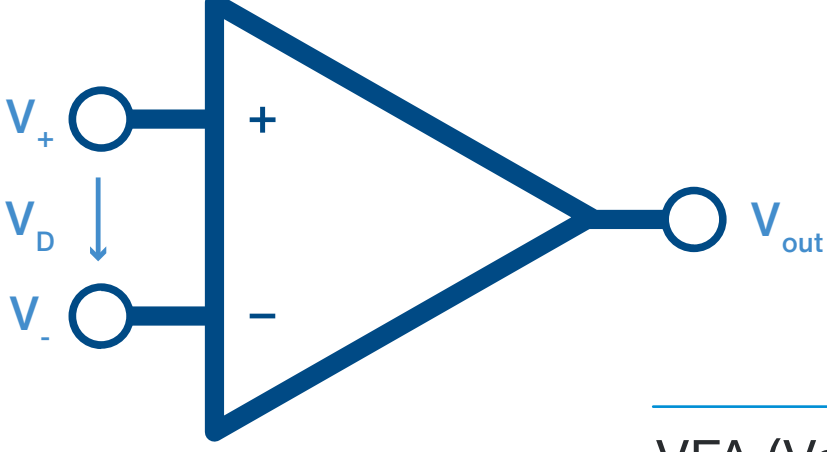
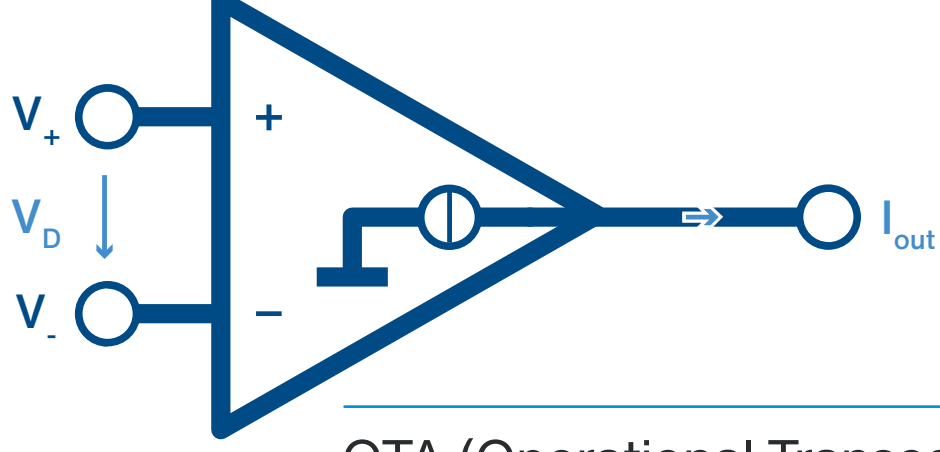
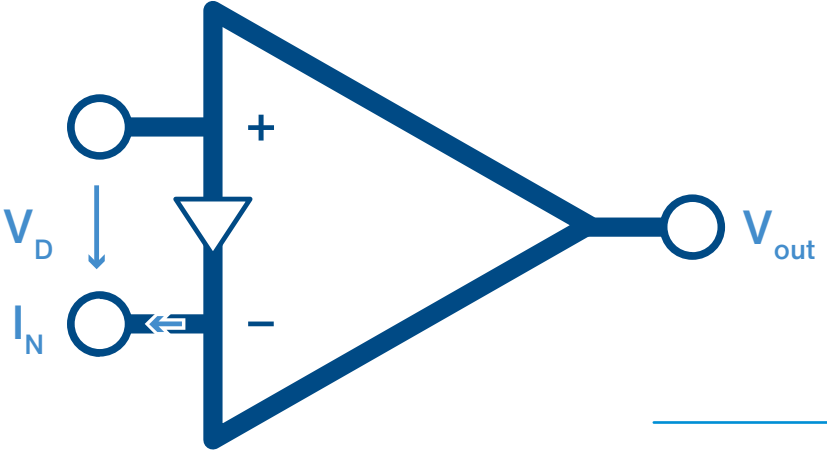
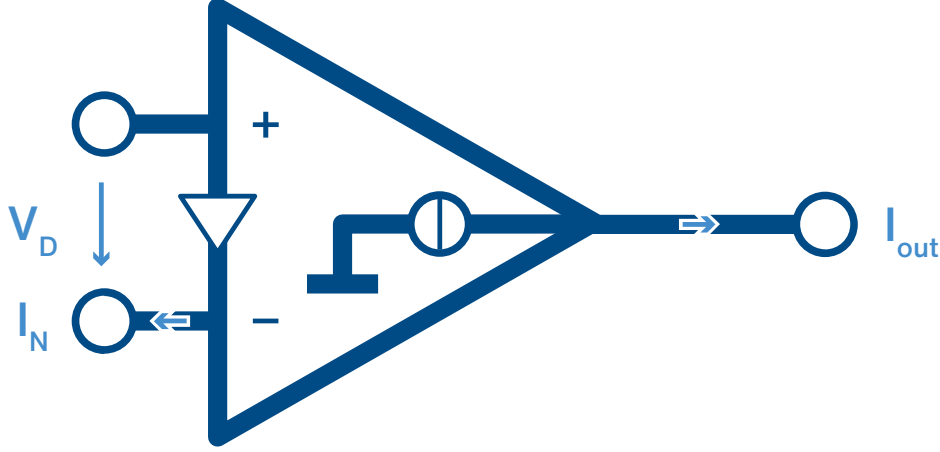
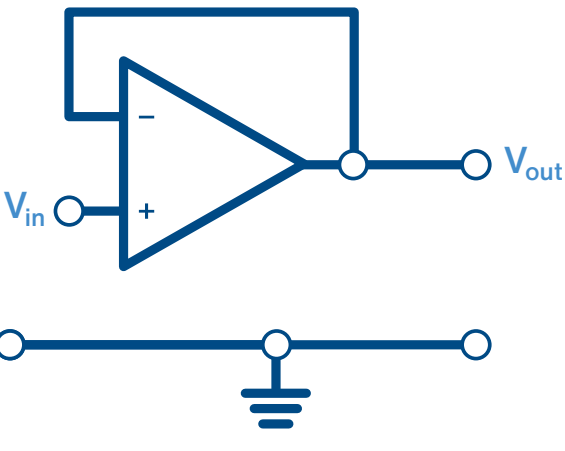
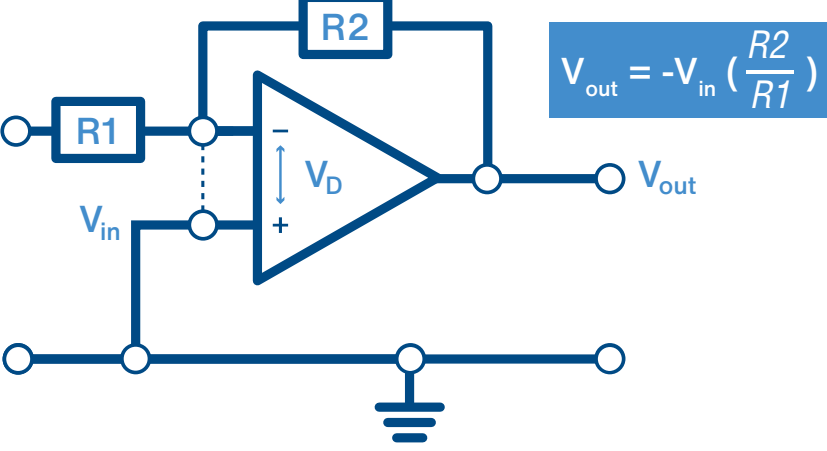
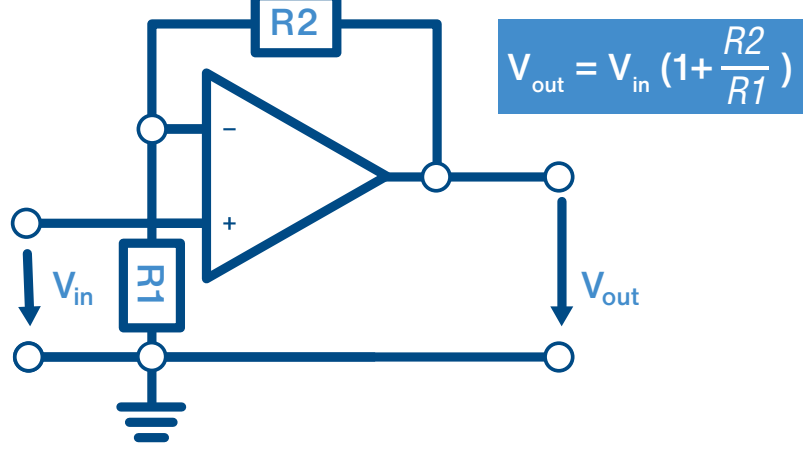
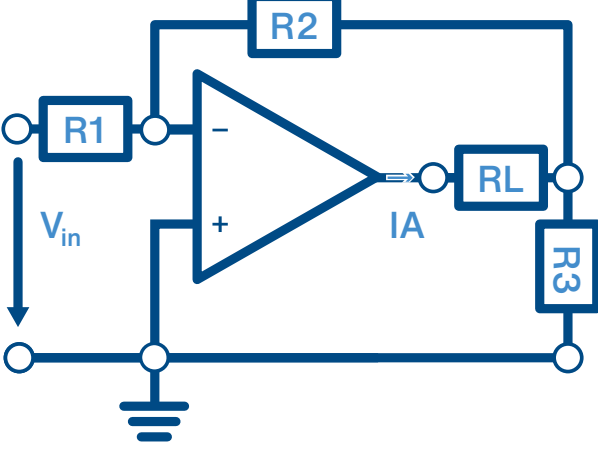
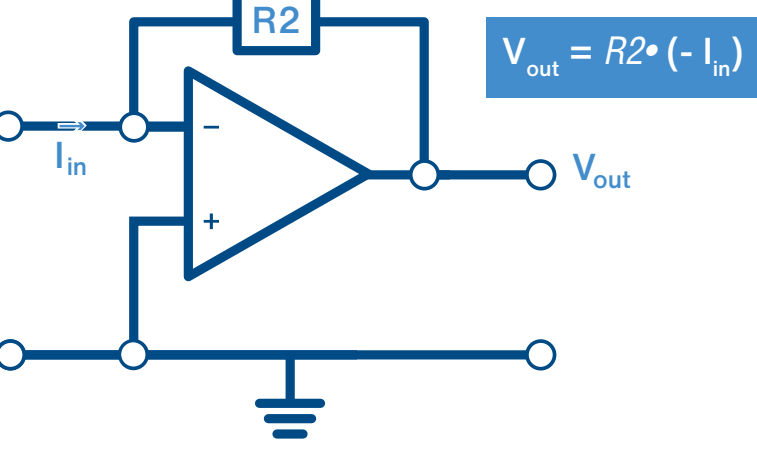
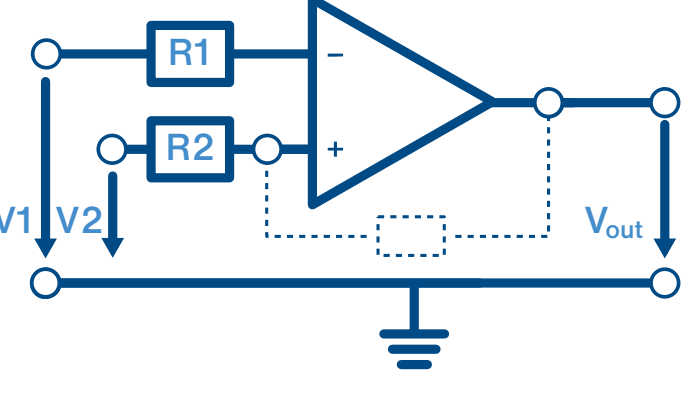
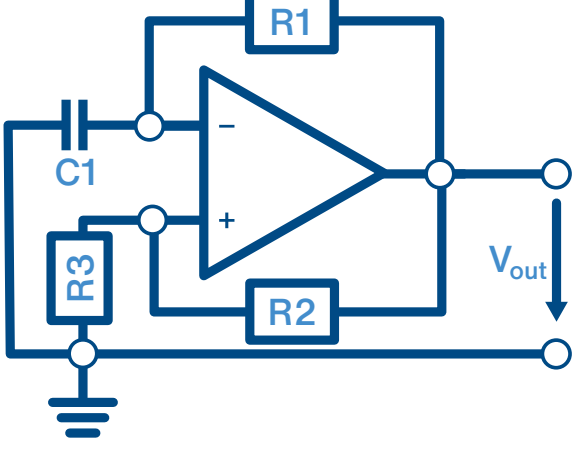
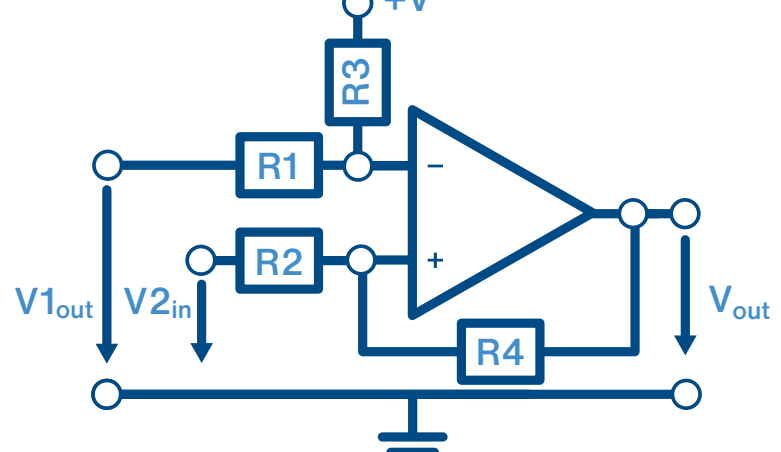


Circuit symbol	Basic properties
	<p>The two basic configurations of operational amplifiers are “inverting” and “non-inverting”</p>
	<p>The elementary operational amplifier is a 3-terminal device, with 2 inputs and 1 output, (excluding power connections)</p>
	<p>One of the inputs is called the inverting input, marked with a minus sign, the other input is the non-inverting Input, marked with a plus.</p>
	<p>The ideal operational amplifier has infinite input impedance ($Z_{IN} = \infty$), meaning that no current flows into either of its two inputs</p>
	<p>The ideal operational amplifier has zero output impedance ($Z_{OUT} = 0$)</p>
	<p>The ideal operational amplifier has zero input offset voltage $V_1 = V_2$</p>
	<p>The output port can both sink and source either a voltage or a current</p>

Op-amp configurations			
Input: Voltage	Output: Voltage	Output: Current	
	 <p>VFA (Voltage Feedback Amplifier)</p>	 <p>OTA (Operational Transconductance Amplifier)</p>	
Input: Current	 <p>CFA (Current Feedback Amplifier)</p>	 <p>Current Amplifier</p>	

Additional properties	Key values of op-amps
<p>Every op-amp has two inputs (+) and (-) and one output. Generally, it detects the difference between the input voltages $V_D = V_+ - V_-$</p>	<p>Open Loop Gain: $v_{oL} = V_{out}/V_{in}$</p>
<p>If $V_+ > V_-$ then V_{out} increases, if $V_+ < V_-$ then V_{out} decreases.</p>	<p>Common Mode Voltage: $V_{CM} = (V_+ + V_-)/2$</p>
<p>$v < 0$: inverting, $v > 0$ non-inverting</p>	<p>Common Mode Gain: $v_{CM} = \Delta U_{out}/V_{CM}$</p>
<p>VFA: V_+ and V_- are high impedance voltage inputs, the output V_{out} behaves like a low impedance voltage source. Example: Texas Instruments OPA2356-EP</p>	<p>Common Mode Rejection Ratio: $CMRR = 20 \log(v_{oL}/v_{CM})$</p>
<p>CFA: inverting input is low impedance, output V_{out} is a low-impedance voltage source. Example: Analog Devices AD8014ARTZ-REEL7</p>	
<p>OTA: Both inputs high impedance, output high impedance current source. Example: ON Semiconductor NE5517DR2G</p>	
<p>Current amplifier: low impedance inverted current input, high impedance current output</p>	

Basic circuits				
 <p>Voltage Follower</p>	 <p>Inverting Amplifier</p>	 <p>Non-inverting Amplifier</p>	 <p>Voltage/Current Converter</p>	
 <p>Current/Voltage Converter</p>	 <p>Voltage Comparator</p>	 <p>Astable Multivibrator</p>	 <p>Flip-Flop</p>	