25K133,25K134,25K135

SILICON N-CHANNEL MOS FET

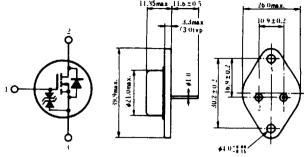
HITACHI/(OPTOELECTRONICS)

LOW FREQUENCY POWER AMPLIFIER

Complementary pair with 2SJ48, 2SJ49, 2SJ50

FEATURES

- High Power Gain.
- Excellent Frequency Response.
- High Speed Switching.
- Wide Area of Safe Operation.
- Enhancement-Mode.
- Good Complementary Characteristics.
- Equipped with Gate Protection Diodes.



1. Gate

2. Drain 3. Source (Case)

(JEDEC TO-3)

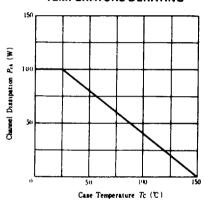
(Dimensions in mm)

B ABSOLUTE MAXIMUM RATINGS $(T_a=25^{\circ}\text{C})$

Item	Symbol	Rating			
		2SK133	2SK134	2SK135	Unit
Drain-Source Voltage	Vosx	120	140	160	v
Gate-Source Voltage	V_{GSS}	±14			v
Drain Current	I _D	7			A
Body-Drain Diode Reverse Drain Current	IDR	7			A
Channel Dissipation	P _{ch} +	100		W	
Channel Temperature	Tch	150		°C	
Storage Temperature	T_{ilg}	-55~+150		°C	

^{*}Value at Tc=25 °C

POWER VS. **TEMPERATURE DERATING**



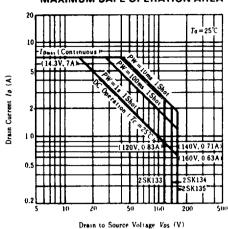
■ ELECTRICAL CHARACTERISTICS (T_e=25 °C)

Item		Symbol	Test Condition	min.	typ.	max.	Unit
Drain-Source Breakdown	2SK133	V _{(BR)DSX}		120	_	_	v
	2SK134		$I_D=10$ mA, $V_{GS}=-10$ V	140	_	_	v
Voltage	2SK135] [160		_	v
Gate-Source Breakdown Voltage		V _{(BR)GSS}	$I_G = \pm 100 \mu A$, $V_{DS} = 0$	±14		 	v
Gate-Source Cutoff Voltage		VGROSS	I _D =100mA, V _{DS} =10V	0.15		1.45	v
Drain-Source Saturation Voltage	V _{DS(zer)}	$V_{DS(set)}$ $I_D=7A$, $V_{GD}=0$ *	_		12	v	
Forward Transfer Admitta	nce	ויעו	$I_D=3A$, $V_{DS}=10V^*$	0.7	1.0	1.4	S
Input Capacitance		Cus		_	600	_	pF
Output Capacitance Reverse Transfer Capacitance		Coss	$V_{GS} = -5 \text{ V}, V_{DS} = 10 \text{ V}, f = 1 \text{ MHz}$		350	_	pF
		Crss		_	10	_	pF
Turn-on Time		ton			180	 -	ns
Turn-off Time		t _{off}	$V_{DD}=20$ V, $I_D=4$ A		60	<u> </u>	ns

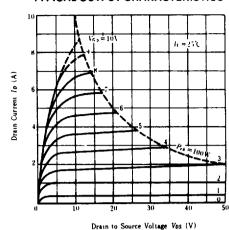
^{*}Pulse Test

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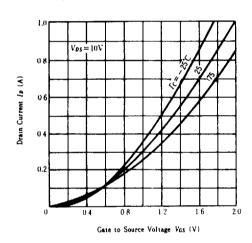
MAXIMUM SAFE OPERATION AREA



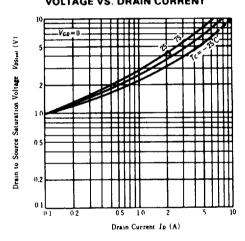
TYPICAL OUTPUT CHARACTERISTICS



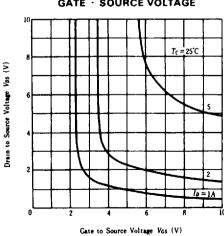
TYPICAL TRANSFER CHARACTERISTICS



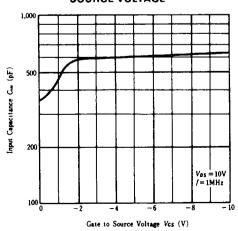
DRAIN - SOURCE SATURATION VOLTAGE VS. DRAIN CURRENT



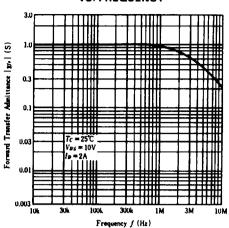
DRAIN - SOURCE VOLTAGE VS. GATE - SOURCE VOLTAGE



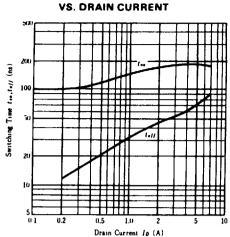
INPUT CAPACITANCE VS. GATE SOURCE VOLTAGE



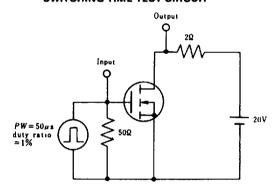
FORWARD TRANSFER ADMITTANCE VS. FREQUENCY



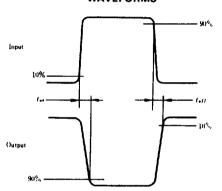
SWITCHING TIME VS. DRAIN CURRENT



SWITCHING TIME TEST CIRCUIT



WAVEFORMS



HITACHI/(OPTOELECTRONICS) 61E D