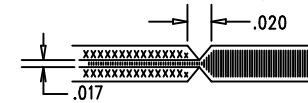


SHOWN FROM TOP SIDE

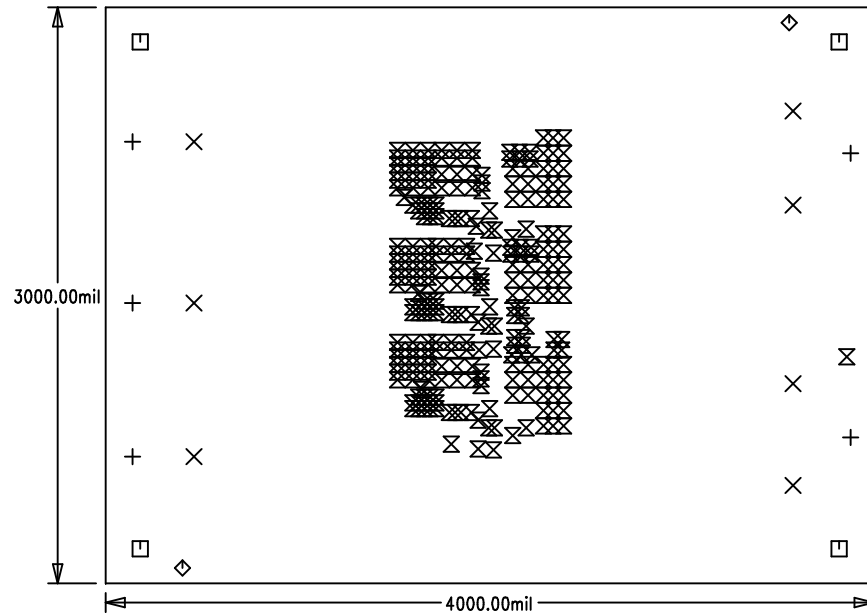
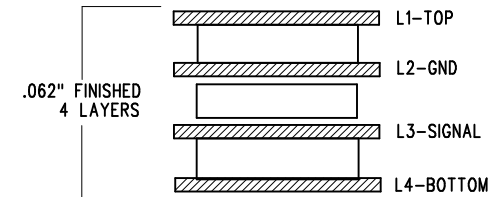
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	KAUNG H.	11-20-15

NOTES: UNLESS OTHERWISE SPECIFIED


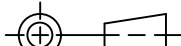
- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 2 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



LAYER STRUCTURE



SIZE	QTY	SYM	PLATED	TOL
94	5	+	YES	+/-0.0
205	7	X	YES	+/-0.0
188	4	□	YES	+/-0.0
70	2	◇	NO	+/-0.0
10	268	⊗	YES	+/-0.0

UNLESS OTHERWISE SPECIFIED		APPROVALS		 <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>	
DIMENSIONS ARE IN INCHES		PCB DES.	KAUNG H.		
TOLERANCES:		APP ENG.	KAUNG H.	TITLE: FABRICATION DRAWING	
0.XX" = ±0.01"				3-PHASE IDEAL DIODE BRIDGE	
0.XXX" = ±0.005"				SIZE	IC NO.
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				N/A	LT4320IDD-1
THIRD ANGLE PROJECTION				DEMO CIRCUIT 2465A	
				SCALE = NONE	REV 2
				FILENAME: DC2465A-1.PCB	SHT 1 OF 1