```
# run 1000ns
PC=0  | Instr=01095020 | RegDst=1 | RegWr=1 | AluSrc=0 | ALUOp=010 | ALURes=      14 | WriteReg=10 | WriteData=      7 | ReadData=      x | Zero=0 | PcSrc=0 | Jump=0
PC=4  | Instr=01095022 | RegDst=1 | RegWr=1 | AluSrc=0 | ALUOp=110 | ALURes=       0 | WriteReg=10 | WriteData=      7 | ReadData=      4 | Zero=1 | PcSrc=0 | Jump=0
PC=8  | Instr=01095024 | RegDst=1 | RegWr=1 | AluSrc=0 | ALUOp=000 | ALURes=       7 | WriteReg=10 | WriteData=      7 | ReadData=     11 | Zero=0 | PcSrc=0 | Jump=0
PC=12 | Instr=01095025 | RegDst=1 | RegWr=1 | AluSrc=0 | ALUOp=001 | ALURes=       7 | WriteReg=10 | WriteData=      7 | ReadData=     11 | Zero=0 | PcSrc=0 | Jump=0
PC=16 | Instr=11090001 | RegDst=0 | RegWr=0 | AluSrc=0 | ALUOp=110 | ALURes=       0 | WriteReg= 9 | WriteData=      7 | ReadData=      4 | Zero=1 | PcSrc=1 | Jump=0
PC=24 | Instr=0109502a | RegDst=1 | RegWr=1 | AluSrc=0 | ALUOp=111 | ALURes=       0 | WriteReg=10 | WriteData=      7 | ReadData=      4 | Zero=1 | PcSrc=0 | Jump=0
PC=28 | Instr=ad0a0000 | RegDst=0 | RegWr=0 | AluSrc=1 | ALUOp=010 | ALURes=       7 | WriteReg=10 | WriteData=      0 | ReadData=     11 | Zero=0 | PcSrc=0 | Jump=0
PC=32 | Instr=8d0a0000 | RegDst=1 | RegWr=1 | AluSrc=1 | ALUOp=010 | ALURes=       7 | WriteReg= 0 | WriteData=      0 | ReadData=      0 | Zero=0 | PcSrc=0 | Jump=0
PC=36 | Instr=290a0005 | RegDst=0 | RegWr=1 | AluSrc=1 | ALUOp=111 | ALURes=       0 | WriteReg=10 | WriteData=      0 | ReadData=      4 | Zero=1 | PcSrc=0 | Jump=0
PC=40 | Instr=15090001 | RegDst=0 | RegWr=0 | AluSrc=0 | ALUOp=110 | ALURes=       0 | WriteReg= 9 | WriteData=      7 | ReadData=      4 | Zero=1 | PcSrc=0 | Jump=0
PC=44 | Instr=0109502a | RegDst=1 | RegWr=1 | AluSrc=0 | ALUOp=111 | ALURes=       0 | WriteReg=10 | WriteData=      7 | ReadData=      4 | Zero=1 | PcSrc=0 | Jump=0
PC=48 | Instr=350a0005 | RegDst=0 | RegWr=1 | AluSrc=1 | ALUOp=001 | ALURes=       7 | WriteReg=10 | WriteData=      0 | ReadData=      0 | Zero=0 | PcSrc=0 | Jump=0
PC=52 | Instr=310a0003 | RegDst=0 | RegWr=1 | AluSrc=1 | ALUOp=000 | ALURes=       3 | WriteReg=10 | WriteData=      0 | ReadData=      7 | Zero=0 | PcSrc=0 | Jump=0
PC=56 | Instr=0800000f | RegDst=0 | RegWr=0 | AluSrc=0 | ALUOp=000 | ALURes=       0 | WriteReg= 0 | WriteData=      0 | ReadData=      4 | Zero=1 | PcSrc=0 | Jump=1
PC=60 | Instr=xxxxxxxx | RegDst=0 | RegWr=0 | AluSrc=0 | ALUOp=000 | ALURes=       x | WriteReg= x | WriteData=      x | ReadData=      x | Zero=x | PcSrc=0 | Jump=0
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