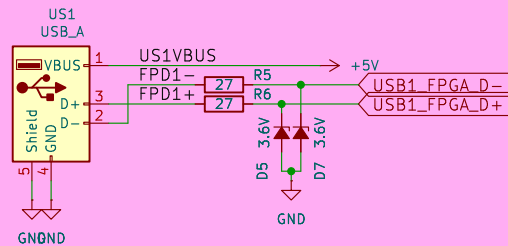


Main Page

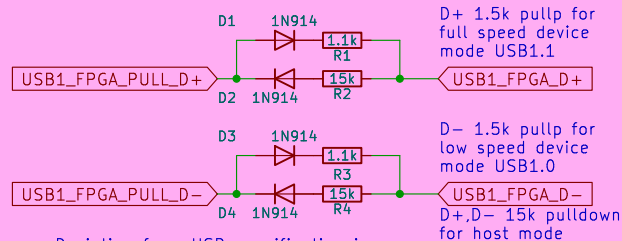
!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
Olimex board is delivering MAX 2.5V on connector.
You can use 2.5V output signals for 3.3V.
Be carefull FPGA input only tolerate MAX 2.5V!
!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!

!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!
Set and Voltage on BANK 1.2V/1.8V/2.5V
STEP UP can boost any of those voltages
to 3.3V/5V depending on selected chip
!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!

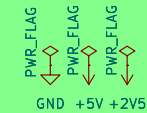
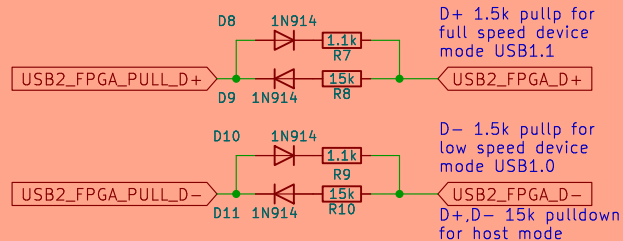
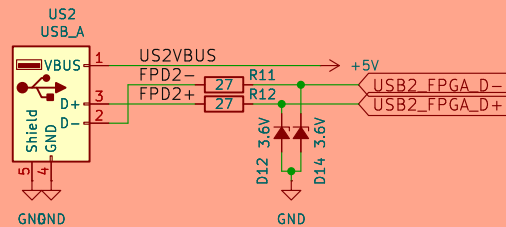


D5,D7: Schottky 2A/30V
Low drop V_{fmax}=0.375V
Parts reduction: Only D5 is required.
D6, D13 can be 1206
1A polyfuses or 0-ohm/2A jumpers

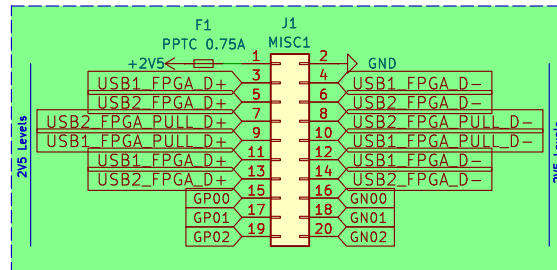
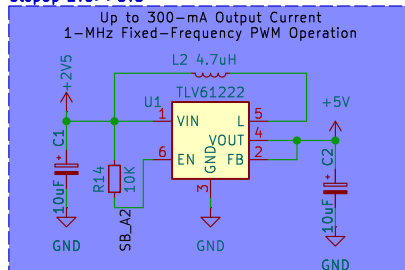
USB pull lines connected to
BANK1 on "gpdi" sheet



Deviation from USB specification in
pulldowns for BOM simplification.
With series diode, correct value R2 R4
should be 12k but 15k is used.



StepUp 2V5>>3V3



Sheet: /
File: USB.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 8.0.4

Rev:

Id: 1/1