

Logic Family and Circuits :-

* Combinational Circuit :-

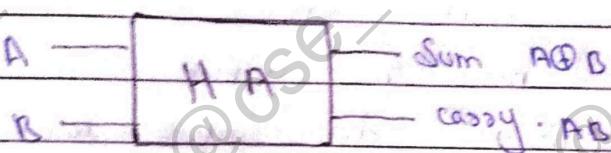
Address :-

- It is a digital circuit which adds two binary digits or numbers.
- Adder contains two output they are sum & carry.

Types of Adder :-

Half Adder :-

- It is a digital circuit in which only two binary digit added.
- Half Adder has two output \rightarrow sum & carry.
- Half Adder is a two input two output device.



Truth Table :-

Input		Output	
A	B	sum	carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

K-map always design for output

K-map of sum : { No boundary for POS or SOP }

	\bar{B}	B
A	0	1
\bar{A}	1	0
	1	1

$$\rightarrow f(A, B) = A \cdot \bar{B} + \bar{A} \cdot B$$

$$\text{Sum} = A\bar{B} + \bar{A}B$$

$$= A \oplus B$$

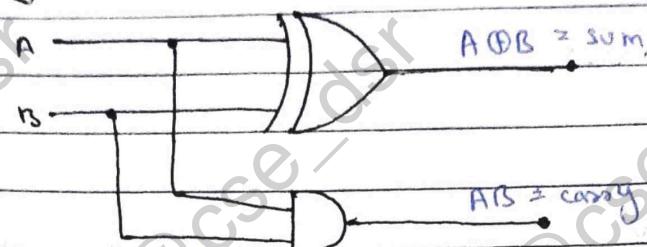
→ K-map of Carry:-

$A \setminus B$	\bar{B}	B
\bar{A}	0	1
A	1	0
	1	1

$$\rightarrow f(A, B) = A \cdot B$$

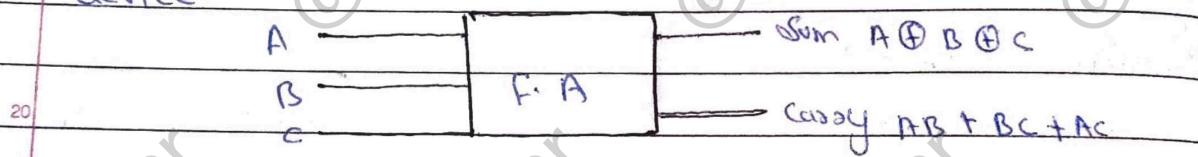
$$\text{Carry} = A \cdot B$$

Logic Circuit Diagram:-



Full Adder:-

- ⇒ It is a digital circuit in which only binary digit added.
- ⇒ Full adder has two output sum and carry.
- ⇒ Full adder is a three input, two output device.



Truth Table:-

Input				Output	
	A	B	C	Sum	Carry
0 0 0	0	0	0	0	0
0 0 1	0	0	1	1	0
0 1 0	0	1	0	1	0
0 1 1	0	1	1	0	1
1 0 0	1	0	0	1	0
1 0 1	1	0	1	0	1
1 1 0	1	1	0	0	1
1 1 1	1	1	1	1	1

K-map of sum

A \ BC	$\bar{B}\bar{C}$	$\bar{B}C$	$B\bar{C}$	BC
\bar{A}	0	1	2	3
A	4	5	6	7
	↓	↓	↓	↓

$$\begin{aligned} & A\bar{B}\bar{C} \quad A\bar{B}C \quad AB\bar{C} \quad \bar{A}B\bar{C} \\ \text{sum} = & A\bar{B}\bar{C} + A\bar{B}C + AB\bar{C} + \bar{A}B\bar{C} \\ & = B(A\bar{C} + \bar{A}C) + B(\bar{A}C + \bar{A}C) \end{aligned}$$

Now :-

$$A\bar{C} + \bar{A}C = x = A \oplus C$$

Adding complement both sides :-

$$A\bar{C} + \bar{A}C = \bar{x}$$

$$A\bar{C} \cdot \bar{A}C = \bar{x}$$

$$(\bar{A} + \bar{C}) \cdot (\bar{A} + C) = \bar{x}$$

$$(\bar{A} + C) (A + \bar{C}) = \bar{x}$$

$$\bar{A} + C \cdot A + \bar{C} = \bar{x}$$

$$BA + \bar{A}\bar{C} + AC + CC' = \bar{x}$$

$$\bar{A}\bar{C} + AC = \bar{x}$$

Putting the value of in eqn (i) :-

$$\Rightarrow \bar{B}(x) + B(\bar{x})$$

$$\Rightarrow \bar{B}x + B\bar{x}$$

$$\Rightarrow B \oplus x$$

$$\Rightarrow B \oplus (A\bar{C} + \bar{A}C)$$

$$\Rightarrow B \oplus A \oplus C$$

K-map of carry :-

A \ BC	$\bar{B}\bar{C}$	$\bar{B}C$	$B\bar{C}$	BC
\bar{A}	0	1	2	3
A	4	5	6	7
	↓	↓	↓	↓

$$F(A, B, C) = (AC + BC + AB)$$

$$\text{Carry} = AC + BC + AB$$

Diag name

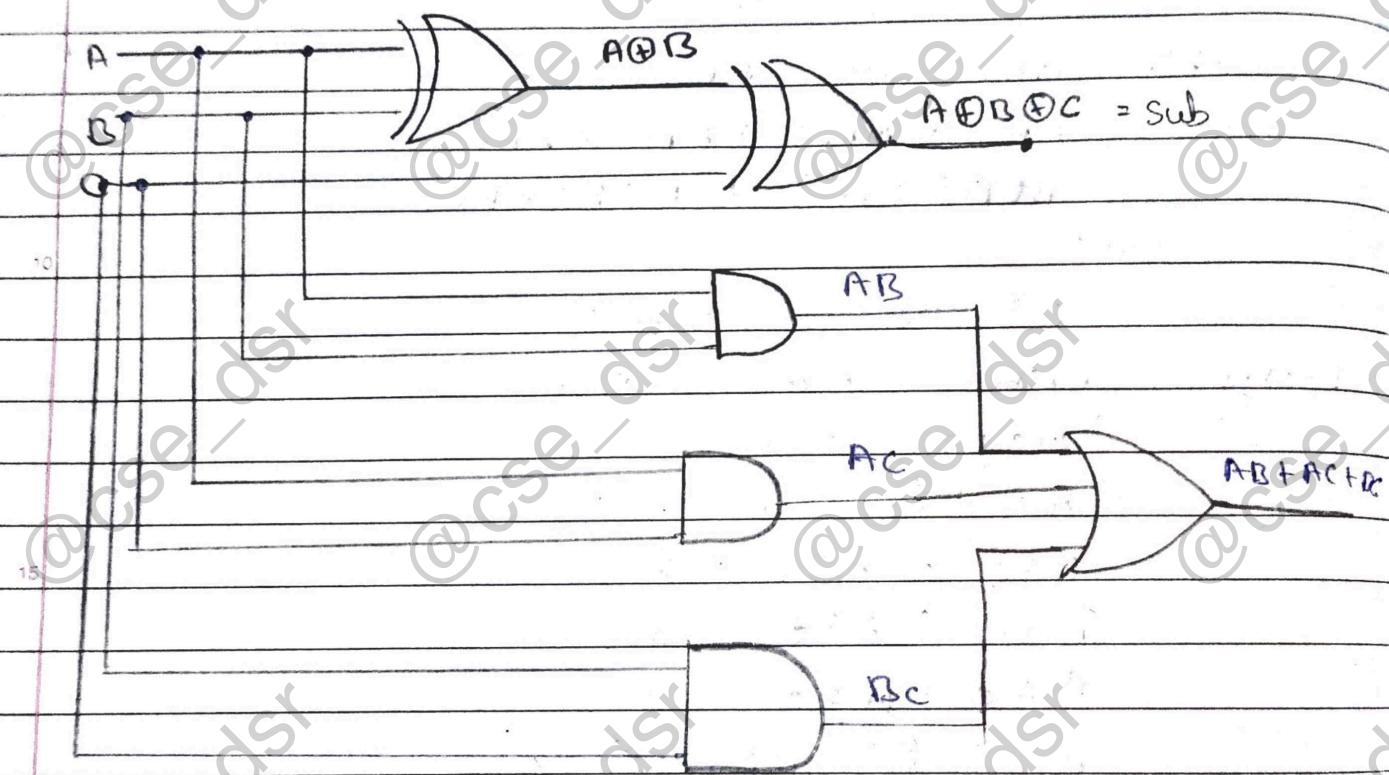


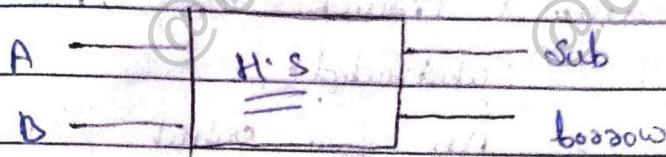
fig : full adder

Digital Subtractor:-

- ⇒ off is a digital ckt which subtracts two or more binary digit or number.
- ⇒ Subtraction has two output sub & borrow.

Half Subtractor:-

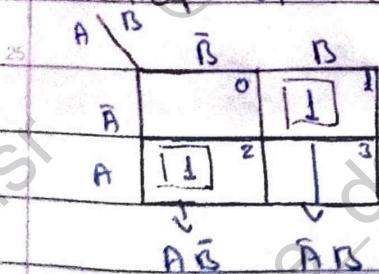
- ⇒ off is a digital ckt which subtracts two binary digit.
- ⇒ off has 2 output :- sub and borrow.
- ⇒ half subtractor is a two input one output device.



Truth Table:-

Input		Output	
A	B	Sub	borrow
0	0	0	0
0	1	1	1
1	0	1	0
1	1	0	0

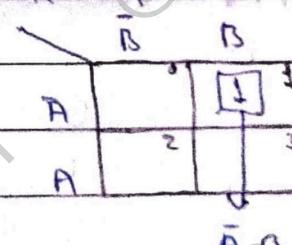
K-map of sub:-



$$\text{Sub} = AB + \bar{A}\bar{B}$$

$$\Rightarrow A \oplus B$$

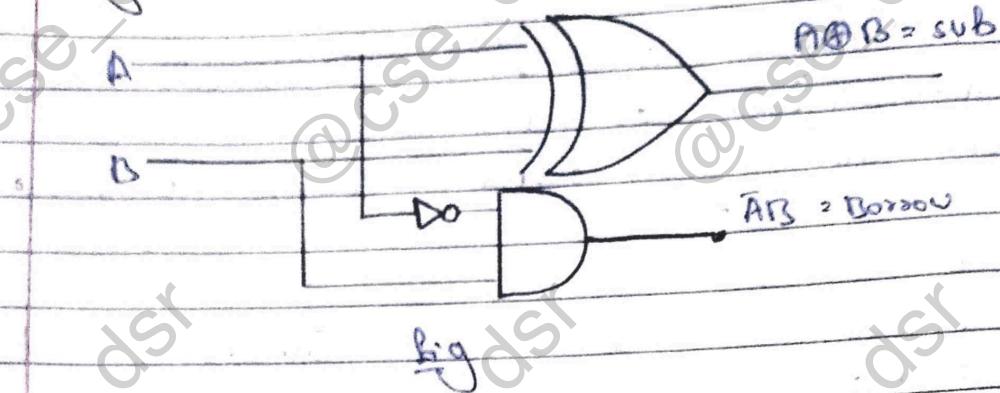
K map of borrow:-



$$\Rightarrow \text{Borrow}$$

$$\Rightarrow \bar{A}B$$

Logic ckt Diagram-



full Subtraction:-

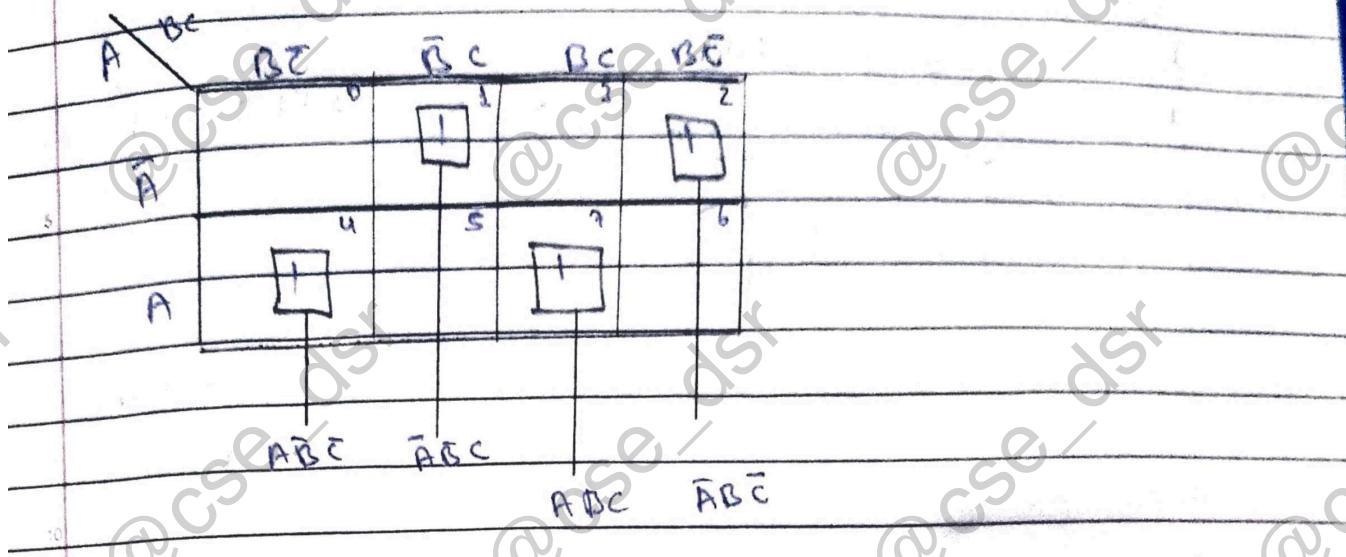
- It is a digital circuit in which only three binary digit is subtracted.
- Full Subtractor has two output Sub and borrow.
- Full Subtractor is a three input two output device.

A	B	C	Full Subtractor	Sub	Borrow
0	0	0			
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	0			
1	1	1			

Truth Table :-

Input	Output				
	A	B	C	sub	borrow
0 0 0	0	0	0	0	0
0 0 1	0	0	1	1	1
0 1 0	0	1	0	1	1
0 1 1	0	1	1	0	1
1 0 0	1	0	0	1	0
1 0 1	1	0	1	0	0
1 1 0	1	1	0	0	0
1 1 1	1	1	1	1	1

K-map of Sub:-



$$\text{Sub} = \bar{A}\bar{B}C + \bar{A}\bar{B}c + ABc + \bar{A}B\bar{C}$$

$$= (\bar{B}(A\bar{C} + A\bar{C}) + B(A\bar{C} + \bar{A}\bar{C}))$$

$$= A\bar{C} + \bar{A}C = X = A \oplus C$$

Taking complement both sides :-

$$A\bar{C} + \bar{A}C = \bar{X}$$

$$\bar{A}\bar{C} \cdot \bar{A}C = \bar{X}$$

$$(\bar{A} + C)(A + \bar{C}) = \bar{X}$$

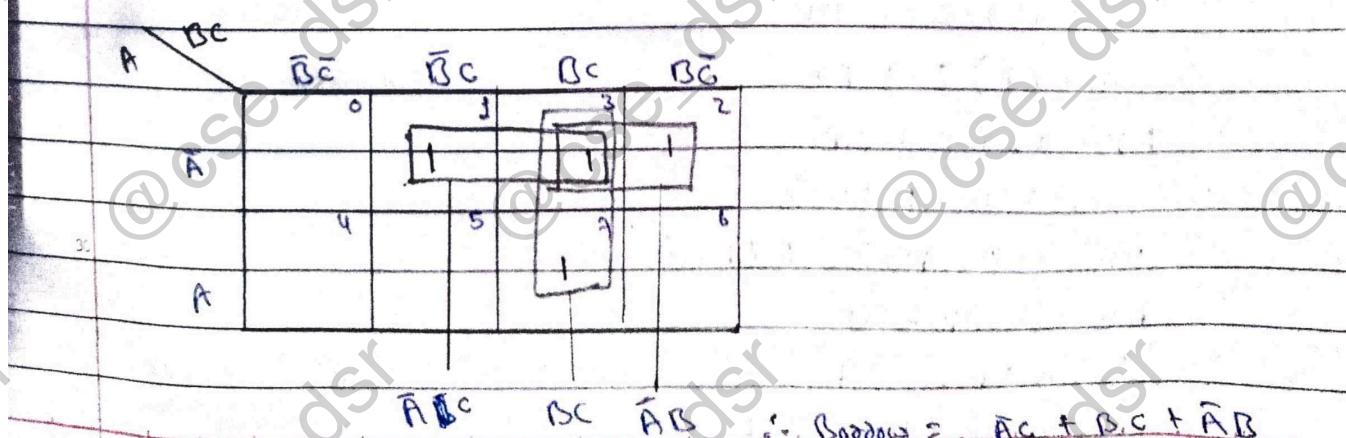
$$\bar{A}A + \bar{A}C + AC + C\bar{C} = \bar{X}$$

$$\bar{A}\bar{C} + AC = \bar{X}$$

$$\text{Sub} = \bar{B}X + B\bar{X} = B \oplus X = B \oplus A \oplus C.$$

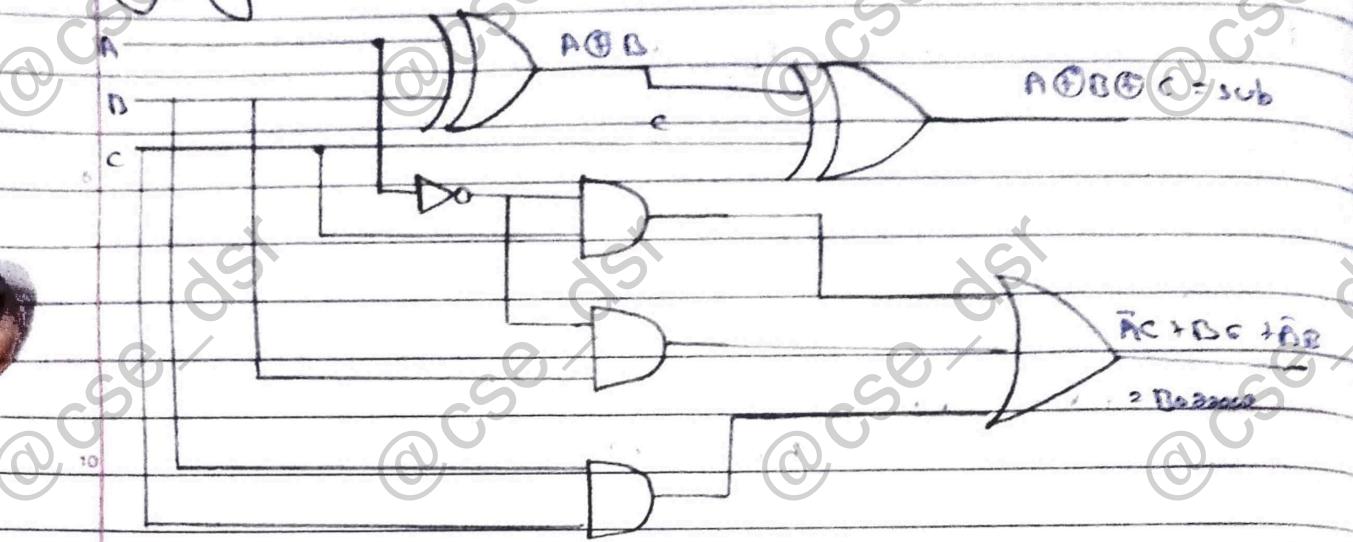
$$\text{Sub} = A \oplus B \oplus C.$$

K-map of Borrow:-

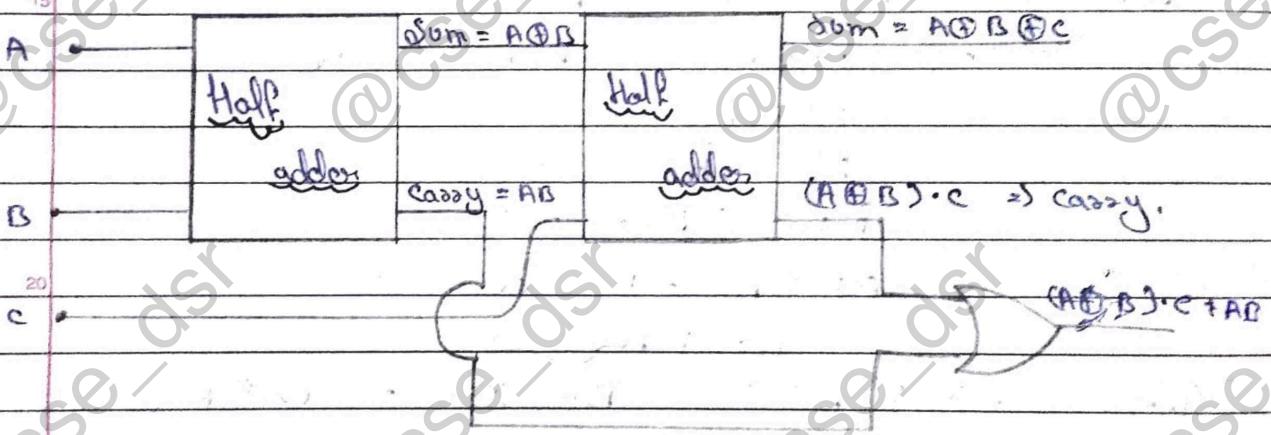


$$\therefore \text{Borrow} = \bar{A}C + B.C + \bar{A}B$$

Logic Circuit Diagram:-



Making full adder with the help of half adder.



Solving:-

$$\begin{aligned}
 & (A \oplus B) \cdot C + AB \\
 \Rightarrow & (\bar{A}B + A\bar{B})C + AB \\
 \Rightarrow & \bar{A}BC + A\bar{B}C + AB \\
 \Rightarrow & \bar{A}BC + A(B + \bar{B}C) \\
 \stackrel{(2)}{\Rightarrow} & \bar{A}BC + A(B + C) \\
 \Rightarrow & \bar{A}BC + AB + AC \\
 \Rightarrow & B(A + \bar{A}C) + AC \\
 \Rightarrow & B(CA + \bar{A})(A + C) + AC \\
 \Rightarrow & AB + BC + AC
 \end{aligned}$$