

THE 5th INTERNATIONAL CONFERENCE
ON VLSI, COMMUNICATION AND SIGNAL PROCESSING

VICAS 2022

October 14-16, 2022

@MNNIT ALLAHABAD

CALL FOR PAPER

“On VLSI, Communication and Signal Processing”

Selected & presented papers from the conference is to be published by **Springer** as a proceedings book volume indexed in **SCOPUS** database



Scopus®

Organised By:

The Department of Electronics and Communication Engineering,
Motilal Nehru National Institute of Technology Allahabad,
Prayagraj, Uttar Pradesh, India, 211004.



Motilal Nehru National Institute of Technology (MNNIT) ALLAHABAD

(October 14-16, 2022)

Conference Website: <http://www.mnnit.ac.in/vcas2022>

Organized by: Electronic and Communication Engineering Department, MNNIT Allahabad



ORGANIZING COMMITTEE

Chief Patron: Prof. Rama Shanker Verma, Director, MNNIT Allahabad

Patron: Prof. Rajeev Tripathi, MNNIT Allahabad

General Chair: Prof. R. K. Nagaria, MNNIT Allahabad

Conference Chair: Prof. R.A. Mishra, MNNIT Allahabad

Program Chair: Prof. V. S. Tripathi, MNNIT Allahabad

Program Co-Chair-I: Dr. Vinay Kumar, MNNIT Allahabad

Program Co-Chair-II: Dr. Anand Sharma, MNNIT Allahabad

Keynote Chair: Prof. Basant Kumar, MNNIT Allahabad

Tutorial Chair: Prof. Vijaya Bhadauria, MNNIT Allahabad

Workshop Chair: Prof. V. K. Srivastava, MNNIT Allahabad

Workshop Co-Chair: Dr. Shweta Tripathi, MNNIT Allahabad

Industrial Chair: Prof. Haranath Kar, MNNIT Allahabad

Public Relation Chair: Dr. Amit Dhawan, MNNIT Allahabad

Printing & Publication Chair: Dr. Manish Tiwari, MNNIT Allahabad

Finance Chair: Dr. Sanjeev Rai, MNNIT Allahabad

Sponsorship Chair: Dr. V. Krishna Rao K., MNNIT Allahabad

Sponsorship Co-Chair: Dr. Sumit Kumar Jha, MNNIT Allahabad

Event Management Chair: Er. Asim Mukherjee, MNNIT Allahabad

Event Management Co-Chair: Dr. Smriti Agarwal, MNNIT Allahabad

Publicity Chair: Dr. Arvind Kumar, MNNIT Allahabad

Hospitality Chair: Dr. Rajeev Gupta, MNNIT Allahabad

Registration Chair: Dr. Arun Prakash, MNNIT Allahabad

Registration Co-Chair: Dr. P. Karuppanan, MNNIT Allahabad

Website Chair: Dr. S. K. Gupta, MNNIT Allahabad

Organizing Secretary: Dr. Yogendra Kumar Prajapati, MNNIT Allahabad

TRACK CHAIRS

VLSI	Dr. Shweta Tripathi (MNNIT-Allahabad)
Communication	Dr. Anand Sharma (MNNIT Allahabad)
Signal Processing	Dr. Sumit Kumar Jha (MNNIT Allahabad)

REGISTRATION FEE

Category	Indian Delegates	Foreign Delegates
Student	INR 5,000/-	USD 250
Faculty	INR 8,000/-	USD 400
Industry	INR. 10,000/-	USD 500
Participation Only (outside MNNIT Allahabad Delegates)	INR 4,000/-	USD 200

50% Discount in registration fees may be offered to communicating author for the registration of the Second Paper.

Early bird Rs. 1000/-

CONTACT INFORMATION

EMAIL: vcas.eced@mnnit.ac.in
PHONE: +91 532 227 1451
ADDRESS: Organizing Secretary, VCAS 2022
 ECED, MNNIT Allahabad, Prayagraj-211004,
 UP, India
WEBSITE: <http://www.mnnit.ac.in/vcas2022>

ABOUT THE CONFERENCE

The 5th IEEE International Conference on **VLSI, Communication and Signal Processing (VCAS 2022)** in will be held in the Sangam city of Uttar Pradesh, India, during October 14-16, 2022, hosting by the Department of Electronics & Communication Engineering, Motilal Nehru National Institute of Technology Allahabad, Prayagraj, India in association with Springer. The conference aims to offer a platform for the researchers, academicians, individuals working in the industry and students to showcase their research innovations and get recognition. VCAS 2022 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

MNNIT-VCAS Conferences Springer Publication Links:

VCAS 2018: <https://www.springer.com/gp/book/9789813297746>

VCAS 2019: <https://www.springer.com/gp/book/9789811568398>

VCAS 2020: <https://www.springer.com/gp/book/9789811627606>

VCAS 2021: [Under Process](#)

IMPORTANT DATES

<i>Paper Submission Starts</i>	March 05, 2022
<i>Paper Submission Deadline</i>	June 30, 2022
<i>Acceptance Notification</i>	September 20, 2022
<i>Start of Registration</i>	September 20, 2022
<i>Early Bird Registration Deadline</i>	September 25, 2022
<i>End of Registration</i>	October 06, 2022
<i>Camera Ready Paper Submission Deadline</i>	October 06, 2022
<i>Conference Date</i>	October 14-16, 2022

EASY CHAIR LINK FOR PAPER SUBMISSION

<https://easychair.org/conferences/?conf=vcas2022>

PUBLICATION

Selected and presented papers from the conference is to be undergo quality and plagiarism checks by Springer. The papers that pass these checks are to be published by Springer as proceedings in the book series Lecture Notes in Electrical Engineering (LNEE) (*Scopus and Web of Science indexed*).

TRACKS

VLSI

Analog, Digital & Mixed Signal Design, Testing & Testable Circuit, Low-Power Design, Packaging & Interconnect, 2D material-based Devices, MEMS & NEMS design and Technology, Devices Modeling and Process simulation, Thin Film, Ultra-High-Speed Transistors, HEMT/HBT, Reconfigurable Architecture Design, Power Devices and Reliability, Artificial Intelligence (Process & Device), Internet of Things (Process & Device), Emerging Technology

Communication

Communication Theory and Applications, Optical Networks and Systems Microwave Communications, Algorithms and Techniques for 5G and beyond Wireless and Ad Hoc Networks, IOT and Sensor Networks, Antenna Design for Communication Systems and Networks, Vehicular Communications and Networks, Wireless Modeling/Algorithms and Simulation, Cognitive Radio and Spectrum Management, Adaptive Communication Systems and Networks, Intelligent Protocols and Designing for Communication Systems and Networks, Computer Communication.

Signal Processing

Signal Processing: Theory, Methods and Applications, Signal Processing Systems and Architectures, Signal Processing Algorithms, Image/Video and Multidimensional Signal processing, Multimedia Signal Processing, Biomedical Signal Processing and Bioinformatics, Signal Processing for Communication and Networking, Signal Processing for Machine Learning and IoT, Gnostic Signal Processing, Coding and Compression, Signal Processing for Smart systems, Sparse Signal Processing, Signal processing for VLSI.

* The Topics of interest include but are not limited to the above-mentioned Tracks