

THE 5<sup>th</sup> INTERNATIONAL CONFERENCE  
ON VLSI, COMMUNICATION AND SIGNAL PROCESSING

# VICAS 2022

October 14-16, 2022

@MNNIT ALLAHABAD

## CALL FOR PAPER

“On VLSI, Communication and Signal Processing”

Selected & presented papers from the conference is to be published by **Springer** as a proceedings book volume indexed in **SCOPUS** database



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Organised By:

The Department of Electronics and Communication Engineering,  
Motilal Nehru National Institute of Technology Allahabad,  
Prayagraj, Uttar Pradesh, India, 211004.





# Motilal Nehru National Institute of Technology (MNNIT) ALLAHABAD

(October 14-16, 2022)

Conference Website: <http://www.mnnit.ac.in/vcas2022>

Organized by: Electronic and Communication Engineering Department, MNNIT Allahabad



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## TRACK CHAIRS

<b>VLSI</b>	Dr. Shweta Tripathi (MNNIT-Allahabad)
<b>Communication</b>	Dr. Anand Sharma (MNNIT Allahabad)
<b>Signal Processing</b>	Dr. Sumit Kumar Jha (MNNIT Allahabad)

## REGISTRATION FEE

Category	Indian Delegates	Foreign Delegates
Student	INR 1,500/-	USD 250
Faculty	INR 3,000/-	USD 400
Industry	INR. 10,000/-	USD 500
Participation Only (outside MNNIT Allahabad Delegates)	INR 2,000/-	USD 200

50% Discount in registration fees may be offered to communicating author for the registration of the Second Paper.

## CONTACT INFORMATION

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**WEBSITE:** <http://www.mnnit.ac.in/vcas2022>

## ABOUT THE CONFERENCE

The 5<sup>th</sup> IEEE International Conference on **VLSI, Communication and Signal Processing (VCAS 2022)** in will be held in the Sangam city of Uttar Pradesh, India, during October 14-16, 2022, hosting by the Department of Electronics & Communication Engineering, Motilal Nehru National Institute of Technology Allahabad, Prayagraj, India in association with Springer. The conference aims to offer a platform for the researchers, academicians, individuals working in the industry and students to showcase their research innovations and get recognition. VCAS 2022 aspires to bring together research professional fraternity for the discussions and exchange of information on advances in VLSI, Signal Processing and Communications.

## MNNIT-VCAS Conferences Springer Publication Links:

**VCAS 2018:** <https://www.springer.com/gp/book/9789813297746>

**VCAS 2019:** <https://www.springer.com/gp/book/9789811568398>

**VCAS 2020:** <https://www.springer.com/gp/book/9789811627606>

**VCAS 2021:** [Under Process](#)

## IMPORTANT DATES

<b>Paper Submission Starts</b>	<b>March 05, 2022</b>
<b>Paper Submission Deadline</b>	<b>June 30, 2022</b>
	<b>July 20, 2022</b>
	<b>August 05, 2022</b>
	<b>August 20, 2022</b>
<b>Acceptance Notification</b>	<b>September 20, 2022</b>
<b>Start of Registration</b>	<b>September 20, 2022</b>
<b>End of Registration</b>	<b>October 06, 2022</b>
<b>Camera Ready Paper Submission Deadline</b>	<b>October 06, 2022</b>
<b>Conference Date</b>	<b>October 14-16, 2022</b>

## EASY CHAIR LINK FOR PAPER SUBMISSION

<https://easychair.org/conferences/?conf=vcas2022>

## PUBLICATION

It is planned to publish the peer reviewed and selected papers of conference as proceedings with Springer in their prestigious "Lecture Notes in Electrical Engineering" series (<https://www.springer.com/series/7818>). For detailed instructions for author and editors of conference proceedings, kindly visit the following link: <https://www.springer.com/us/authors-editors/conference-proceedings>. Select papers from the conference will be published by Springer as a proceedings book volume. Springer will conduct quality checks on the accepted papers and only papers that pass these checks will be published. Springer Nature does not charge any money for publication of Non-Open Access content. Abstracts/extended abstracts and short papers (less than 4 pages) are not considered for publication.

## TRACKS

### VLSI

Analog, Digital & Mixed Signal Design, Testing & Testable Circuit, Low-Power Design, Packaging & Interconnect, 2D material-based Devices, MEMS & NEMS design and Technology, Devices Modeling and Process simulation, Thin Film, Ultra-High-Speed Transistors, HEMT/HBT, Reconfigurable Architecture Design, Power Devices and Reliability, Artificial Intelligence (Process & Device), Internet of Things (Process & Device), Emerging Technology

### Communication

Communication Theory and Applications, Optical Networks and Systems Microwave Communications, Algorithms and Techniques for 5G and beyond Wireless and Ad Hoc Networks, IOT and Sensor Networks, Antenna Design for Communication Systems and Networks, Vehicular Communications and Networks, Wireless Modeling/Algorithms and Simulation, Cognitive Radio and Spectrum Management, Adaptive Communication Systems and Networks, Intelligent Protocols and Designing for Communication Systems and Networks, Computer Communication.

### Signal Processing

Signal Processing: Theory, Methods and Applications, Signal Processing Systems and Architectures, Signal Processing Algorithms, Image/Video and Multidimensional Signal processing, Multimedia Signal Processing, Biomedical Signal Processing and Bioinformatics, Signal Processing for Communication and Networking, Signal Processing for Machine Learning and IoT, Gnostic Signal Processing, Coding and Compression, Signal Processing for Smart systems, Sparse Signal Processing, Signal processing for VLSI.

\* The Topics of interest include but are not limited to the above-mentioned Tracks