serial communication BOSI can able to send or receive the data from the outside world The transmission of data (receiving) sending) from Mc to the I/o devices -> compressionication (Social to possible transmission of date (Social) MC + Roceive devices 1861) & commenication> parallel to Parallal Lone by one y Lall at some time? Social as many obdata Kingce line for long distance commonication, social comm is best boos of somplicity (single line) Types of communication links 1. Simplex D. Half dopage Ef:-composee -> prointer Single Transmitter line Transmit | Receive Sinte Roceive /bransmit Eg: waskie-taller Lone action at a time Receive Transmitter & Peccing Ef: phone Types of Sexial communication 1. Synchronous - Tx & Rx are syn (same dack) 2. Asynchronous - The 2 Rx are not in sinc (both have Separate Clock)

Synchronous	Asynchronous,
- transmitted & received aske Synchroni 224	- not synchronized
- Share a common clock	- different
- tiest sympocheracter is sent	- start bit, alara,
St stata	SMPH
- block of allata Tapol pate	- single bit is transfered at a time
transmittee	Roceive
Clock	
transmiffer Data Data Start Start Clock Start Start data	
4.	y 6°+
Bacod rate /transfer rate. Bits transmitted per - Asynchronous -) steet Seprohorous -> Ex: 9600 /dara po	scord. 3, stop bit different clock elphal
1 bit = 1 second = 0.	104 ms
2051 Social commonication	
- coopposes full dopcex seria	D 10
-3 SFR SBOF ASPLIA	es bottop
SBOF:	
- Hansmission of delta, it a receive data, 180 it need 1801 data, butter.	can transmit as well as
- 2 bit register	
- TXD line - bransmission	
- RXD Pin - Receiving	

- 8 bit register - mode selection, serial post interpret bie (TIS, RI), 9th data bit too bransmission sy reception. D) D6 D5 D4 D8 DD D1 D6
RI), 9th datas bit too transmission of reception.
DO TO DE DE DE DE DE
00 00 05 04 D3 D0 D1 0-
and SMI SM2 REN TBS PBS TI RI
SWD 3
00 - male 0 - 8 bit shift register made
01 - moder - 8 bit UART
10 -modez - 9bit WART
1 - mode3 - 9 bit OART
Son -> moltiprocessor communication bit
mode 2, SM2=1, Then it erables -
mose s,
L'erable social reception
and gith hit that is transmitted in mode 292
RBB - " " " Received " "
9th bir -> steet/stop ber
TI - Transout Interret Flag TI, RI = 0
RI - Received Interrept Flag Reset
PCON:
- 8 bit register
- Control to power of 8051 MC
- 1 bit in Front is used too search commonication
D7 DE D5 D4 D8 D2 D1 D0
SHOD GFI GFO PP IPL
SMOD > Sevicel mode Levicel rate modify bits Levicel rate modify bits Levicel rate modify bits

It snop=1 - doobce board mate veing times, SMOD=0 - USED times - | balled rate Ic modes in 805) made 0 -> 8 bit shift regrister made clock eighal data Rignal data is mansmitted of received theough RND Ph TXD is used too clock right. Bacod rocate = 1 of ceach froughton to a transmission 12 mode 1 -> 8 bit wars SBUF -> LO bir toll duplex branscrive. ten bits _ 1 start bit - 8 data bit 1 STOP bit TI/RI will set once transmission / receiption is an Back late = [25Mop, 82] x Times-1 Overtow they Avariable> = [2 snop | 182] x [DSWII] x (256-(7#17))

times | orall
titles mode 2 -> 11 bits DART 11 bit - 1 Start bit - 8 doctor by

SCON (FBB, RBB) 2 stop bit 1 postatoroable 9th data 4+ 1 Chop bit back rate = [2 smod & J & Oscillator clock frag

bond rate is calculated as mode 1.

mode 3 -> 11 bite

11 bit =