Problem 1 – 20 points total

Simplify the following Boolean expression by applying the properties of Boolean algebra, until it matches the simplified form given. sequence as well as the name of the property used at that step. sequence as well as the name of the answer is given, you will be graded on properly expressing the equations at each step in the Use the rule numbers, law and theorem names given on the attached sheet to indicate which transformations you have applied in

Note: in the following, I have used the prime notation, x', as used in the textbook to denote the not operation and shown as \bar{x} in class.

a) Show that xy + x'y + y'(y' + z) can be simplified to 1. Hthistory of state of complements

of (1) + y' + zy' complements

of (1) + y' + zy' complements

of (1) + y' + zy' complements (10 pts)

(10 pts)

INVOIMAN

Complements & idempolency

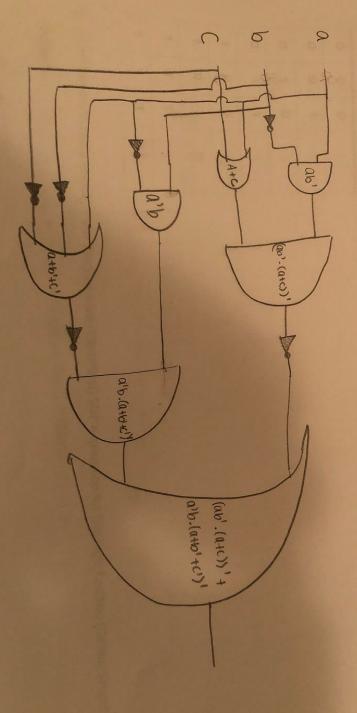
a'bta'b

(conventry

d+b

Problem 2 – 30 points total

a) Problem 1, part b, draw the combinational logic circuit diagram using the non-simplified equation:



(10 pts)

b) Form the truth table that fully describes the operation of the circuit from part a, and enter the values in the table provided below.
(10 pts) (10 pts)

Truth Table

1	-	-	1	0	0	0	0	۵	
-	-		0			0	0	6	
-	0	-	0	-	0	-	0	0	
_		0	0	-	-	-	-	Output	

c) From the truth table above, form the Standard (Canonical) SOP expression for this logic function (Do Not Simplify): (5 pts) output = abc + abc + abc + abc + abc + abc

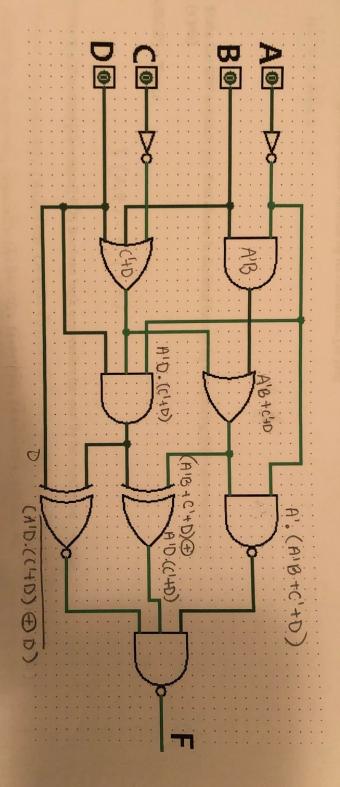
DAA

d) From the truth table above, form the Standard (Canonical) POS expression for this logic function (Do Not Simplify): (5 pts)

output= (a+b+c). (atb+c)

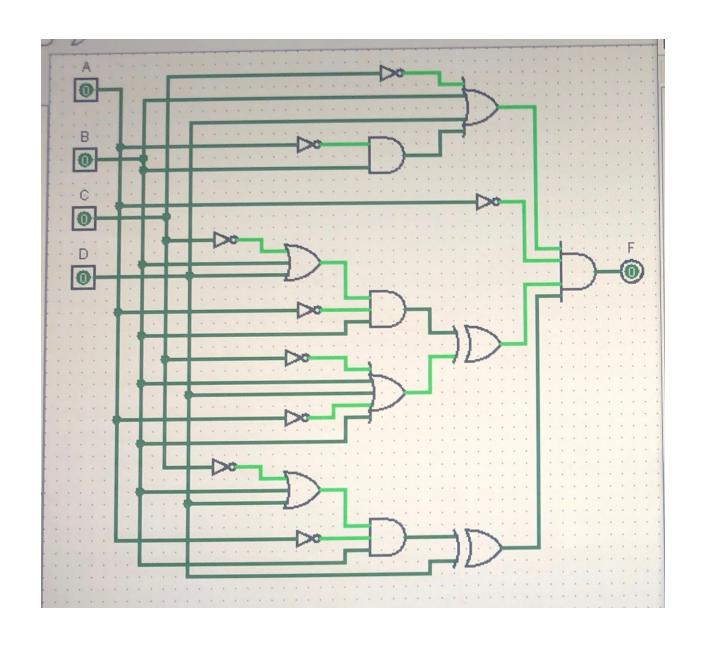
2b.
$$(a.b^1.(a+c))^1 + (a^1.b).(a+b^1+c^1)^1$$

 $(0,0,0) = [(0.1).(a+c)]^1 + (1.0).(a+1+1)^1$
 $(0,0,0) = [0.1.(a+1)]^1 + (1.0).(a+1+0)^1$
 $(0,1,0) = [0.0.(a+0)]^1 + (1.0).(a+0+1)^1$
 $(0,1,1) = [(0.0).(a+1)]^1 + (0.1).(a+0+0)^1$
 $(1,0,0) = [(1.1).(1+0)]^1 + (0.1).(1+1+1)$
 $(1,0,0) = [(1.1).(1+0)]^1 + (0.0).(1+1+0)$
 $(1,1,0) = [(1.0).(1+0)]^1 + (0.0).(1+1+0)$
 $(1,1,0) = [(1.0).(1+0)]^1 + (0.0).(1+0+0)^1$
 $(1,1,0) = [(1.0).(1+0)]^1 + (0.0).(1+0+0)^1$



a) State the function F implemented by this circuit as a Boolean expression. (There are many forms and simplifications for F. Just write down the one that most directly expresses the function of this circuit, don't simplify it). (10 pts)

F(A,B,C,D)= TA,B,CD F(A,B,C,D,E,F,G,H,I,I,I,K,L,M,N)= TT (0,2,4,5,6,7,8,9,10,11,12,13,14,15)	e) State the POS solution you find above in the decliniar formación (5 pts) below:	the decimal format by completing the statement		(A+B4CHD). (A+B+C+D). (A+B+C+D). (A+B+C+D). (A+B+C+D). (A+B+C+D).		4	d) State the Standard (Canonical) POS Boolean logic expression formed by its maxterms. (10 pts)	F(A,B)= 5, (1,3)		$F(A,B,C,D) = \sum_{A,B,C,D}$	(5 pts)	c) State the SOB solution was find the solut		1-54	O-5 4		SOP Boolean logic expression formed by its mintered (Carty)
- "	7	¥	ىن	2		0	-	-	0	0	0	0	0	0	0	0	A
	-							0	1	1	1	4	0	0	0	0	00
	-	-	-		0	0	0)			0	0	1	1	0	0	0
	1	-	0	0	-	-	0	0	-				-	0	1	0	0
	1	0	-	0	-	0	_	0	-	0	10	0	_	0	, -	-0	77
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Problem 4 – 10 points total

Write the Verilog code (use structural modeling) for the combinational logic diagram in problem 3 having inputs A, B, C, & D and output F.

module PROPLEM3 (F, A,B,C,D);

Output F;

Input A,B,C,D;

Ossign F = (~A) | D | (~B&C);

Modimodule.