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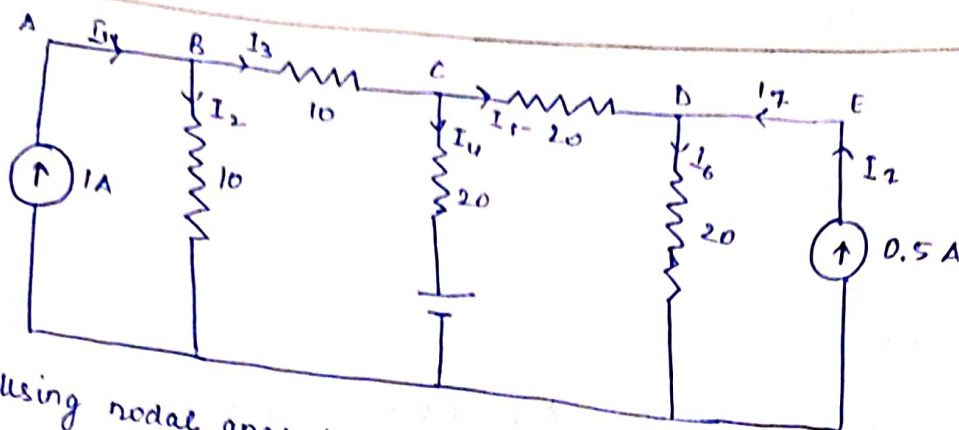
Reg No: 20BCE10093

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Slot/Code: EEE1001/E11+E12+E13

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Using nodal analysis,

we will apply KCL on B, C and D nodes.

Let the voltages be  $V_B$ ,  $V_C$  and  $V_D$  respectively.

$$I_1 = I_2 + I_3$$

$$1 = \frac{V_B}{10} + \frac{V_B - V_C}{10}$$

$$\therefore 2V_B - 10 - V_C = 0 \rightarrow (1)$$

\* now  $I_3 = I_4 + I_5$  at node C

$$\frac{V_B - V_C}{10} = \frac{V_C - 12}{20} + \frac{V_C - V_D}{20}$$

$$= 2V_B - 4V_C + V_D + 12 = 0 \rightarrow (2)$$

\* now,  $I_5 + I_7 = I_6$

$$\frac{V_C - V_D}{20} + 0.5 = \frac{V_D}{20}$$

$$V_C - V_D + 10 = V_D$$

$$V_C + 10 - 2V_D = 0 \rightarrow (3)$$

From the above eq<sup>n</sup>s, 1, 2, 3,

(2)

$$2V_B - 10 - V_C = 0$$

$$2V_B - 4V_C + V_D + 12 = 0$$

$$V_C - 2V_D + 10 = 0$$

$$V_D = \frac{V_C + 10}{2}$$

$$\Rightarrow 2V_B = 10 + V_C$$

$$\therefore \text{in } (2), \quad 10 + V_C - 4V_C + \frac{V_C + 10}{2} + 12 = 0$$

$$= 10 - 3V_C + \frac{V_C}{2} + 5 + 12 = 0$$

$$= 27 + \frac{V_C}{2} - 3V_C = 0$$

$$= 27 = 3V_C - \frac{V_C}{2} = \frac{6V_C - V_C}{2} = \frac{5V_C}{2}$$

$$= \frac{27 \times 2}{5} = V_C = 10.8 \text{ V}$$

$$\therefore 2V_B = 10 + V_C = 10 + 10.8 = 20.8$$

$$V_B = 10.4 \text{ V}$$

$$V_D = \frac{10.8 + 10}{2} = 10.4 \text{ V}$$

$$I_2 = \frac{V_B}{10} = \frac{10.4}{10} = 1.04 \text{ A}$$

$$I_3 = \frac{V_B - V_C}{10} = \frac{10.4 - 10.8}{10} = \frac{-0.4}{10} = -0.04 \text{ A}$$

$$I_6 = \frac{V_D}{20} = \frac{10.4}{20} = 0.52 \text{ A}$$

$$I_5 = \frac{V_C - V_D}{20} = \frac{10.8 - 10.4}{20} = \frac{0.4}{20} = 0.02 \text{ A}$$

$$I_6 = I_7 + I_5 = 0.5 + 0.02 = 0.52 \text{ A}$$

$$I_2 = I_1 + I_3 = 1.0 + 0.04 = 1.04 \text{ A}$$

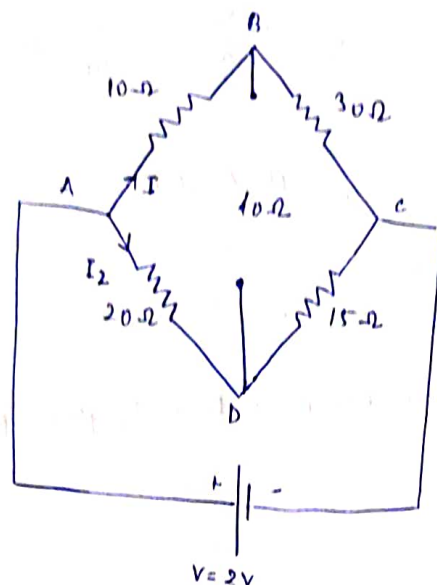
4

Now,  $I_4 + I_r = I_3$

$\therefore I_4 = I_3 - I_r = 0.02 \text{ A}$

$= (0.04 - 0.02) =$

2 (a)



\* EMF of 2V

To use Thevenin's theorem, we need to remove the load  $40\Omega$  and calculate  $V_{BD}$  and  $V_{OC}$ .

$$V_{BC} = \frac{30 \times V}{10 + 30} = 30 \text{ I}_1$$

$$= \frac{30 \times 2}{40} = \frac{60}{40} = 1.5 \text{ V}$$

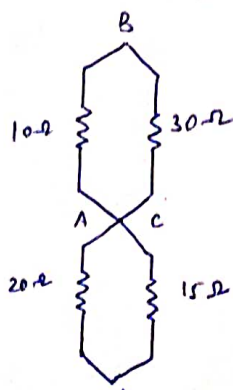
$$V_{DC} = 15 I_2 = \frac{15 \times V}{20 + 15}$$

$$= \frac{15 \times 2}{20 + 15} = \frac{30}{35} = \frac{6}{7} \text{ V}$$

Potential difference between B & D  $\Rightarrow V_B - V_D$

$$= \frac{2}{2} - \frac{6}{7} = \frac{7}{14} \text{ V}$$

To find  $R_{TH}$ , we have to short circuit the voltage source and solve.



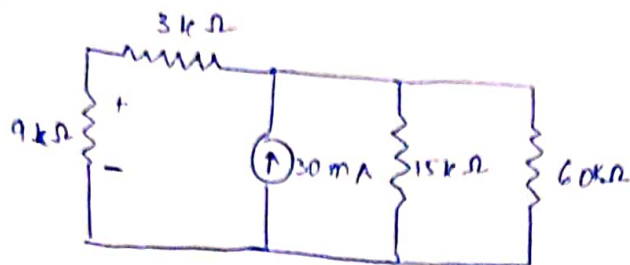
④

$$R_{TH} = \frac{20 \times 15}{20 + 15} + \frac{10 \times 30}{10 + 30} = \frac{300}{35} + \frac{300}{40} = \frac{225}{14} \Omega$$

$$\therefore I_L = \frac{V_{TH}}{R_{TH} + R_L} = \frac{9}{\frac{225}{14} + 40} = 11.457 \text{ mA}$$

B is at a higher potential than point D,  $I_L$  flows from B to D.

3



$$I_1 = I \cdot \frac{R_{eq}}{12 k\Omega} \quad I_2 = I \cdot \frac{R_{eq}}{15 k\Omega}$$

$$\frac{1}{R_{eq}} = \frac{1}{60} + \frac{1}{15} + \frac{1}{12} = \frac{1}{60} + \frac{4}{60} + \frac{1}{12} = \frac{5}{60} + \frac{1}{12} = \frac{5}{60} + \frac{5}{60} = \frac{10}{60} = \frac{1}{6} = 6 k\Omega$$

$$I_1 = 30 \text{ mA} \times \frac{1}{2} = 15 \text{ mA}$$

$$I_2 = 30 \text{ mA} \times \frac{2}{5} = 12 \text{ mA}$$

(i)

$$V_1 = I_1 \times 3 k\Omega = 15 \text{ mA} \times 3 k\Omega = 45 \text{ V}$$

$$V_2 = I_2 \times 15 k\Omega = 12 \text{ mA} \times 15 k\Omega = 180 \text{ V}$$

(ii)

$$P_{3k} = I_1^2 \times 3 k\Omega = \frac{V_1^2}{3 k\Omega} = \frac{15^2 \times 45}{3 \times 1000} = 0.675 \text{ W}$$

$$P_{15k} = I_2^2 \times 15 k\Omega = \frac{V_2^2}{15 k\Omega} = \frac{60^2 \times 12}{15 \times 1000} = 2.16 \text{ W}$$

(iii)

$$P_{30mA} = -I \times V_2 = -30 \times 180 = -5400 \text{ mW} = -5.4 \text{ W}$$

4 Ratio = 50:5

Input voltage =  $V = 220 \sin \omega t$

diode forward  $R \approx 25 \Omega$

Load Resistance =  $1.4 \text{ k}\Omega$

To find, transformer  $R = 1.1 \text{ k}\Omega$ , (a) RMS value of load

(b) Rectification efficiency

(c) Ripple factor

Here, input  $V_m = 220$

turn ratio = 50:5

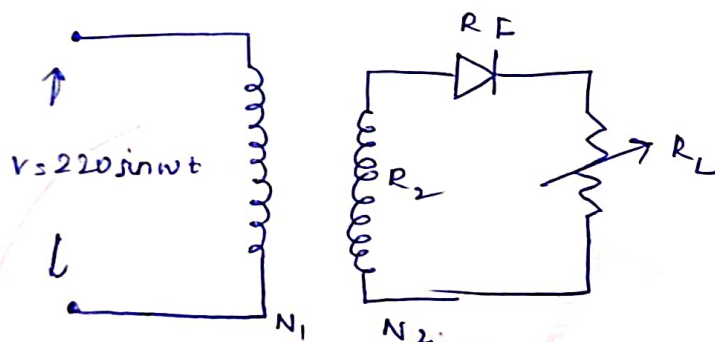
$\therefore$  output  $V_m = \frac{220}{\frac{50}{5}} \times 1 = 22 \text{ V}$

$I_m = \frac{V_m}{R_2 + R_F + R_L}$

$R_2$  = winding resistance of transformer

$R_F$  = Forward resistance

$R_L$  = load resistance



$I_m = \frac{22}{1.1 \times 1000 + 25 + 1.4 \times 1000} = \frac{22}{1100 + 25 + 1400}$

$= \frac{22}{2525} \text{ A} = 0.0087 \text{ A} = 8.7 \text{ mA}$

$I_{rms} = \frac{I_m}{2} = \frac{8.7}{2} = 4.35 \text{ mA} = \frac{11}{2525} \text{ A}$

$I_{dc} = \frac{I_m}{\pi} = \frac{22}{2525} \times \frac{7}{22} = \frac{7}{2525} \text{ A} = 2.7 \text{ mA}$



(7) (8)

$$\text{Output dc power} = I_{dc}^2 R_L = (0.0027 \times 0.0027) \times 1400$$

$$= 0.010 \text{ W}$$

$$\text{AC input} = I_{rms}^2 (R_s + R_F + R_L)$$

$$= \frac{11}{2525} \times \frac{11}{2525} \times \cancel{2525} = 0.0479 \text{ W}$$

$$\text{rectifier efficiency} = \frac{\text{Output dc power} \times 100}{\text{Input ac power}}$$

$$= \frac{0.010 \times 100}{0.048} = \frac{10}{48} \times 100 = 20.83 \%$$

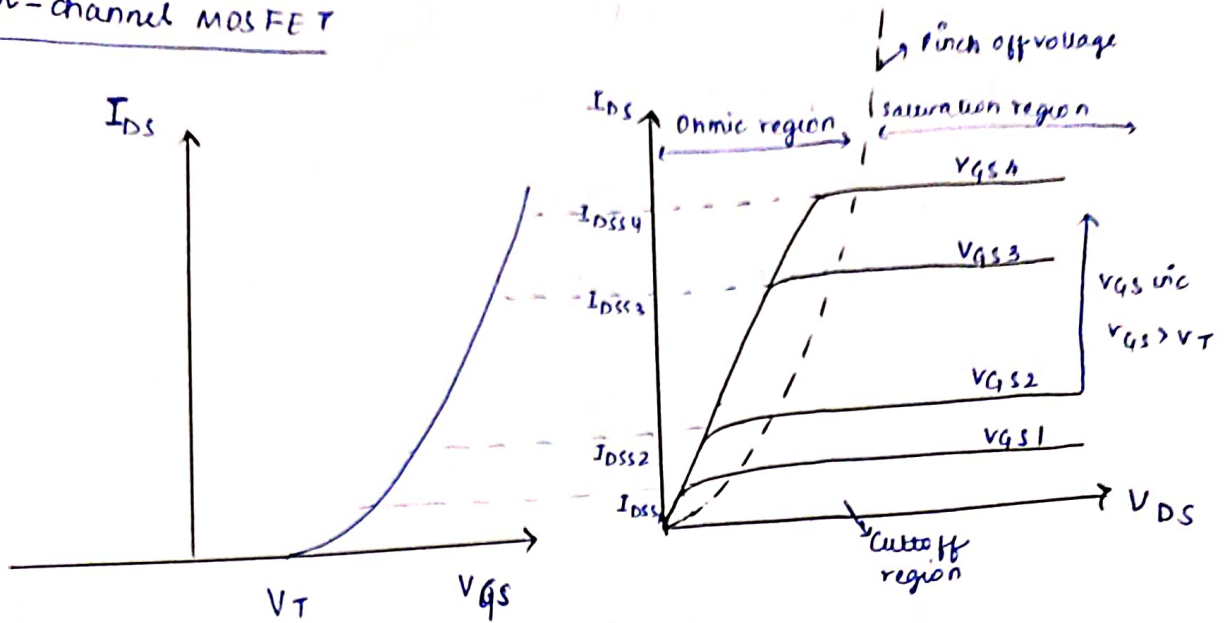
$$\text{Ripple factor} = r = \sqrt{\left(\frac{I_{rms}}{I_{dc}}\right)^2 - 1} = \sqrt{\left(\frac{\frac{11}{2525}}{\frac{7}{2525}}\right)^2 - 1}$$

$$= \sqrt{\left(\frac{11}{7}\right)^2 - 1} = \sqrt{\frac{121}{49} - 1}$$

$$= \sqrt{\frac{121-49}{49}} = \sqrt{\frac{72}{49}} = \frac{6\sqrt{2}}{7} = \frac{8.48}{7} = 1.212$$

5 (a)

# n-channel MOSFET



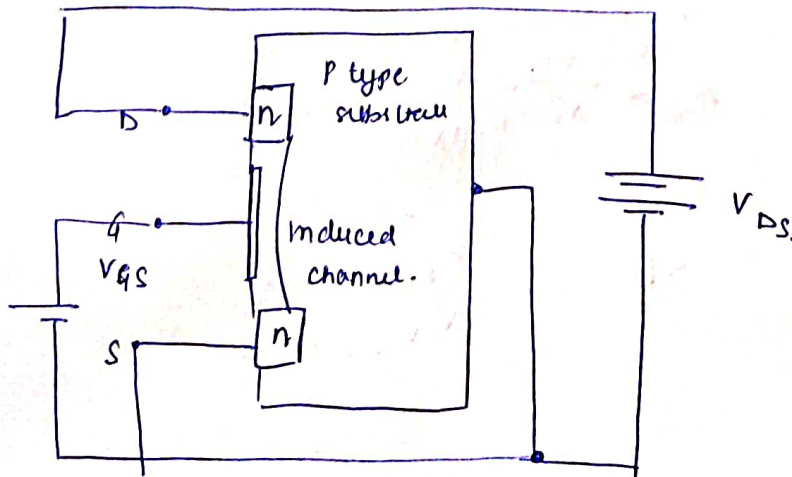
\* The figure shows drain to source current  $I_{DS}$  versus gate to source voltage  $V_{GS}$  of n-channel enhancement type MOSFETs.

\* It is pretty clear that the current will be zero until  $V_{GS}$  exceeds the threshold voltage  $V_T$ . It happens because under this state, the device will be void of channel which will be connecting the drain and source. During this condition, if  $V_{DS}$  increase, no current will flow as shown by  $I_{DS}$  v/s  $V_{DS}$  graph. As a result, this state represents the cutoff region of MOSFET's operation.

Once  $V_{GS}$  crosses  $V_T$ , the current increases:  $I_{DS}$  increases initially in the ohmic region and then saturates.

By the graph,  $I_{DSS2}$  is greater than  $I_{DSS1}$  as  $V_{GS2} > V_{GS1}$  and a similar pattern is observed further.

Pinch off voltage's locus is also visible, from which  $V_p$  increases with an increase in  $V_{GS}$ .



N channel MOSFET



5 (b)

AC input signal =  $\pm 50 \text{ mV}$ 

(9)

$$\beta_{dc} = 90, \text{ dc current} = 120$$

$$V_B = 0.7 \text{ V}, I_B = 17 \text{ mA}$$

$$V_i = \pm 50 \text{ mV}, I_B = \pm 7 \text{ mA}$$

$$\text{DC base current } I_B = 17 \text{ mA}$$

$$V_B = 0.7 \text{ V}$$

$$\beta_{dc} = 90$$

$$I_C = \beta_{dc} I_B = 90 \times 17 \text{ mA} = 1530 \times 10^{-3} \text{ A}$$

The collector voltage  $V_{CE}$  is\*  $R_L$  and  $V_{CC}$  missing

$$V_{CE} = V_{CC} - I_C R_L$$

$$V_{CE} = V_{CC} - 1530 \times 10^{-3} \times R_L$$

=

$$\text{AC base current} = I_B = \pm 7 \text{ mA for } V_i = \pm 50 \text{ mV}$$

$$I_C = \beta_{ac} I_B = 120 \times 7 \text{ mA}$$

$$I_C = \pm 840 \text{ mA}$$

(i)

AC output voltage across load resistance.

$$V_O = I_C R_L = \pm 840 \times 10^{-3} \times R_L$$

\*  $R_L$  is missing

=

(ii)

AC voltage amplification factor

$$A_c = \frac{V_O}{V_i} = \frac{V_O}{\pm 50 \times 10^{-3}} = A_c$$

(iii)

$$V_{CE} = V_{CC} - I_C R_L$$

\*  $R_L$  is missing\*  $V_{CC}$  is missing

=

— X — X — X —