



Elektrobit

# EB tresos<sup>®</sup> AutoCore Generic 8 RTE documentation

release notes update for the Rte module

product release 8.8.7



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# 1. Overview

This document provides you with the release notes to accompany an update to the `Rte` module. Refer to the changelog [Section 2.1, “Change log”](#) for details of changes made for this update.

## Release notes details

- ▶ EB tresos AutoCore release version: 8.8.7
- ▶ EB tresos Studio release version: 29.2.1
- ▶ AUTOSAR R4.0 Rev 3
- ▶ Build number: B602344

## 2. Rte module release notes

- ▶ AUTOSAR R4.0 Rev 3
- ▶ AUTOSAR SWS document version: 3.2.0
- ▶ Module version: 6.9.3.B602344
- ▶ Supplier: Elektrobit Automotive GmbH

### 2.1. Change log

This chapter lists the changes between different versions.

#### Module version 6.9.3

2023-01-30

- ▶ ASCRTE-6918 Fixed known issue: The Rte verifier falsely reports an error for compatible DataConstraints for connected sender-receiver data elements

#### Module version 6.10.1

2022-11-25

- ▶ Added support for OsSpinlockLockMethod config parameter in mode communication scenario
- ▶ ASCRTE-6892 Fixed known issue: Rte might use insufficient transformer buffer array if ComSignalEndianness of Com group signal is "BIG\_ENDIAN" and OverrideXfBufferComputation is true
- ▶ Updated the Rte Verifier to report an error if a Com/LdCom signal is mapped to multiple provided operation instances (inter-ECU client-server communication)
- ▶ ASCRTE-6888 Fixed known issue: Rte\_OSShutdownEvent is not calculated by Service Needs Calculator, leading to a compilation error
- ▶ ASCRTE-6876 Fixed known issue: Rte Generator fails if initial value is specified for ApplicationRecordDataType with optional elements but without availabilityBitField
- ▶ Omitted generation of unused server function declaration in case of inter-partition client (connected ports) without Rte\_Call() API

## Module version 6.10.0

2022-10-28

- ▶ Removed unused Rte\_EventBuffer variables which were generated under rare circumstances
- ▶ ASCRTE-6875 Fixed known issue: Under certain conditions, Rte\_IRead() API doesn't read the data sent by Rte\_Write() in 1:n sender-receiver communication

## Module version 6.9.2

2022-09-30

- ▶ Added full support for inter-partition inter-ECU network representation conversion for sender-receiver communication
- ▶ Improved data consistency calculations (locks) by evaluating the execution context of Com reception callbacks
- ▶ ASCRTE-6813 Fixed known issue: The Rte Generator fails with an error if a write access exists for an ApplicationRecordDataType mapped to an ImplementationDataType with optional elements
- ▶ ASCRTE-6782 Fixed known issue: The Rte starts runnables before the Rte\_Start() API is called if the SingleScheduleTablePartitionRef is configured
- ▶ Added support for I-PDU based time-out according to SWS\_Rte\_08103
- ▶ Updated the Rte Verifier to report a warning if there is no corresponding RteExclusiveAreaImplementation/RteBswExclusiveAreaImpl container for an ExclusiveArea
- ▶ Added support for generating implicit sender-receiver APIs as C functions when enableTakeAddress for the port is set to true

## Module version 6.9.1

2022-08-18

- ▶ ASCRTE-6789 Fixed known issue: The Rte may generate nested memory sections for mode disabling dependency variable declarations
- ▶ Improved declaration of DataHandleVariables by declaring them as extern if they are exported as measurement symbols

## Module version 6.9.0

2022-07-22

- ▶ ASCRTE-6781 Fixed known issue: Rte Editor may erroneously show error RTEAS\_129 if multiple instances of the same BSW module exist

## Module version 6.8.4

2022-07-15

- ▶ Improved the Rte editor to create RteSwComponentType names based on the type short name
- ▶ ASCRTE-6794 Fixed known issue: Rte doesn't generate Rte\_IsAvailable() and Rte\_SetAvailable() APIs for some optional elements
- ▶ Improved the initialization of inter-partition mode communication in order to avoid write accesses to the mode variable from multiple partitions

## Module version 6.8.3

2022-06-17

- ▶ Added support for ImplementationDataType.typeEmitter set to header file name
- ▶ Improved sender-receiver buffer sharing by ignoring different aliveTimeout values in case of pure intra-ECU sender-receiver communication
- ▶ Added error code macros that enables an application to check the return value of an API (Infrastructure error, Overlayed error and Application error)
- ▶ Added support for Std\_TransformerForwardCode in Rte\_Write and Rte\_Send
- ▶ Added support for time-triggered execution of runnnables for InternalTriggerOccurredEvent\BswInternalTriggerOccurredEvent
- ▶ Added support for optional upper limit of processed client requests per server executable entity within one task cycle
- ▶ ASCRTE-6717 Fixed known issue: Under certain conditions Rte calls the Com APIs with wrong type argument
- ▶ ASCRTE-6703 Fixed known issue: The Rte schedule table activation offset uses the default value when Rte Os schedule table activation mechanism is set to Next
- ▶ ASCRTE-6729 Fixed known issue: The Rte triggers the DataReceivedEvent twice in a ComSignalGroup reception callback when 1:n sender-receiver inter-ECU communication is used
- ▶ Improved the generation of Memory Allocation Keywords for struct and pointer variables by considering platform-specific alignment requirements

## Module version 6.8.2

2022-05-20

- ▶ Added support for the parameter RteBswExclusiveAreaOsSpinlockRef
- ▶ ASCRTE-6695 Fixed known issue: Inter-partition mode-disabling dependencies do not work with implicit sender-receiver communication
- ▶ Added support to initialize the Rte implicit write buffers at the beginning of an OsTask body according to SWS\_Rte\_08418
- ▶ ASCRTE-6697 Fixed known issue: The value written with the Rte\_IWrite() or Rte\_IWriteRef() API might get lost if the runnable is mapped to a task with an explicit schedule point and the implicit data handle variable is shared
- ▶ Added support for time-triggered execution of runnable entities for AsynchronousServerCallReturnsEvent
- ▶ ASCRTE-6705 Fixed known issue: The Rte generates incomplete memory sections for VFB tracing function declarations
- ▶ ASCRTE-6721 Fixed known issue: The Rte may falsely generate a direct access to a data handle buffer in an Rte\_Read() API function

## Module version 6.8.1

2022-04-08

- ▶ Fixed violation for MISRA Rule 08.2: function pointer declarations (P2FUNC) shall contain the parameter names
- ▶ Changed error message for an empty or invalid RteBswImplementationRef to a warning
- ▶ Improved the verification of ImplementationDataType of category STRUCTURE with optional elements

## Module version 6.8.0

2022-03-18

- ▶ Sorted task list from the Rte Editor's Event Mapping tab in alphanumerical order

## Module version 6.7.3

2022-02-23

- ▶ ASCRTE-6621 Fixed known issue: The Rte might not invoke the runnable triggered by a periodic DataReceivedEvent in case of inter-partition n:1 sender-receiver communication



- ▶ ASCRTE-6651 Fixed known issue: The Rte\_Read() API always returns RTE\_E\_NEVER\_RECEIVED in case of intra-partition inter-ECU sender-receiver communication over LdCom TP and handleNeverReceived set to true
- ▶ Improved order and grouping of implicit read and write accesses in task bodies
- ▶ ASCRTE-6644 Fixed known issue: Rte Editor does not support all scenarios of the n:1 use case for RteBswRequiredSenderReceiverConnection
- ▶ ASCRTE-6627 Fixed known issue: The Rte might define the global copy of an implicit sender-receiver communication in a different partition than the one targeted by the RIPS FlatInstanceDescriptor
- ▶ ASCRTE-6654 Fixed known issue: Under certain conditions the Rte Generator ignores the timeout specified for an AsynchronousServerCallPoint with a configured AsynchronousServerCallReturnsEvent and non-blocking Rte\_Result() API
- ▶ Added support for time-triggered execution of runnable entities and schedulable entities for mode switch events
- ▶ Improved the definition of the pre-compiler symbol RTE\_RUNNABLEAPI\_<rn> macros in order to reduce Rte generation time, the size of Application Header Files, and compile time
- ▶ ASCRTE-6637 Fixed known issue: The Rte wrongly handles the VSA OUT argument of the ClientServerOperation of an asynchronous client if RTE\_E\_HARD\_TRANSFORMER\_ERROR occurred in the result signal callback
- ▶ Added Memory Section Allocation Keyword INIT and CLEARED to distinguish between initialized and uninitialized variables

## Module version 6.7.2

2022-02-04

- ▶ Optimized the error prioritization code for implicit sender-receiver communication
- ▶ ASCRTE-6605 Fixed known issue: The Rte Generator might fail with an error if multiple Rte\_IRead APIs mapped to the same task directly read from Com and network representation conversion is required
- ▶ ASCRTE-6620 Fixed known issue: The non-blocking Rte\_Result() API does not return RTE\_E\_TIMEOUT when it is called with a frequency higher than the configured timeout
- ▶ ASCRTE-6581 Fixed known issue: The Rte reports a NullPointerException when multiple ClientServerOperations trigger the same server runnable but have different argument names
- ▶ Removed possible empty code blocks for implicit write accesses in task bodies
- ▶ Added support for ImplementationDataTypes of category FUNCTION\_REFERENCE

## Module version 6.7.1

2021-12-15

- ▶ ASCRTE-6592 Fixed known issue: The Rte might not invoke the runnable triggered by a periodic DataReceivedEvent in case of inter-partition n:1 sender-receiver communication

## Module version 6.7.0

2021-11-19

- ▶ ASCRTE-6578 Fixed known issue: The Rte\_Call() API might apply an inefficient array copy operation if the server handles multiple operations with array arguments of different lengths
- ▶ Improved the memory usage of the Rte Generator for software components with many runnables and API functions
- ▶ ASCRTE-6579 Fixed known issue: The Rte triggers only one server runnable if there are two or more server runnables with the same short name that are mapped to the same task

## Module version 6.6.3

2021-10-20

- ▶ Support for time-triggered execution of runnables for DataSendCompletedEvent
- ▶ Added support for the generation of review instructions to easily identify memory writes access for freedom from interference analyses (released)
- ▶ Added support for the transmission preparation callback (Rte\_COMCbkTxPrep\_mn) for inter-partition inter-ECU communication. To activate this feature, set the configuration parameter RteGeneration/SendSignalQueueStrategy to "OnePerMainFunction"
- ▶ ASCRTE-6563 Fixed known issue: The Rte may not compile if mixed Rte and BSW events are mapped to a mode user task in case of synchronous inter-partition mode communication
- ▶ ASCRTE-6556 Fixed known issue: The Rte Generator may not generate the ISR body for events mapped to ISRs if the Event Mapping tab of the Rte Editor is used
- ▶ Improved the software component template code generation by adding the proper CODE memory sections

## Module version 6.6.2

2021-09-24

- ▶ ASCRTE-6513 Fixed known issue: The Rte\_Send/Write API uses a wrong buffer if multiple variable data prototypes are mapped to the same system signal and the senders are not located in the Com partition

- ▶ Improved the runtime of the Rte generator for software components with many client-server provide ports
- ▶ Added a verifier to reject configuration with 1:n client to server connection
- ▶ Added a verifier to report an error if the server is synchronously connected to more than 1 client, and at least 1 client is mapped to a different partition than the server and 1 partition-local client is mapped to the same task as server
- ▶ The Rte generator now allows direct server calls where the server runnable has a runsInsideExclusiveArea
- ▶ ASCRTE-6500 Fixed known issue: Under certain conditions, an unqueued sender-receiver data element with a structure data type is not received intra-partition inter-ECU if it is directly read from Com
- ▶ Added support for Rte\_IrvlWriteRef API

## Module version 6.6.1

2021-08-20

- ▶ The Rte will no longer export the alignment attribute of a memory section if the memoryAllocationKeyword-Policy of the SwAddrMethod is set to ADDR-METHOD-SHORT-NAME
- ▶ Avoided exception when there is no valid BswOsTaskRef in case of multi-core client/server communication and Det enabled
- ▶ ASCRTE-6533 Fixed known issue: The Rte Bswmd generator may fail with an error on attempt to create a McDataInstance for an ApplicationArrayDataType with a DynamicArraySizeProfile VSA\_LINEAR or VSA\_SQUARE

## Module version 6.6.0

2021-07-28

- ▶ Improved verifier in case of single receive signal port runnable not mapped to a task
- ▶ ASCRTE-6501 Fixed known issue: Under certain conditions the tasks in a task chain are not activated if all TimingEvents define the same offset greater than 0
- ▶ Improved RTE\_516 Verifier Warning to be more descriptive to the user (included the minimum Queue length required)
- ▶ Changed verification of the BswModuleDescription.moduleID from mandatory to optional

## Module version 6.5.3

2021-07-02

- ▶ Improved support for variable size arrays
- ▶ ASCRTE-6446 Fixed known issue: If one return signal is used for n:1 inter-ECU client-server communication, the response from the server might get lost
- ▶ ASCRTE-6476 Fixed known issue: Missing memory sections for VFB tracing function declarations
- ▶ ASCRTE-6473 Fixed known issue: The Rte generator fails with error in case of inter-partition inter-ECU SR communication on receiver side where no runnable has access to the data element and a time-out is configured
- ▶ ASCRTE-6485 Fixed known issue: Under certain conditions the Rte falsely applies network representation conversion to inter-partition inter-ECU sender-receiver communication on sender side

## Module version 6.5.2

2021-05-28

- ▶ ASCRTE-6453 Fixed known issue: The Rte Generator may falsely export elements with duplicate IDs in the RteDataModel.xml
- ▶ Added option to always invoke the transformer chain for required sender-receiver data elements if ExecuteDespiteDataUnavailability is true. To enable this option, set the configuration parameter Rte/RteGeneration/ExecuteTransformersDespiteLocalDataQueued (for queued communication) resp. Rte/RteGeneration/ExecuteTransformersDespiteLocalDataUnqueued (for unqueued communication) to true
- ▶ Improved sharing of implicit sender-receiver buffers by allocating the buffers in highest to lowest status element order

## Module version 6.5.1

2021-04-30

- ▶ ASCRTE-6439 Fixed known issue: Data type of Rte variable "Rte\_AliveTimeoutCounter\_<Sig>" is wrongly generated in case of 1:n sender-receiver communication with different aliveTimeout values
- ▶ Improved sharing of implicit sender-receiver buffers between sender and receiver with different compatible data types

## Module version 6.5.0

2021-04-16

- ▶ Improved: The Rte\_Write/Rte\_Send API in case of LdCom TP usage (EcuC\_TpTransmit) and locked tx buffer

- ▶ Improved the Rte generator to trigger autonomous error reaction irrespective of the transformer class
- ▶ Improved startup and initialization of NvBlockSwComponents: Restore default data if both ROM and initialization callback are configured
- ▶ ASCRTE-6407 Fixed known issue: The Rte\_Read() API may copy wrong data when inter-partition-inter-ECU sender-receiver communication is used with transformers and function elision is enabled
- ▶ ASCRTE-6410 Fixed known issue: The Rte does not apply data conversion to an implicit intra-ECU sender-receiver communication under certain conditions

## Module version 6.4.11

2021-03-12

- ▶ ASCRTE-6360 Fixed known issue: The Rte does not trigger DataSendCompletedEvent if an Rte\_LdCom-CbkTpTxConfirmation() callback arrives with a value different from E\_OK
- ▶ The Rte falsely reports a warning for arrays of anonymous structs
- ▶ Added measurement support for Nv RomBlocks
- ▶ Added support for retrieving partition context for LdCom on a per-PDU basis
- ▶ ASCRTE-6355 Fixed known issue: The Rte\_Send() API might not return RTE\_E\_LIMIT if a queue was full in case of 1:n inter-partition queued sender-receiver communication
- ▶ Added support for the generation of review instructions to easily identify memory writes access for freedom from interference analyses (draft)
- ▶ Added support to generate graphical representation of the internal Rte generator model

## Module version 6.4.10

2021-02-17

- ▶ Improved DET checking/reporting code in Enter/Exit APIs
- ▶ Added support for mapping of variable data prototypes to parameter data prototypes
- ▶ ASCRTE-6342 Fixed known issue: The Rte Generator fails with an error if an ArrayImplementation-DataType is used for parameter access in combination with RIPS
- ▶ ASCRTE-6336 Fixed known issue: The Rte Generator might ignore system data mappings of receive signals/signalGroups to PRPortPrototypes
- ▶ Added support for memory sections with alignment when SWAddrMethod or SwAlignment are not configured
- ▶ ASCRTE-6339 Fixed known issue: The Rte Generator fails with an error when generating the Rte\_Ld-ComCbkRxIndication for a variable size array

- ▶ ASCRTE-6327 Fixed known issue: The Rte Generator is not generating the definition of Rte\_Transformer-Error when contract phase is generated
- ▶ Added support for retrieving partition context for COM on a per-PDU basis
- ▶ Added support for Rte\_IReadRef API

## Module version 6.4.9

2021-01-22

- ▶ ASCRTE-6291 Fixed known issue: Rte Editor does not support the n:1 use case for RteBswRequiredSenderReceiverConnection

## Module version 6.4.8

2020-12-18

- ▶ ASCRTE-6292 Fixed known issue: The SvcAs configures duplicate XfrmImplementationMappings in case of inter-ECU client-server communication with multiple clients
- ▶ ASCRTE-6296 Fixed known issue: Under certain conditions the Rte verifier does not report an error if an NvBlockSwComponentType is connected to another component type from a different partition
- ▶ Improved precision of data conversion formulas with floating-point operands
- ▶ Added support for synchronous mode switching procedure over multi partitions/cores

## Module version 6.4.7

2020-10-30

- ▶ ASCRTE-6235 Fixed known issue: The Rte\_IStatus API might be empty in case of implicit 1:n sender-receiver communication with different aliveTimeout values
- ▶ Added support for inter-partition direct server call
- ▶ ASCRTE-6228 Fixed known issue: The Rte might not activate the runnable triggered by an AsynchronousServerCallReturnsEvent if a timeout occurred in case of non-blocking result reception
- ▶ ASCRTE-6241 Fixed known issue: The Rte generates an incorrect call to RIPS APIs Rips\_Enter()/Rips\_Exit() under certain conditions
- ▶ ASCRTE-6204 Fixed known issue: The Rte may generate wrong event handling in case a server uses multiple instantiation
- ▶ ASCRTE-6223 Fixed known issue: The Rte Generator fails if an AsynchronousServerCallReturnsEvent exists for an unconnected non-blocking AsynchronousServerCallPoint with time-out

- ▶ Added support for mapping of ModeSwitchEvents for initialMode switches to RteInitializationRunnable-Batch
- ▶ Improved: The Rte now aborts the execution of the SvcAs in case of an error
- ▶ ASCRTE-6188 Fixed known issue: The Rte may generate extern definitions of the same data handle variable in multiple C-files in case of implicit inter-partition 1:n sender-receiver communication
- ▶ Added: The verification of the mapping constraints (component clustering and separation) for EcuExtract
- ▶ ASCRTE-6249 Fixed known issue: The Rte might not activate the runnable triggered by ModeSwitchedAckEvent in case of mode user partitions without ModeSwitchEvents
- ▶ ASCRTE-6265 Fixed known issue: Under certain conditions OperationInvokedEvents and DataReceivedEvents are not triggered according to their configured RtePeriod/RteBswPeriod
- ▶ ASCRTE-6219 Fixed known issue: Under certain conditions the Rte does not trigger ModeSwitchedEvents
- ▶ ASCRTE-6277 Fixed known issue: Rte\_IRead() API might not read written value in implicit 1:1 sender-receiver communication with transmission acknowledgement enabled

## Module version 6.4.6

2020-09-25

- ▶ Added support of multiple receivers of SignalGroups with different subelement mappings to primitives
- ▶ ASCRTE-6189 Fixed known issue: The Rte Generator fails with an error if a SwServiceArg of category DATA\_REFERENCE is used for SchM client-server communication
- ▶ Added support for handling the RTE\_E\_NEVER\_RECEIVED error in case of inter-partition communication

## Module version 6.4.5

2020-08-28

- ▶ ASCRTE-6187 Fixed known issue: The Rte Generator fails with an error if metadata is used for C/S communication over LdCom TP
- ▶ Added support of RIPS for ParameterInterfaces
- ▶ Removed the verifier which reports a warning when the ComTimeout value is different from the AliveTimeout value

## Module version 6.4.4

2020-07-31

- ▶ ASCRTE-6171 Fixed known issue: The Rte Generator fails with an error if an application data type which represents a new world variable size array is used for S/R communication
- ▶ ASCRTE-6175 Fixed known issue: The Rte rejects configurations if client-server operations are compatible according to TPS\_SWCT\_01125 and if an ArgumentDataPrototype is typed by an ApplicationArray-DataType
- ▶ Added support of multiple receivers with different aliveTimeout values mapped to the same SystemSignal
- ▶ ASCRTE-6177 Fixed known issue: The Rte may generate a wrong alignment suffix for the Memory Allocation Keyword if a SwAddrMethod is configured for a VariableDataPrototype typed by an array of struct data type
- ▶ ASCRTE-6185 Fixed known issue: The Rte Generator fails with an error if metadata is configured for a PDU and a signal contained in this PDU is sent via S/R communication over LdCom
- ▶ Added missing task end/chain hook in task chains according to [SWS\_Rte\_06032]

## Module version 6.4.3

2020-06-29

- ▶ Changed linking of Rte symbols from object files to the newly introduced Rte\_src.lib library file
- ▶ ASCRTE-6106 Fixed known issue: The Rte\_Feedback() API never returns a transformer error in case of inter-partition inter-ECU communication with transformers
- ▶ ASCRTE-6109 Fixed known issue: The Rte may generate an empty Rte\_IStatus() macro if an implicit S/R communication buffer (data handle buffer) is shared
- ▶ Added support for initial value calculation with RuleBasedValueSpecification
- ▶ ASCRTE-6124 Fixed known issue: The Rte does not enter/exit an exclusive area in the role runsInside-ExclusiveArea of a RunnableEntity that is mapped to a BswSchedulableEntity and started by a BswEvent
- ▶ ASCRTE-6125 Fixed known issue: An implicit inter-runnable variable is not updated for a RunnableEntity that is mapped to a BswCalledEntity which is started by an BswOperationInvokedEvent and mapped to a task
- ▶ Added a verifier to report a warning when the ComTimeout value is different from the AliveTimeout value
- ▶ ASCRTE-6086 Fixed known issue: The Rte generator does not consider the value of the parameter RteRipsPluginFillFlushRoutineFncSymbol
- ▶ ASCRTE-6123 Fixed known issue: The Rte may falsely generate a direct access to a data handle buffer in an Rte\_Write() API function
- ▶ ASCRTE-6117 Fixed known issue: The Rte may update a shared sender-receiver data handle buffer from different partitions
- ▶ Added support for DataWriteCompletedEvents and Rte\_IFeedback API



- ▶ ASCRTE-6147 Fixed known issue: The Rte generates an invalid switch statement in the Com task if a data element to signal fan-out is used and the signals are sent on different Com partitions

## Module version 6.4.2

2020-05-27

- ▶ Improved argument buffering for synchronous client/server communication
- ▶ Added support for synchronized triggers
- ▶ ASCRTE-6085 Fixed known issue: The Event Mapping tab of the Rte Editor reports a StackOverflowError when a circular client-server call chain is configured
- ▶ Added support for variable size array inter-ECU communication without transformers
- ▶ ASCRTE-6092 Fixed known issue: The Rte verifier may falsely report an error about a wrong availability-Bitfield array size for structs with optional elements
- ▶ Improved: The Rte shall optionally use the (Ld)Com container's length as transformer buffer length
- ▶ ASCRTE-6091 Fixed known issue: The Rte\_Read() API generates an incorrect call to Com\_ReceiveSignal if DirectReadFromCom applies but LdCom is used

## Module version 6.4.1

2020-04-24

- ▶ Improved verifier SWCUTILS\_SYSDVFY\_658 to allow blocking data reception of the same required VariableDataPrototype from different runnables which are mapped to the same task
- ▶ Added verifier to check transformer buffer sizes against LdCom signal sizes
- ▶ ASCRTE-6049 Fixed known issue: The contract phase generation fails with an error if a Bsw module contains a BswInternalTriggeringPoint or an IssuedTrigger
- ▶ Added export of internal Rte generator model to XML when RteDataModelExport is enabled
- ▶ ASCRTE-6060 Fixed known issue: The Rte generates an unnecessary cast to void\* in the IRead() API if RIPS is enabled
- ▶ ASCRTE-6064 Fixed known issue: RTE does not generate calls to RIPS APIs IWBufferRef()/IRBufferRef() for implicit S/R communication if RtePluginSupportsIReadIWrite is set to false
- ▶ ASCRTE-6043 Fixed known issue: An AsynchronousServerCallReturnsEvent triggers the non-blocking result runnable twice if a timeout is defined for the AsynchronousServerCallPoint
- ▶ Improved the Rte generator to verify the consistency of PortAPIOptions and PortDefinedArgumentValues if EventInstances are merged

- ▶ ASCRTE-6072 Fixed known issue: The Rte invokes configured Rte\_Rips\_FillFlushRoutines although the parameter RteRipsSupport is set to RTE\_RIPS\_OFF
- ▶ Added verifier to check restriction to explicit sending semantics for the usage of DataServices in the context of a SwcServiceDependency that aggregates DiagnosticValueNeeds that in turn is referenced by a DiagnosticIoControlNeeds

## Module version 6.4.0

2020-03-25

- ▶ Improved the RTE generator to call the RIPS APIs for unconnected Sender/Receiver ports
- ▶ Added verifier check for implicit S/R 1:n communication where receivers are not compatible and mapped to the same FID and RIPS is managed by plugin
- ▶ Added support for generating Bitfield Texttable definitions according to the AUTOSAR 4.4 RTE spec

## Module version 6.3.14

2020-02-21

- ▶ Added support for LdCom without transformers and inter-partition communication over loc
- ▶ ASCRTE-5943 Fixed known issue: The Rte triggers ModeSwitchedAckEvents at the end of the transition to the initial mode
- ▶ Improved performance of queue accesses by caching the queue index in a stack variable. This also fixes a possible compiler warning: "the order of volatile accesses is undefined"
- ▶ ASCRTE-5990 Fixed known issue: The Rte software component header might include a wrong RIPS software component header
- ▶ ASCRTE-5988 Fixed known issue: The Rte Editor does not display all BswEvents under certain conditions
- ▶ ASCRTE-5981 Fixed known issue: The Rte generates an incorrect subElement mapping under certain conditions
- ▶ ASCRTE-5967 Fixed known issue: The Rte does not allow mapping of ApplicationRecordDataType to ImplementationDataType with optional elements
- ▶ ASCRTE-5992 Fixed known issue: Under certain conditions the Rte falsely skips data conversion for inter-ecu sender-receiver communication
- ▶ ASCRTE-5970 Fixed known issue: The Rte triggers ModeSwitchedAckEvents and BswModeSwitchedAckEvents wrongly in case of multiple mode user partitions
- ▶ ASCRTE-5950 Fixed known issue: Subsequent calls to LdCom\_TpTransmit() fail if an Rte\_LdComCbK-TpTxConfirmation() callback arrives very shortly after LdCom\_TpTransmit()

- ▶ Generate execution condition for the A1 mapped timing events to ISR if periods are not equal
- ▶ ASCRTE-5978 Fixed known issue: Rte\_LdComCbkgTriggerTransmit() copies wrong data in case of inter-partition inter-ECU communication with an autonomous error reaction
- ▶ ASCRTE-5979 Fixed known issue: An autonomous error reaction may corrupt data of outstanding server responses
- ▶ Added support for RIPS for BSW explicit queued sender/receiver communication and exclusive area protection
- ▶ ASCRTE-5963 Fixed known issue: The Rte\_Main.c does not include Rte\_UserDefinedExclusiveArea.h in case multiple ExclusiveAreas are shared across different partitions
- ▶ Updated the check for Rte Det error reporting with partitioning and shared Schm exclusive areas
- ▶ Added support for memory optimization for zero initial values
- ▶ Improved the Rte verifier to report an error if a server RunnableEntity is mapped to a BswSchedulableEntity. It must be mapped to a BswCalledEntity instead
- ▶ ASCRTE-6002 Fixed known issue: The Rte might return a wrong server result to a client if all other connected clients use result-free asynchronous client-server communication
- ▶ Improved the Rte generator to allow client tasks to have a higher priority than the server task also in basic intra-partition scenarios
- ▶ Improved data consistency optimizations and task mapping verifications by considering Os resources of exclusive areas in the runsInsideExclusiveArea role
- ▶ ASCRTE-6004 Fixed known issue: The Rte generates duplicate macros in the Rte\_Buffers.h for 1:n implicit S/R RIPS communication

## Module version 6.3.13

2020-01-24

- ▶ ASCRTE-5942 Fixed known issue: Rte generation fails if an ImplementationDataType is used in a PortInterface and has the same name as one of the standard OsTypes
- ▶ Improved: The Rte now adds suffixes for float and double literals if initial values are generated but not explicitly configured
- ▶ Improved: The Rte Generator now verifies the partition of Tx Com signals against the partition of the Bsw task if no ComTaskConfiguration containers are configured
- ▶ ASCRTE-5954 Fixed known issue: The SvcAs fails with an error in case of a SystemSignal-to-ISignal Tx fan-out with LdCom, even if distinct TransformationTechnology elements are used for each ISignal
- ▶ ASCRTE-5945 Fixed known issue: The Rte does not ensure data consistency between LdCom\_Transmit() and Rte\_LdComCbkgTriggerTransmit()

## Module version 6.3.12

2019-12-06

- ▶ ASCRTE-5912 Fixed known issue: The server might not be called synchronously if intra-partition or inter-partition-intra-core client-server communication is used and a category 2 executable is mapped to the server task
- ▶ ASCRTE-5903 Fixed known issue: The SvcAs does not create all CustomXf XfrmImplementationMappings for Tx signals if a transformer chain contains multiple TransformationTechnology elements with transformerClass set to custom
- ▶ Added support for configurable output directory of Rte\_Bswmd.xml
- ▶ Added support for LdCom without transformers
- ▶ ASCRTE-5915 Fixed known issue: The Rte may copy wrong data in Rte\_LdComCbkCopyTxData()
- ▶ ASCRTE-5914 Fixed known issue: The Rte does not lock the LdCom TP transmit buffer sufficiently
- ▶ Updated the verifier to report a warning when an ImplementationDatType of category ARRAY contains an ImplementationDataTypeElement of category STRUCTURE or UNION
- ▶ ASCRTE-5904 Fixed known issue: The Rte\_Receive() API never returns RTE\_E\_LOST\_DATA in case of inter-partition inter-ECU communication with transformers
- ▶ Provided ALIGNMENT info for CONST MEMORY-SECTIONS which are generated in Rte\_Bswmd.xml
- ▶ Added support for RteCalibrationSwAddrMethodRef for software calibration
- ▶ Added support for mapping timing events to ISRs

## Module version 6.3.11

2019-11-08

- ▶ Add partitioning support for communication timeout with transformers
- ▶ Improved lock optimizations for Rte/SchM APIs that are accessed from non-interruptible tasks only

## Module version 6.3.10

2019-10-11

- ▶ Improved: Define macro of PR port initial value to be generated once
- ▶ Changed the parameter RteSwNvRamMappingRef of the container RteNvRamAllocation to optional
- ▶ ASCRTE-5826 Fixed known issue: Under certain conditions the Rte may send/receive incorrect data to/from the Com module
- ▶ Improved verifier for inter Ecu C/S communication with client runnable entity not mapped to a task

- ▶ Added support for `TimingEvent.offset`
- ▶ ASCRTE-5850 Fixed known issue: The SvcAs does not set `ComTimeout` in case of inter-partition inter-ECU sender-receiver communication
- ▶ Added support for overlayed errors in `Rte_Read()`, `Rte_Receive()` and `Rte_IStatus()` APIs (intra-partition intra/inter-ECU sender-receiver communication only)
- ▶ ASCRTE-5851 Fixed known issue: The SvcAs does not set `ComSignalInitValue`, `ComSignalDataInvalidValue` and `ComDataInvalidAction` in case of inter-partition inter-ECU sender-receiver communication
- ▶ ASCRTE-5848 Fixed known issue: Rte generates incorrect code in the case of multiple internal triggering points with the same short name triggering the same runnable
- ▶ ASCRTE-5849 Fixed known issue: Rte generation fails for inter-core asynchronous client-server communication with `InterPartitionCommunication` set to `Mixed`
- ▶ ASCRTE-5822 Fixed known issue: The generated Rte may not compile in case of implicit mixed n:1 inter/intra-partition S/R communication with transformers
- ▶ Added support for `Rte(Bsw)ExclusiveAreaOsResourceRef` to reference an `OsResource` in case `RteExclusiveAreaImplMechanism` is configured to `OS_RESOURCE` for this `ExclusiveArea`
- ▶ ASCRTE-5845 Fixed known issue: The `Rte_IStatus()` API does not return any transformer error in case of inter-partition inter-ECU sender-receiver communication
- ▶ ASCRTE-5854 Fixed known issue: The Rte generator may falsely report a warning if multiple `NonqueuedReceiverComSpec.initValue` of the same port reference data elements with different application data types
- ▶ Improved the number of enter/exit critical section calls by grouping the SMC channel read/write accesses of the same OS task by their critical section blocks in case of implicit S/R communication
- ▶ ASCRTE-5879 Fixed known issue: The Rte Generator may fail if `RteDevErrorDetect` is true and a software component has multiple ports with the same port interface
- ▶ ASCRTE-5855 Fixed known issue: Det error reporting and partitioning may lead to linkage errors about multiple definitions of `Rte_Det_IntLockTask`
- ▶ Improved data consistency optimizations and task mapping verifications by considering internal Os resources

## Module version 6.3.9

2019-09-06

- ▶ ASCRTE-5791 Fixed known issue: The function declaration of the `RunnableEntity` triggered by `ExternalTriggerOccurredEvent` is not visible for the trigger source if direct function call and function elision apply
- ▶ ASCRTE-5814 Fixed known issue: The Rte Generator generates a wrong argument cast in direct server call when server and client use different client-server-interfaces

- ▶ ASCRTE-5815 Fixed known issue: The Rte Generator falsely reports an error if an ExternalTriggerOccurredEvent is mapped to an OsIsr in a multi-partition environment
- ▶ Added support for intra-ECU sender-receiver sub element mapping to a primitive data element
- ▶ Added support for mode switch acknowledgement for basic software

## Module version 6.3.8

2019-08-09

- ▶ Improved: The Rte now supports casting to compatible data types when calling transformers
- ▶ Changed the assignment of void pointer arguments to always use TS\_MemCpy, e.g. in the Rte\_SetMirror and Rte\_GetMirror callbacks

## Module version 6.3.7

2019-08-06

- ▶ Added support for compu methods specified through the physical props of SystemSignal
- ▶ ASCRTE-5752 Fixed known issue: The Rte.bmd file contains an invalid destination reference to the NvM-BlockDescriptor for parameter RteNvmBlockRef
- ▶ ASCRTE-5775 Fixed known issue: The Rte verifier may falsely report an error about a wrong transformer buffer size if GroupSignals are not byte-aligned
- ▶ Added inter-partition support for coherency groups
- ▶ Fixed violation of MISRA rule 20.7 by using FUNC\_P2CONST/FUNC\_P2VAR macro instead of nested compiler abstraction
- ▶ ASCRTE-5753 Fixed known issue: The Rte may not properly evaluate the SwDataDefProps if multiple ImplementationDataTypes with the same shortName exist
- ▶ ASCRTE-5749 Fixed known issue: The Rte does not protect the inter-partition SchM\_Switch API against concurrent mode switch notifications under certain conditions
- ▶ ASCRTE-5761 Fixed known issue: The Rte Generator may not call the server function with an activating event argument

## Module version 6.3.6

2019-07-12

- ▶ Added support for Rte(Bsw)UsedOsEventRef to specify which OsEvent is used to activate the OsTask

## Module version 6.3.5

2019-06-14

- ▶ ASCRTE-5676 Fixed known issue: Data conversion for network representation without an application data type is not performed
- ▶ ASCRTE-5689 Fixed known issue: The Rte may not apply data consistency for the readers of inter-core SMC channels where only implicit writers exist
- ▶ Improved EB\_FAST\_LOCK for multi-core set-ups
- ▶ Improved separation of the Rte partitions for COM send signals by adding a new configuration parameter (SendSignalQueueStrategy)
- ▶ Added support for inter-partition inter-ECU communication timeout. The RTE will write the data and RTE\_E\_OK in case of successful reception of data and will write RTE\_E\_MAX\_AGE\_EXCEEDED as status in case of reception timeout
- ▶ ASCRTE-5710 Fixed known issue: The Rte may not apply data consistency for the implicit writers of inter-partition n:1 sender-receiver communication
- ▶ Added limited support for Coherency Groups
- ▶ ASCRTE-5907 Fixed known issue: The Rte generation may fail in case of inter-core intra-ECU C/S communication where the server runnable is target of multiple OperationInvokedEvents
- ▶ Added support for multiple mode user partitions

## Module version 6.3.4

2019-05-17

- ▶ Improved Rte generator to support arbitrary Os TaskType definitions
- ▶ ASCRTE-5649 Fixed known issue: Potential loss of precision for initial/invalid values defined by ApplicationValueSpecifications
- ▶ Removed verifier warnings for diverse SwBaseType native declarations of type "long long"
- ▶ Improved verifier for BswCalledEntity SwServiceArgs
- ▶ ASCRTE-5670 Fixed known issue: The Rte Generator may fail if a received group signal is mapped to a primitive non-queued sender-receiver data element and DirectReadFromCom applies
- ▶ Added Support for time-triggered execution of runnables for data received events
- ▶ Improved the Rte generator to not throw a warning message in case R-Port of NVBlockSoftwareComponentType is unconnected and do not define an INIT value
- ▶ Improved Rte by providing appropriate suffixes for 64-bit integer values
- ▶ Implemented timeout monitoring for asynchronous client/server communication with non-blocking result calls

## Module version 6.3.3

2019-04-18

- ▶ Enhanced the information displayed for mode switched events in the Rte Editor
- ▶ ASCRTE-5616 Fixed known issue: The Rte\_Read API does not protect receiving of a Com signal against concurrent access if DirectReadFromCom applies
- ▶ Added support for autonomous error reaction on client side
- ▶ ASCRTE-5586 Fixed known issue: User-defined ExclusiveArea callouts are not aware of multiple SWC instances
- ▶ Improved spinlock allocation for cross-core queues in case of unmapped executables
- ▶ ASCRTE-5637 Fixed known issue: The Rte Editor configures wrong trace hook function names for COM callbacks
- ▶ Improved client data structure type naming
- ▶ Changed the return code of Rte\_Read in case of intra-partition inter-ECU sender-receiver communication with data transformation; the RTE\_E\_SOFT\_TRANSFORMER\_ERROR and RTE\_E\_HARD\_TRANSFORMER\_ERROR of the previous transformer invocation is returned repeatedly unless new data are available and executeDespiteDataUnavailability is false
- ▶ ASCRTE-5614 Fixed known issue: The Rte calculates a wrong deserializing transformer buffer size if subElement mappings/partial records and a non-constant bufferComputation are used
- ▶ ASCRTE-5607 Fixed known issue: The generated Rte BSWMD file does not contain the OUT-GOING-CALLBACK entries for defined RteVfbTraceFunctions if RteGeneratorOutput is set to BSW\_SCHEDULER\_ONLY

## Module version 6.3.2

2019-03-22

- ▶ Improved the transformer hard error handling for intra-ECU transformations
- ▶ Replaced SMC API by direct buffer access
- ▶ Improved allocation of OsResources for ReceiveSignals if no API uses them

## Module version 6.3.1

2019-02-15

- ▶ Implemented Rte\_IsAvailable and Rte\_SetAvailable APIs to query and modify the availability status of optional structure elements



- ▶ ASCRTE-5564 Fixed known issue: The Rte generator falsely reports an error if a CompuMethod of category TEXTTABLE contains non-point ranges
- ▶ Added support for RteBswRequiredSenderReceiverConnections in the Rte Editor
- ▶ ASCRTE-5566 Fixed known issue: The Rte Editor faces delays on the attempt to change the position of an EventToTaskMapping entry
- ▶ Improved generation of Rte\_DummyDirtyFlag
- ▶ Added support for flag ExecuteDespiteDataUnavailability, for unqueued sender-receiver communication
- ▶ Improved the Rte Generator by avoiding null pointer exception when allocating a BswModeSwitchEvent with an invalid TargetModeRef
- ▶ Implemented verifier to check transformer buffer sizes against Com signal(group) sizes
- ▶ ASCRTE-5578 Fixed known issue: The Rte generates an invalid switch statement if a client-server provide port is connected but no ServerCallPoint references the operation
- ▶ Improved: The Rte Generator allows non-mapped client executables (i.e. executable with unknown task context) in case of intra-ECU result-free asynchronous client-server communication
- ▶ Updated the Rte Verifier to report an error if a VariableDataPrototype instance in the role ramBlock is accessed by SW-C instances of different partitions
- ▶ Added verifier to check that the returnSignal and callSignal of a ClientServerToSignalMapping are mapped to Com signals of type UINT8\_N or UINT8\_DYN
- ▶ Improved: The Rte Generator now considers the SwAddrMethod in the memory section of a BswScheduleableEntity/BswCalledEntity, in case it is present
- ▶ Added configurable generation of timestamp for generated RTE files
- ▶ ASCRTE-5565 Fixed known issue: The Rte generator falsely reports an error if the Init-Value of an ApplicationPrimitiveDataType does not fall in the internal interval of the CompuMethod
- ▶ Implemented additional verifier for Implementation data type according to [constr\_1383]
- ▶ Updated the Rte Verifier to report an error if the mode values of a mode declaration group do not fit into the mapped implementation data type
- ▶ Improved verification of compatibility of Com signal type with base type of NetworkRepresentationProps

## Module version 6.3.0

2019-01-25

- ▶ Improved: The Rte Generator now verifies that no Bsw event triggering any type of Bsw module entity is mapped to the Bsw Os task

- ▶ Added support for a new configuration parameter "Respect configured task type" to prevent the Rte from ignoring the configured task type BASIC although an EXTENDED task is required and to report an error message instead
- ▶ Added verifier for missing configured BSW module implementation reference
- ▶ Added support for data filtering on sender side for inter-ecu sender/receiver communication
- ▶ Implemented additional verifier for SwcBswMapping according to [constr\_4085] and [constr\_4084]
- ▶ Updated the Rte verifier to report a warning if a ComSignal has the wrong direction

## Module version 6.2.27

2018-12-21

- ▶ Added a configuration parameter (HumanReadableBufferNames) to switch between the generation of hashed buffer names and human readable buffer names
- ▶ Improved: The Rte Generator shall report warning RTE\_526 only for the used data element prototypes of an interface that do not have an initial value defined in the port prototype's ComSpec
- ▶ ASCRTE-5539 Fixed known issue: The Rte generates illegal hexadecimal constants on 64-bit architectures under certain conditions

## Module version 6.2.26

2018-12-13

- ▶ Improved the Rte Bswmd generator to export the McSupportData as self-contained artifact
- ▶ Improved ROM consumption of the Rte by removing the unnecessary zero-based initializations of transformer rawValue arrays
- ▶ Added support for Lock-free queues
- ▶ ASCRTE-5525 Fixed known issue: The Rte Generator fails with an error if a signal uses a data type of category VALUE referencing a SWBaseType
- ▶ Fixed known issue: The Rte Generator fails with an error when the NvBlockSwComponentType has an unconnected client port in the role NvMNotifyJobFinished
- ▶ ASCRTE-5519 Fixed known issue: The Rte\_Write API does not update a data handle variable in an N:1 sender-receiver communication under certain circumstances
- ▶ ASCRTE-5527 Fixed known issue: If a server runnable handles multiple inter-partition operations with an array argument of different length, the argument's data may be lost

## Module version 6.2.25

2018-11-23

- ▶ Improved the access of RTE Implementation Plugins (RIPS) by applying the reference type URI-REFERENCE for RteRipsPluginConfigurationRef
- ▶ Improved sorting of column Position on the Event Mapping tab of the Rte Editor
- ▶ Improved verifier for constr\_1011 in case of SwBaseType with category VOID
- ▶ ASCRTE-5503 Fixed known issue: The Rte generates inconsistent memory sections for a per-instance memory that is mapped to an NvM block
- ▶ Added support for time-triggered server executables
- ▶ Changed queuing support of triggers to strictly adhere to [SWS\_Rte\_07087] refined with AUTOSAR 4.4.0
- ▶ Improved: The Rte now supports including additional type header files for ImplementationDatTypes whose typeEmitter is different from RTE
- ▶ Added support for mixed data conversion for intra-ECU SenderReceiverInterfaces (SCALE\_LINEAR\_AND\_TEXTTABLE to TEXTTABLE or LINEAR) and mixed linear conversion for network representation according to rte\_sws\_3832

## Module version 6.2.24

2018-11-02

- ▶ Improved queue efficiency and memory consumption (e.g. for queued sender-receiver communication). RTE\_E\_LOST\_DATA is now only reported on first reception after the overflow occurred in case of inter-partition queued sender-receiver communication
- ▶ Added the return code SCHM\_E\_LOST\_DATA for the SchM\_Receive API
- ▶ ASCRTE-5367 Fixed known issue: The Rte does not reset the transformer error status at the beginning of an implicit write
- ▶ ASCRTE-5462 Fixed known issue: The Rte creates too small buffers for Com reception and transformers if partial record support is used
- ▶ Added support for multiple data elements in the context of a software component port being all mapped to one data element in the context of a NvBlockSwComponentType port using TextTableMappings
- ▶ Added warning message in case locking strategy is overruled by the Rte (for OsResources only in case of unmapped runnables)
- ▶ ASCRTE-5465 Fixed known issue: Possible corruption of mode disabling dependencies in case of multiple inter-partition mode communications
- ▶ ASCRTE-5474 Fixed known issue: The server may not be called synchronously if an inter-partition intra-core client is mapped to a non-preemptive task

- ▶ ASCRTE-5461 Fixed known issue: The Rte aborts the execution of all subsequent transformer chains if a transformer returns a hard error
- ▶ Added support for the RTE Implementation Plugins (RIPS) for explicit/implicit sender receiver communication
- ▶ Implemented support for inter-partition queued triggering for application software components and basic software modules.
- ▶ Implemented: Mapping of ExternalTriggerOccurredEvents for category 1 executables to category 2 ISRs
- ▶ ASCRTE-5452 Fixed known issue: The SchM\_Trigger API may activate the triggered executables multiple times in case of 1:n external trigger communication
- ▶ ASCRTE-5433 Fixed known issue: The Rte Generator fails with an error in case of mixed Rte\_IWrite/Rte\_Read API and non-mapped reader
- ▶ Added support for sender-receiver intra-partition data conversion for CompuMethod categories TEXTTABLE and SCALE\_LINEAR\_AND\_TEXTTABLE
- ▶ ASCRTE-5445 Fixed known issue: Rte\_Read/Rte\_IRead/Rte\_IStatus might return the wrong value until first reception of data if invalidation for complex types is used
- ▶ Added support for InitEvents triggering runnable entities via RteInitializationRunnableBatch
- ▶ Added a prefix for Shared Memory Communicator (SMC) inter-core variables

## Module version 6.2.23

2018-10-03

- ▶ Enabled moving of multiple mapped events / runnables at once and editing of position numbers
- ▶ Changed Rte Bswmd generator to a module dependent pre generator
- ▶ ASCRTE-5271 Fixed known issue: The Rte may not compile if intra-partition mode management is used and inter-partition events with mode disabling dependencies exist
- ▶ Updated verification of [constr\_4022] to accept BswModuleDescription.implementedEntry according to AUTOSAR 4.3.1
- ▶ ASCRTE-5420 Fixed known issue: The prioritization rules of attribute SwDataDefProps.swImplPolicy are not handled properly for a VariableAccess to a sender-receiver data element
- ▶ ASCRTE-5423 Fixed known issue: Os events may be falsely shared for executable with a minimumStartInterval greater zero
- ▶ Implemented: Support for multiple dirty flags
- ▶ ASCRTE-5421 Fixed known issue: The Rte generation fails when using floating point values for CompuScale limits
- ▶ Added support for empty initialization of variable size arrays

- ▶ ASCRTE-5446 Fixed known issue: The ROM Block Location Symbols are not generated in the Rte\_NvmData.h if SwcServiceDependency.assignedDatas in the role "defaultValue" are used
- ▶ ASCRTE-5432 Fixed known issue: Rte\_Cbk.h might not include Nvm\_Types.h when partitioning is enabled with NvBlockSwComponents

## Module version 6.2.22

2018-08-24

- ▶ ASCRTE-5327 Added deviation: A DataReceivedEvent for a queued sender-receiver data element is triggered even if enqueueing of the data failed due to a full receive queue
- ▶ Implemented: Component Data Structure (CDS) generation optimization regarding data handle buffers and inter runnable variables
- ▶ ASCRTE-5311 Fixed known issue: The generated Rte BSWMD file does not contain all used memory sections

## Module version 6.2.21

2018-08-08

- ▶ Implemented: A configuration parameter (SpinlockAllocationStrategy) that makes it possible to allocate spinlocks with different strategies
- ▶ ASCRTE-5387 Fixed known issue: The Rte generator fails with an error if an event with activationReasonRepresentation is non-exclusively mapped to a task
- ▶ Improved: The Rte shall find the NvMBlockDescriptor entry of an NvBlockDescriptor via name matching if no RteNvmBlockRef exists for it
- ▶ ASCRTE-5378 Fixed known issue: The Rte does not set Xfrm(Inv)TransformerBswModuleEntryRef correctly if name clashes during the allocation of XfrmImplementationMappings occur

## Module version 6.2.20

2018-07-27

- ▶ Implemented basic task support for Rte and Bsw timing events mapped to the same task in case OneScheduleTablePerPartition is enabled
- ▶ ASCRTE-5328 Fixed known issue: The Rte might not send the result of a server runnable that is started by multiple OperationInvokedEvents which are mapped to the same task
- ▶ Changed queueing behavior for asynchronous result-free client-server communication by permitting multiple outstanding server invocations for the same client

- ▶ Added new optional configuration parameters RteServerQueueLength and RteBswServerQueueLength as defined by AUTOSAR RfC #79150
- ▶ Added configuration check for BSW mode communication with mode users on multiple partitions
- ▶ Added support for configurable type of buffer length for transformers

## Module version 6.2.19

2018-06-28

- ▶ Updated the included memory mapping header file for Bsw module entities in the sense of [SWS\_Rte\_07830], i.e. <Msn>[\_<vi>\_<ai>]\_MemMap.h
- ▶ Added support for InitEvents triggering runnable entities via OS task
- ▶ Removed error message RTE\_59 which states that the implementation selection could not be found although there is only one SwcImplementation available and thus does not require a configuration of the RteImplementationRef parameter
- ▶ Implemented vendor-specific MetaData support of type SOCKET\_CONNECTION\_ID\_16 for inter-ECU client-server communication
- ▶ ASCRTE-5259 Fixed known issue: The Rte overwrites the complete RamBlock despite a configured Text-TableMapping if two PR-Ports are connected
- ▶ ASCRTE-5151 Fixed known issue: The Rte doesn't allocate consistency mechanism for atomic type in receiver buffer despite enabled serializer
- ▶ ASCRTE-5269 Fixed known issue: The Rte\_NvMData.h may declare parameter buffers as extern and static if the calibration parameter uses a ServiceDependency
- ▶ Removed the declaration of a runnable entity from the Application Header File, if a SwcBswRunnableMapping exists for it (AUTOSAR RfC 79717)
- ▶ Added support for swAddrMethod respectively swDataDefProps in 'C' typed PerInstanceMemory
- ▶ ASCRTE-5214 Fixed known issue: If a server runnable handles multiple operations with an array argument of different length, the argument's data may be lost
- ▶ ASCRTE-5274 Fixed known issue: The Rte Generator may generate duplicate event names if Bsw communication with multiple internal behaviors is used
- ▶ Implemented check for LdCom usage without DataTransformation
- ▶ Removed reporting of message RTE\_612 in case of UINT8\_N or UINT8\_DYN signal type
- ▶ Implemented support for NvM API runnables that can now be mapped to a task
- ▶ Changed 'Required' attribute in EventMapping-Tab of Rte Editor for BswOperationInvokedEvents and BswInternalTriggerOccuredEvents to false

- ▶ ASCRTE-5294 Fixed known issue: The Rte generates an insufficient counter data type for queued Rte or Bsw trigger communication if the configured queue length is 256
- ▶ ASCRTE-5297 Fixed known issue: The Rte generates an insufficient counter data type for data filter oneEveryN if the configured period is 256
- ▶ Fixed known issue: Opening and Closing of 'NVRAM Allocation' of the Rte Editor erases the NVRAM Allocations made in the Generic Editor if no Service Dependency exists for the mapping
- ▶ ASCRTE-5272 Fixed known issue: The NvBlockDescriptor's RunnableEntity for storeImmediate is triggered too often
- ▶ Implemented: The Rte shall find the NvMBlockDescriptor entry of an NvBlockDescriptor via name matching if no RteNvmBlockRef exists for it

## Module version 6.2.18

2018-05-25

- ▶ Improved memory usage for mixed implicit and explicit sender-receiver communication; the explicit Rte\_Read API now reads from an existing data handle buffer under certain circumstances
- ▶ ASCRTE-5161 Fixed known issue: The optional transformerError argument is not considered for the intra-ECU asynchronous Rte\_Call() API
- ▶ ASCRTE-5136 Fixed known issue: Under certain conditions the Rte generates an invalid signature for Rte\_ComHook\_<ComSignal>\_SigTx/SigRx trace functions
- ▶ Implemented support of maps and curves (compound primitives)
- ▶ ASCRTE-5200 Fixed known issue: Floating point constants cause compiler warning
- ▶ Implemented support of transport protocol for serialization
- ▶ ASCRTE-5167 Fixed known issue: The Rte calls a runnable in a task of another partition or falsely reports error RTE\_571 when software components mapped to different partitions have compatible timing events mapped to the same task
- ▶ ASCRTE-5252 Fixed known issue: Dirty Flag storeImmediate NvBlockDescriptor's RunnableEntity is triggered too early

## Module version 6.2.17

2018-04-20

- ▶ Added support for providing the activating event of an ExecutableEntity
- ▶ ASCRTE-5202 Fixed known issue: The generated Rte may not compile if an inter-partition BswOperationInvokedEvent is mapped to a task with many other events

- ▶ ASCRTE-5218 Fixed known issue: The Rte Generator may generate an insufficient buffer in a Com call-back for a multiple receiver transformer scenario
- ▶ ASCRTE-5165 Fixed known issue: The generated Rte BSWMD file contains non-existing measurement symbols if IOC partitioning support is enabled
- ▶ Implemented checks for LdCom usage without DataTransformation and for configuring the same ISignal-ToIPduMapping for multiple Com/LdCom containers.
- ▶ Improved the Rte generator with ClientIdDefinitionSet support for inter-ECU client-server communication
- ▶ ASCRTE-5049 Fixed known issue: The Rte generates non-compilable code if all BSW module instances are mapped to the same partition and the RteGeneratorOutput is BSW\_SCHEDULER\_ONLY

## Module version 6.2.16

2018-03-16

- ▶ Added support for setting the value of Rte\_TransformerClass to RTE\_TRANSFORMER\_UNSPECIFIED and Rte\_TransformerErrorCode to E\_OK for transformer error argument of an Rte Api to which no data transformation applies
- ▶ ASCRTE-5129 Fixed known issue: The transformer error parameter for the Rte\_Write()/Rte\_Send()/Rte\_Call()/Rte\_Trigger() function is not updated when the partition is not active
- ▶ Implemented support for multiple Com instances
- ▶ ASCRTE-4991 Fixed known issue: The generated Rte code may cause an OS\_E\_INTDISABLE error if the Det checks are enabled
- ▶ Improved the check for the error message RTE\_803
- ▶ Removed Permanent RAM Block consideration for SwcServiceDependency / RoleBasedDataAssignment roles different to "ramBlock" or "ramMirror"

## Module version 6.2.15

2018-02-23

- ▶ ASCRTE-5112 Fixed known issue: The Rte Generator may fail if multiple IRead APIs are configured for the same data element and no initial value is specified
- ▶ Removed unused sender-receiver buffers in case only implicit read accesses and no write accesses are configured
- ▶ ASCRTE-4993 Fixed known issue: The Rte verifier does not consider the default value of the swImplPolicy
- ▶ Improved Rte Generator by ignoring unmapped runnables for send/receive signal/signal group data consistency. The new warnings RTE\_610 and RTE\_611 are reported for affected signals/signal groups



- ▶ ASCRTE-5089 Fixed known issue: The Event Mapping tab of the Rte Editor reports a `NullPointerException` if a `BswOperationInvokedEvent` is configured
- ▶ Improved `NvBlockSwComponentType` dirty flag support: A runnable in the context of a `NvBlockSwComponentType` can now handle multiple `NvRamBlocks`
- ▶ Improved Rte Generator to support LINEAR CompuMethods with a factor equal to zero
- ▶ ASCRTE-5080 Fixed known issue: The values of the predefined error codes are generated with a cast to the type `Std_ReturnType`
- ▶ Improved the filtering in Rte Editor for required event mapping for `DataReceivedEvent(s)` of `NvBlockSwComponentType(s)`
- ▶ Improved generation of function like macro `Rte_WaitGetClearEvent` by calling the Rte trace hooks even if Os defines its own `WaitGetClearEvent` function
- ▶ Improved receive buffer sharing for explicit intra-partition 1:n sender-receiver communication
- ▶ Added support for debounced activation of executable entities
- ▶ ASCRTE-5135 Fixed known issue: BSW Os task shutdown may be delayed
- ▶ Improved data conversion by considering the `baseType` of the `NetworkRepresentationProps`

## Module version 6.2.14

2018-01-19

- ▶ Improved generation of very large numerical value specifications and data filter attributes
- ▶ Improved the Rte generator to accept a missing `BswImplementation.ResourceConsumption` that is mandatory by means of the strict AUTOSAR schema
- ▶ ASCRTE-5066 Fixed known issue: The Rte generator may fail if the number of elements of an `ApplicationRecordDataType` of a require port is less than the number of signals of the signal group mapped to the provide port
- ▶ ASCRTE-4691 Fixed known issue: The timing event execution offset may be shifted after a partition restart

## Module version 6.2.13

2017-12-20

- ▶ ASCRTE-5019 Fixed known issue: The generated Rte may not compile if the Det checks are enabled and multiple clients call the same server by a direct function call
- ▶ Added support for external replacement invalidation for sender-receiver communication
- ▶ Added support for handling of Section Name Prefix for `BswSchedulableEntitys`

- ▶ ASCRTE-4965 Fixed known issue: The Rte reports an error if a variable size array is initialized with an application value specification without a constant mapping

## Module version 6.2.12

2017-12-11

- ▶ ASCRTE-4979 Fixed known issue: The Rte contract phase does not compile if implicit sender-receiver communication using array type is configured
- ▶ Improved Rte generation time for data types, especially for single-pointered or double-pointered calibration parameter reference tables
- ▶ ASCRTE-4995 Fixed known issue: The Rte generator fails if an operation argument is named "Status" and inter-partition-intra-ECU client-server communication is used
- ▶ ASCRTE-4975 Fixed known issue: Subsequent calls of Rte\_Call() always return RTE\_E\_LIMIT if RTE\_E\_HARD\_TRANSFORMER\_ERROR occurred in the result signal callback (inter-partition)
- ▶ ASCRTE-4689 Fixed known issue: Com\_SendSignal/Com\_SendSignalGroup function may be accessed concurrently for the same Com signal/Com signal group
- ▶ ASCRTE-4973 Fixed known issue: Os schedule table offset is not always taken into account in case of OSEK compatibility mode
- ▶ Updated Memory Mapping: No additional type qualifier is added to the Memory Allocation Keyword if a SwAddrMethod is configured
- ▶ Added support for implicit communication for directly called ModeSwitchEvent runnables in the context of asynchronous mode communication

## Module version 6.2.11

2017-11-17

- ▶ ASCRTE-4976 Fixed known issue: The Rte generator fails if PossibleErrorRefs are used for asynchronous inter-partition-inter-ECU client-server communication
- ▶ Removed falsely reported warnings for constr\_9082
- ▶ ASCRTE-5010 Fixed known issue: The access to the event bit mask of the internal Rte event buffer mechanism may be out-of-bounds
- ▶ ASCRTE-4326 Fixed known issue: Some inter-partition channels are not protected against concurrent access
- ▶ Added support for intra partition Bsw client/server calling chains
- ▶ ASCRTE-4969 Fixed known issue: Enumerations of ImplementationDataTypeElements of category VALUE are not considered

## Module version 6.2.10

2017-10-20

- ▶ Improved task mapping verification for trigger communication
- ▶ ASCRTE-4948 Fixed known issue: The Rte generates non-compilable code in case of intra-partition sender-receiver communication with complex data type and handleInvalid set to REPLACE
- ▶ ASCRTE-4964 Fixed known issue: Subsequent calls of Rte\_Call() always return RTE\_E\_LIMIT if RTE\_E\_HARD\_TRANSFORMER\_ERROR occurred in the result signal callback

## Module version 6.2.9

2017-10-06

- ▶ ASCRTE-4846 Fixed known issue: The Rte\_Enter and Rte\_Exit API return a non-void value if OsResource is used and function elision is enabled
- ▶ ASCRTE-4782 Fixed known issue: The RTE generates incorrect code for sender/receiver communication with transformers and partial records but no subElementMappings
- ▶ ASCRTE-4928 Fixed known issue: The Rte does not propagate the transformerError to Rte\_Read() for unqueued inter-partition inter-ECU communication
- ▶ Added support for implicit communication for directly called triggers
- ▶ ASCRTE-4924 Fixed known issue: Subsequent calls of Rte\_Call always return RTE\_E\_LIMIT if RTE\_E\_HARD\_TRANSFORMER\_ERROR or RTE\_E\_COM\_STOPPED was returned before
- ▶ ASCRTE-4847 Fixed known issue: Implicit inter-runnable variable buffer is not updated for a RunnableEntity that is mapped to a BswModuleEntity and started by a BSW event
- ▶ Implemented support for measurement of Nv RAM Block and non volatile data communication
- ▶ Added dirty flag support for NvBlockSwComponentType
- ▶ Added support for the Rte\_NvMNotifyJobFinished callback
- ▶ ASCRTE-4899 Fixed known issue: The Rte does not generate any code if an invalid value is specified for a deeply nested element
- ▶ Implemented memory mapping initialization strategy
- ▶ ASCRTE-4925 Fixed known issue: The Rte may generate a non-existing memory section in the SchM module interlink header
- ▶ Added support for Bsw client/server communication

## Module version 6.2.8

2017-08-25

- ▶ ASCRTE-4830 Fixed known issue: An out of bounds access may occur in the APIs Rte\_Enter / Rte\_Exit if the Det check is enabled in the Rte
- ▶ ASCRTE-4850 Fixed known issue: The Rte Generator fails if a blocking Rte\_Result without timeout references an asynchronous server call point with a timeout
- ▶ ASCRTE-4893 Fixed known issue: The Rte\_Main.h may declare parameter buffers as extern and static if partitioning is enabled
- ▶ Added support for SenderReceiverToSignalGroupMapping/SenderRecArrayTypeMapping to byte arrays
- ▶ Added support to configure if the Os supports OsSpinlockLockMethod which allows the Rte to generate optimized spinlocks
- ▶ ASCRTE-4860 Fixed known issue: The Rte Editor configures wrong trace hook function names for BSW Schedulable entities
- ▶ ASCRTE-4892 Fixed known issue: The Rte Generator fails if partitioning is disabled and a basic software module instance or software component instance is mapped to an Os application
- ▶ Added deviation to document that the Rte allows multiple inter-partition synchronous client server calls for the same client

## Module version 6.2.7

2017-07-28

- ▶ Improved sharing of inter-partition S/R channels
- ▶ ASCRTE-4834 Fixed known issue: The Rte generator may apply a wrong event setting mechanism in case of inter-partition inter-ECU client-server communication
- ▶ Removed unused Rte\_SetEvent macros from Rte\_Intern<Partition> header files
- ▶ ASCRTE-4857 Fixed known issue: ParameterElementGroup types are not generated in Rte\_Main.h
- ▶ ASCRTE-4773 Fixed known issue: The Rte\_Read API does not overwrite the whole OUT parameter while reading from a NvRamBlockElement with a bitfield texttable mapping

## Module version 6.2.6

2017-06-30

- ▶ ASCRTE-4737 Fixed known issue: Rte could report a misleading error message for events with mode-disabling dependencies in certain conditions
- ▶ ASCRTE-4687 Fixed known issue: The Rte\_Read API does not protect receiving of a Com signal group against concurrent access if DirectReadFromCom applies
- ▶ ASCRTE-4781 Fixed known issue: The Rte considers a sender/receiver partial record communication as incompatible if the matching structure elements are not in the same order

- ▶ Added user-defined exclusive area implementation mechanism
- ▶ ASCRTE-4768 Fixed known issue: The RTE ignores the PortAPIOption errorHandler for the generation of direct function calls to a server runnable
- ▶ Added support for intra-ECU sender-receiver communication with data transformation
- ▶ ASCRTE-4819 Fixed known issue: Implicit S/R communication is not working for a RunnableEntity that is mapped to a BswModuleEntity and started by a BSW event

## Module version 6.2.5

2017-05-31

- ▶ ASCRTE-4747 Fixed known issue: The Rte Generator may fail with an OutOfMemoryError if many direct server calls are configured
- ▶ ASCRTE-4802 Fixed known issue: The Rte may falsely report the error RTE\_538 during execution of the SvcAs
- ▶ Improved the order of intra-partition unqueued sender/receiver receive buffers definitions to reduce the amount of MemMap.h inclusions

## Module version 6.2.4

2017-05-05

- ▶ Removed filter flag from sender/receiver buffer
- ▶ Added support for Os alarm and schedule table allocation with Os counter on different core. This allows to use a single Os counter on a partitioned multi-core system
- ▶ ASCRTE-4665 Fixed known issue: Rte\_Read does not return the initial value of a complex inter-ECU communication data under certain conditions
- ▶ ASCRTE-4683 Fixed known issue: The Rte does not send any data if the S/R data queue overflows in case of inter-partition-inter-ECU communication
- ▶ ASCRTE-2026 Fixed known issue: If inter-partition mode management is used with mode disabling dependencies, the Rte may not compile
- ▶ ASCRTE-4695 Fixed known issue: The synchronous client may not wait for the server to finish while an exclusive area is active
- ▶ ASCRTE-4760 Fixed known issue: The Rte Generator fails if a structure ImplementationDataType contains a pointer element with an invalid target type
- ▶ Removed the receive buffer structure data types for intra-partition unqueued sender/receiver communication. Each receive buffer element is now a discrete global variable

- ▶ Updated the Rte Verifier to report an error if a BSW requiredTrigger is connected to more than one BSW providedTriggers

## Module version 6.2.3

2017-03-31

- ▶ Added support for autonomous error reaction for inter-ECU client/server communication with transformers
- ▶ Added support for TransformerHardErrorEvents for inter-ECU client/server communication
- ▶ ASCRTE-4698 Fixed known issue: DataReceiveErrorEvents are not triggered if inter-ECU S/R invalidation is handled by the Rte and no data receive point exists
- ▶ ASCRTE-4651 Fixed known issue: The Rte may not compile if a schedulable entity and a runnable entity with a blocking API are mapped to the same task
- ▶ ASCRTE-4692 Fixed known issue: The Rte doesn't create the memory section in the Rte\_Bswmd.arxml for a RamBlock of a NvBlockSwComponentType
- ▶ ASCRTE-4704 Fixed known issue: The Rte generates uncompileable MemMap definitions if SectionInitializationPolicies are used according to AUTOSAR specifications
- ▶ Improved data consistency by locking the interrupts when a spinlock is used
- ▶ ASCRTE-4621 Fixed known issue: The RTE does not provide NULL to all transformers in a chain if executeDespiteDataUnavailability is enabled
- ▶ Added support for bitfield mapping of multiple require variable data prototypes to one provide variable data prototype
- ▶ Implemented Asynchronous Mode Switch
- ▶ Added allocation of init and invalid values for ComGroupSignals and ComRxDataTimeoutAction for ComGroupSignals/ComSignals via the service needs assistant
- ▶ Removed unused Rte\_IsModeDisablingDepSet functions from Rte\_<partition>.c files

## Module version 6.2.2

2017-03-03

- ▶ ASCRTE-4064 Fixed known issue: Implicit communication with data conversion is not working for send signals/signal groups
- ▶ Improved measurement support for inter-partition sender/receiver communication in conjunction with the SharedMemory communication mechanism
- ▶ ASCRTE-4664 Fixed known issue: The Rte Editor does not provide available NvM blocks

- ▶ Improved queued triggering to use basic task where extended task is not mandatory
- ▶ Improved generated code relaxing the restriction regarding the SwcInternalBehavior in case of NvBlock-SwComponentType
- ▶ ASCRTE-4531 Fixed known issue: DataFilter oneEveryN is not taking into account the offset and period properly

## Module version 6.2.1

2017-02-03

- ▶ ASCRTE-4131 Fixed known issue: The Rte does not consider the initial value for an unconnected parameter port and reports an error
- ▶ ASCRTE-4547 Fixed known issue: The Rte does not generate a Rte\_IrTrigger API for each RunnableEntity if the InternalTriggeringPoints have equal short names
- ▶ ASCRTE-4599 Fixed known issue: An exclusive area which uses COOPERATIVE\_RUNNABLE\_PLACEMENT may be disabled by another exclusive area which uses OS\_RESOURCE
- ▶ Improved generated code for Rte\_Feedback API in case "function elision" is enabled
- ▶ Added custom constraint checks for LINEAR CompuMethods in the sense of constr\_1375
- ▶ Improved task mapping scenarios for runnables with a synchronous server call point that is implemented as direct function call. The runnable may now be mapped to a basic task regardless of the configured timeout value
- ▶ ASCRTE-4409 Fixed known issue: The component data structure may not be initialized if the SWC contains two provide ports which refer the same trigger interface
- ▶ Updated the inter-partition mode receive emptyQueue

## Module version 6.1.172

2017-01-05

- ▶ Added support for 64 bit datatypes in sender/receiver, client/server and inter-ECU communication

## Module version 6.1.171

2016-12-14

- ▶ ASCRTE-4554 Fixed known issue: DataReceiveErrorEvents are not triggered in case of inter-partition inter-ECU S/R invalidation

- ▶ Consider memory mapping for SMC inter-partition communication
- ▶ ASCRTE-4525 Fixed known issue: DataFilter newIsWithin is more restrictive than specified
- ▶ ASCRTE-4528 Fixed known issue: TaskType BASIC leads to an extended task for Task Mapping Scenario B1
- ▶ ASCRTE-4551 Fixed known issue: If a timeout is specified for an asynchronous server callpoint and no waitpoint is defined, the Rte Generator does not setup an alarm in Rte\_Call
- ▶ ASCRTE-4558 Fixed known issue: The Rte generator fails for inter-ECU client/server communication if an ApplicationRecordDataType argument is mapped to an ImplementationDataType argument
- ▶ Improved merge of OsEvents for multiple TriggerOccuredEvents
- ▶ ASCRTE-4573 Fixed known issue: The Rte generator fails if a complex ApplicationArrayDataType with variable size semantics is used
- ▶ ASCRTE-4483 Fixed known issue: The Rte generates empty Rte\_Invalidate API in case of complex inter-ECU sender/receiver communication
- ▶ ASCRTE-4524 Fixed known issue: Invalidation of primitive inter-partition inter-ECU sender/receiver communication is not working
- ▶ ASCRTE-4553 Fixed known issue: Inter-ECU S/R invalidation of signal groups does not consider the parameter InterECUInvalidationHandledByRte
- ▶ ASCRTE-4482 Fixed known issue: The Rte generates a non-blocking instead of a blocking Rte\_Feedback API under certain conditions
- ▶ Implemented function elision for the Rte\_Trigger and Rte\_IrTrigger API
- ▶ Added support for memory mapping considering SwAddrMethod, SwAlignment and mapping of RunnableEntities in the Application Header File
- ▶ Improved declaration of shared memory API in Smc.h by generating partition-specific header files

## Module version 6.1.170

2016-11-08

- ▶ ASCRTE-3346 Fixed known issue: The Rte does not generate Rte\_Main.[ch] if partitioning is enabled but only one partition is configured
- ▶ ASCRTE-4434 Fixed known issue: If COOPERATIVE\_RUNNABLE\_PLACEMENT is configured for an exclusive area, the Rte might allocate more than one internal resource for a task
- ▶ ASCRTE-4176 Fixed known issue: Port interface mappings may not be applied for client server interfaces
- ▶ ASCRTE-4497 Fixed known issue: If limits are defined for a signed data type, the Rte generates an unsigned suffix
- ▶ Fixed violation for MISRA rule 8.8 by moving external declaration of implicit buffers to Rte\_Partitioning.h



- ▶ ASCRTE-4210 Fixed known issue: The Rte Editor may create duplicate Bsw module instances
- ▶ Added support for asynchronous inter-ECU client server communication
- ▶ ASCRTE-4532 Fixed known issue: The Rte generates an invalid EventMaskType typedef under certain circumstances
- ▶ Extended data consistency for inter-core C/S communication related to bugfix ASCRTE-4445
- ▶ Fixed MISRA rule 5.5 violations which are related to the partition state and execution counter variables
- ▶ ASCRTE-4110 Fixed known issue: Some APIs do not consider data conversion correctly
- ▶ Implemented memory mapping by the size of intra-partition variables if no explicit SwAddrMethod is given
- ▶ Added inter-partition-inter-ECU-inter-core communication optimization: one Com signal IP channel for each core
- ▶ ASCRTE-4388 Fixed known issue: The Rte expects a parameter data prototype mapped to a per instance memory in the same RoleBasedDataAssignment
- ▶ Improved verifier for constr\_1094 in case of runnables of NvBlockSwComponentType

## Module version 6.1.169

2016-10-10

- ▶ Improved the resolution of SwDataDefProps for implementation data types of category TYPE\_REFERENCE.
- ▶ ASCRTE-4487 Fixed known issue: The Rte uses a wrong buffer size for Com/LdCom signal reception callbacks when client/server inter-ECU communication is used
- ▶ Relaxed the verifier for implementation data types with the same short name and DataConstr between C/S arguments
- ▶ Added configuration support for trigger queue lengths in the Rte Editor
- ▶ Added support for SubElementMapping for inter-ECU S/R Data Serialization

## Module version 6.1.168

2016-09-09

- ▶ Added support for sharing implicit S/R communication buffers (data handle buffers) between partitions
- ▶ ASCRTE-4590 Fixed known issue: The elided Rte\_Write API macro does not update data handle buffers of implicit readers
- ▶ ASCRTE-4389 Fixed known issue: The Rte Editor may falsely warn that the schedule table expiry points exceed 5000 entries

- ▶ ASCRTE-3906 Fixed known issue: The Rte does not consider the activation mechanism for the SchM schedule table

## Module version 6.1.167

2016-08-05

- ▶ ASCRTE-4424 Fixed known issue: The Rte Generator may fail if a runnable is started by multiple OperationInvokedEvents for different client/server operations
- ▶ Added queueing support for the SchM\_ActMainFunction API
- ▶ ASCRTE-4433 Fixed known issue: The Rte Generator fails if incompatible inter-ECU client/server operations are called
- ▶ ASCRTE-4337 Fixed known issue: The runnable based scope for application header files might not detect a wrong runnable context
- ▶ Improved inter-partition intra-core synchronous Rte\_Call to not wait for Os events anymore if the client's task priority is lower than the server's task
- ▶ ASCRTE-4426 Fixed known issue: The Rte Bswmd generation fails if measurement is enabled on a PR-PortPrototype
- ▶ ASCRTE-4443 Fixed known issue: The Rte uses a wrong buffer size for Com signal reception callbacks when S/R inter-partition/inter-ECU communication is used
- ▶ ASCRTE-4444 Fixed known issue: The Rte Generator might not set the isUpdated flag in a Com/LdCom signal reception callback
- ▶ ASCRTE-4445 Fixed known issue: Inter-core inter-partition shared-memory communication wrongly shares Os resources
- ▶ ASCRTE-4384 Fixed known issue: The Rte resets Bsw modes during Rte\_Start

## Module version 6.1.166

2016-06-30

- ▶ ASCRTE-4360 Fixed known issue: The Rte Bswmd generation fails if an McDataInstance refers a CompuMethod without a Unit
- ▶ ASCRTE-4385 Fixed known issue: Implicit InterRunnableVariable communication might not work
- ▶ ASCRTE-4387 Fixed known issue: The enhanced Rte\_Mode API returns a wrong mode if the mode provider partition is not started and the ModeDeclarationGroup has category EXPLICIT\_ORDER
- ▶ Removed unused global buffers for unaccessed parameter data prototypes
- ▶ ASCRTE-4397 Fixed known issue: The Rte may not apply data consistency for inter-core SMC channels

- ▶ Added support for queued SchM\_Trigger API
- ▶ ASCRTE-4362 Fixed known issue: The Rte might use the same Os event for an Bsw and Rte event
- ▶ ASCRTE-4393 Fixed known issue: Shared memory with inter-core-inter-ECU transformer leads to non-compilable code
- ▶ ASCRTE-4412 Fixed known issue: The Rte\_Trigger API does not return a value for an unconnected queued trigger

## Module version 6.1.165

2016-06-07

- ▶ Added use of SvcAs feature to configure VariableLength parameter of locChannels
- ▶ ASCRTE-4329 Fixed known issue: Possible infinite wait in inter-core synchronous Rte\_Call
- ▶ Added use of SvcAs feature to configure XfrmOsApplicationRef parameter of XfrmImplementationMappings
- ▶ ASCRTE-4288 Fixed known issue: Rte\_Read might return RTE\_E\_OK although the API is unconnected
- ▶ Added support of SubElementMapping with Transformer
- ▶ Improved asynchronous inter-partition client/server communication so that the client runnable may now be mapped to a basic task
- ▶ ASCRTE-4358 Fixed known issue: A client-server request's sequence counter value may be truncated
- ▶ Added buffer length check for copying raw data to local buffer for re-transformation in (Ld)Com callback

## Module version 6.1.164

2016-04-29

- ▶ ASCRTE-4279 Fixed known issue: Software component without Internal Behavior results in the failure of RTE
- ▶ Improved allocation of XfrmImplementationMapping so that only a single entry for the same tx signal (group) path is created and XfrmVariableDataPrototypeInstanceRef is left disabled
- ▶ ASCRTE-4200 Fixed known issue: The verifier wrongly reports an error about incompatible categories between two ImplementationDataTypes
- ▶ Changed dependency to the Platforms plugin from mandatory to optional
- ▶ ASCRTE-4295 Fixed known issue: The Rte generator fails if calibration support of a ParameterSwComponentType is different than for the connected software component
- ▶ Added support for the AutosarVariableIRef instance reference in NvRamBlockElements
- ▶ Implemented relaxation of constr\_1175

- ▶ Added support for inter-partition/inter-ECU client server communication
- ▶ ASCRTE-3895 Fixed known issue: The enhanced Rte\_Mode API does not return the initial mode if no ModeSwitchEvents exist for inter-partition mode management
- ▶ Removed unused mode variable from the mode provider partition in case of inter-partition mode management with enhanced Rte\_Mode API
- ▶ ASCRTE-4317 Fixed known issue: The verifier wrongly reports an error if an ApplicationArrayDataType is mapped to a variable-size array ImplementationDataType
- ▶ Added support for variable-size arrays (VSA\_LINEAR) for inter-partition queued sender-receiver communication with SMC
- ▶ ASCRTE-4261 Fixed known issue: An Rte client server reception callback function may retransform invalid raw data

## Module version 6.1.162

2016-04-01

- ▶ ASCRTE-4243 Fixed known issue: The Rte generator fails if incompatible initial values are configured for certain data prototypes with measurement support
- ▶ ASCRTE-4223 Fixed known issue: Rte.c does not include NvM.h leading to compilation error when a software component uses NvMService through a NvBlockSwComponent
- ▶ Improved allocation of expiry points for timing events with an offset greater than 0
- ▶ ASCRTE-4252 Fixed known issue: The Rte does not configure the LdComRxIndication for client/server call signals
- ▶ ASCRTE-4248 Fixed known issue: An internal error is reported if a task chain is created of non periodic tasks
- ▶ ASCRTE-4233 Fixed known issue: The Rte creates nested MemMap defines
- ▶ ASCRTE-4269 Fixed known issue: ComIPChannel identifier defines may not be generated if senders are located on more than one non-Bsw partition
- ▶ Implemented the definition of partition specific and shared memory sections within the Rte's BSWMD
- ▶ ASCRTE-4260 Fixed known issue: The Rte does not consider invalid values on array application data types
- ▶ Shortened names of allocated XfrmImplementationMappings
- ▶ ASCRTE-4266 Fixed known issue: The Rte Generator does not generate code if inter-core C/S communication with SharedMemory is used
- ▶ ASCRTE-4281 Fixed known issue: If a task chain is configured, the Rte allocates expiry points for all tasks in the chain
- ▶ Added support for inter-partition/inter-ECU S/R communication

## Module version 6.1.161

2016-03-04

- ▶ Reworked generation of template/example code for each software component.
- ▶ Added comment next to global variables which shows the original name before MD5 hashing is applied
- ▶ Improved the verification of self referencing datatypes to check indirect referencing
- ▶ ASCRTE-4228 Fixed known issue: The Rte editor generates wrong Vfb trace hook function names for certain API functions

## Module version 6.1.160

2016-02-05

- ▶ Added use of SvcAs feature to configure LdComIPdus
- ▶ Added support for fire-and-forget (result-free) asynchronous client/server calls as specified by RfC 70295.
- ▶ Improved handling of the Rte basic software module description: The Basic Software Scheduler does not consider an existing Rte Bswmd as subject for code generation anymore

## Module version 6.1.159

2016-01-15

- ▶ ASCRTE-4069 Fixed known issue: The Rte editor may enable unused configuration parameters
- ▶ Removed Det RTE\_E\_DET\_UNINIT logging for the SchM\_Enter and SchM\_Exit Apis
- ▶ ASCRTE-4075 Fixed known issue: Incorrect warning regarding unconnected client server ports
- ▶ ASCRTE-4000 Fixed known issue: If transformerError argument exists the Rte template code does not compile
- ▶ ASCRTE-4072 Fixed known issue: Rte falsely reports an error when SwcServiceDependency is defined on a PortPrototype which is involved in Nv data management
- ▶ ASCRTE-4125 Fixed known issue: Under certain conditions the Rte does not generate enumeration definitions
- ▶ Adapted the validation of ModeRequestTypeMaps for support of multiple BswInternalBehaviors
- ▶ Updated the verification of constraint constr\_4071
- ▶ ASCRTE-4145 Fixed known issue: The measurement symbols exported to the Rte Bswmd file may not exist in the generated source code
- ▶ Added support for mixed usage of IOC for inter-core and Shared Memory Communicator (SMC) for intra-core communication

- ▶ Added system description verifier check for constr\_1386 (PortDefinedArgumentValue shall only be defined for AbstractProvidedPortPrototype)
- ▶ ASCRTE-4152 Fixed known issue: If inter-partition invalidation is used, it might happen that the receiver reads old data
- ▶ ASCRTE-4142 Fixed known issue: SchM\_Send and SchM\_Receive APIs to do not return a value when a port is unconnected
- ▶ ASCRTE-4114 Fixed known issue: DataSendCompletedEvents are not properly triggered under certain conditions
- ▶ ASCRTE-4159 Fixed known issue: An extended Os task may not be activated on startup
- ▶ ASCRTE-4178 Fixed known issue: The Rte Editor reports a NullPointerException under certain conditions

## Module version 6.1.158

2015-11-10

- ▶ Added use of SvcAs feature to configure Nv blocks and callbacks
- ▶ Changed parameter name XfrmTransformationBswModuleEntryRef for reading from the Xfrm Ecu configurations to XfrmTransformerBswModuleEntryRef (see AUTOSAR RfC #68531)
- ▶ Added support for inter-partition task chains and multiple task chains
- ▶ Improved configuration check regarding the mapping of Bsw events to tasks belonging to the configured Bsw Os application
- ▶ Improved error messages for value specifications that are not compatible to the data type
- ▶ ASCRTE-4068 Fixed known issue: The Rte editor does not show any error or warning under several conditions
- ▶ Extended verifier check for constr\_1075 (point 1.h is now checked according to RfC#67491/ASR 4.2.2)
- ▶ ASCRTE-4073 Fixed known issue: The Rte generator ignores initial values for an arTypedPerInstance-Memory defined by application value specifications
- ▶ ASCRTE-4071 Fixed known issue: A value specification for a multi-dimensional ApplicationArrayDataType can not be applied to a data prototype
- ▶ Added use of SvcAs feature to configure XfrmImplementationMappings
- ▶ ASCRTE-4080 Fixed known issue: Under certain conditions the Rte generator fails if measurement is enabled for a data prototype of complex application data type
- ▶ Removed erroneous warning if init value of primitive data element and mapped Com signal match
- ▶ ASCRTE-4041 Fixed known issue: The Rte writes array initial values to the Com configuration with an "U" suffix
- ▶ Updated the Rte Verifier to report a warning if a timeout is configured and LdCom is used

- ▶ Removed support for system signal/signal group mappings in the Rte editor. The functionality has been moved to the System Signal Mapping Editor.
- ▶ Removed Service Port Mapping tab from Rte editor. The functionality has been moved to the Connection Editor.
- ▶ Improved implicit read/write access for server runnables which are triggered by a direct call. The client(s) can be mapped on different tasks now as long as they do not preempt each other.
- ▶ Updated the Rte Verifier to report an error if a software component name conflicts with a standard Rte header file

## Module version 6.1.157

2015-10-09

- ▶ ASCRTE-3984 Fixed known issue: Inter-partition mode switch events may lead to a deadlock on startup
- ▶ Added serialization support for QUEUED data reception
- ▶ Added supported for queued Bsw sender receiver communication
- ▶ Added support for specification of XfrmVariableDataPrototypeInstanceRef (multiple receivers)
- ▶ ASCRTE-4016 Fixed known issue: If no initial value is specified for a C typed per instance memory, the Rte generates non-compilable code
- ▶ Added support for the use of NumericalValueSpecifications to define the invalidValue of an array data type
- ▶ Updated the definition of the Rte\_Cs\_TransactionHandleType according to RfC#69581
- ▶ ASCRTE-4012 Fixed known issue: Rte miscalculates buffer consumptions for transformers in case the CompuNumerator contains a linear factor
- ▶ ASCRTE-4014 Fixed known issue: Blocking Rte\_Receive may return undefined data even if the queue is empty
- ▶ Removed the Somelp returnValue parameter when a server runnable has no application errors
- ▶ ASCRTE-4023 Fixed known issue: The transformerError argument passed to a runnable may contain undefined values
- ▶ Updated the system description verifier to reject configurations where executeDespiteDataUnavailability=true and signal fan-in is configured
- ▶ Exchanged partition IDs which are used for the naming of Rte\_Smc\_Data\_<id>.c files with partition names
- ▶ Fixed the Rte verifier, which might wrongly report the warning that a category 2 runnable is not exclusively mapped to a task
- ▶ ASCRTE-4040 Fixed known issue: It is required to specify a DataTypeMap for single elements of an ApplicationCompositeDataType
- ▶ ASCRTE-4047 Fixed known issue: The Rte editor may remove data mappings from the system model

- ▶ Added support for the inclusion of multiple instances of a Bsw module into the same partition

## Module version 6.1.156

2015-08-24

- ▶ Added support for the generation of the Bsw Scheduler in multiple partitions
- ▶ ASCRTE-3974 Fixed known issue: Undefined behavior for mode disabling dependencies which are defined in multiple partitions
- ▶ Added support for C/S calls to NvMServices
- ▶ Added verifier checks for Variable-Size Array ImplementationDataTypes
- ▶ ASCRTE-3983 Fixed known issue: An Rte\_Call function may not dereference an INOUT operation argument
- ▶ ASCRTE-3923 Fixed known issue: Rte template code does not compile
- ▶ ASCRTE-3980 Fixed known issue: If the Rte contract phase is executed from the command line, the data type verifier is not executed
- ▶ ASCRTE-3905 Fixed known issue: The Rte does not consider the receiver's initial value if the invalidation policy is set to REPLACE
- ▶ ASCRTE-3991 Fixed known issue: High memory consumption when many complex application data types are defined
- ▶ ASCRTE-3955 Fixed known issue: Missing critical section for inter-partition-inter-ECU communication

## Module version 6.1.155

2015-07-21

- ▶ ASCRTE-3933 Fixed known issue: The Rte Editor does not properly handle data mappings of application array data types within other complex types
- ▶ Updated verification check for constr\_1060 according to RfC#68247
- ▶ ASCRTE-3989 Fixed known issue: Rte Api function arguments may reference an undefined data type
- ▶ ASCRTE-3963 Fixed known issue: The Rte verifier incorrectly evaluates the invalidation policy of sender receiver interfaces
- ▶ ASCRTE-3961 Fixed known issue: If an array of a structure implementation data type is specified the generated code does not compile



## Module version 6.1.154

2015-06-19

- ▶ ASCRTE-3725 Fixed known issue: If the invalid value equals the init value, Rte\_IStatus()/Rte\_Read() returns RTE\_E\_OK for inter-partition invalidation
- ▶ ASCRTE-3798 Fixed known issue: A McDataInstance entry is not generated for ParameterDataPrototype elements of a ParameterSwComponentType
- ▶ ASCRTE-3823 Fixed known issue: Rte\_Mode does not return the initial mode if no mode switch point exists for the provided mode declaration group
- ▶ ASCRTE-3705 Fixed known issue: If an executable entity runs inside several exclusive areas with different interrupt locking mechanisms, the Rte considers only one
- ▶ ASCRTE-3836 Fixed known issue: The Rte Editor does not store event mappings if software component and basic software instance names are ambiguous
- ▶ ASCRTE-3862 Fixed known issue: The Rte incorrectly requires the mapping of operation invoked events
- ▶ Removed the additional measurement buffer in case of 1:1 intra-partition sender/receiver communication
- ▶ Replaced buffer data structure by flat hierarchy
- ▶ ASCRTE-3852 Fixed known issue: The initial value of a data element may be wrong in case of unqueued 1:n inter-partition sender/receiver communication
- ▶ Improved the Rte Bswmd generator to only modify the system model (file SystemModel2.tdb) if necessary
- ▶ ASCRTE-3535 Fixed known issue: The Rte\_Read API may return the last received value from Com instead of the initial value if HandleTimeoutType is set to REPLACE
- ▶ ASCRTE-3867 Fixed known issue: Rte\_Write does not write to a subelement of a complex Nv ram block if the RootVariableDataPrototype is not set
- ▶ Removed constraint check constr\_3131 (see RfC#69207)
- ▶ ASCRTE-3870 Fixed known issue: The generated Rte code does not compile if background events are used with partitioning enabled
- ▶ ASCRTE-3776 Fixed known issue: The Rte\_Read API returns RTE\_E\_INVALID if the invalidation policy is set to replace and an alive timeout is specified or handle never received is enabled
- ▶ ASCRTE-3769 Fixed known issue: The Rte may still activate runnable entities on extended tasks after Rte\_Stop has been called
- ▶ ASCRTE-3830 Fixed known issue: The Rte generates duplicate internal IDs for development error tracing if two software components define the same API function
- ▶ ASCRTE-3814 Fixed known issue: Inter-partition S/R communication lead to compiler warnings if several receivers use different structure types
- ▶ ASCRTE-3689 Fixed known issue: Mixing of enhanced and non-enhanced Rte\_Mode API in inter-partition mode management fails

- ▶ ASCRTE-3290 Fixed known issue: The Rte may incorrectly report the error message RTE\_503
- ▶ ASCRTE-3875 Fixed known issue: The Rte does not stop the BSW inter-partition task
- ▶ ASCRTE-3913 Fixed known issue: The Rte ignores incoming events during a blocking API call
- ▶ ASCRTE-3931 Fixed known issue: If an array implementation data type is referenced by an implementation data type of category TYPE\_REFERENCE an internal error is reported

## Module version 6.1.153

2015-04-27

- ▶ ASCRTE-3287 Fixed known issue: Sender receiver record element mappings to array implementation data type elements will not be recognized by the Rte editor
- ▶ ASCRTE-3799 Fixed known issue: The BSW main function auto-mapping functionality of the Rte editor does not re-create removed Os tasks
- ▶ ASCRTE-3800 Fixed known issue: Inter-partition inter-ECU sender-receiver signal fan-out does not work
- ▶ ASCRTE-3808 Fixed known issue: The Rte generator fails if a base type without a native declaration is used
- ▶ Added support for constant (e.g. initial) value specifications for complex application data types using RecordValueSpecification and ArrayValueSpecification
- ▶ Added support for inter-ECU client server communication
- ▶ ASCRTE-3819 Fixed known issue: Compiler error related to the definition of the data type Rte\_TransformerError
- ▶ ASCRTE-3812 Fixed known issue: The inter partition Rte\_Result API returns the wrong value after starting the partition
- ▶ ASCRTE-3821 Fixed known issue: The Rte may pass invalid data to the LdCom
- ▶ Added support for network representation conversion of complex sender-receiver data elements.
- ▶ Modified the allocation of the Rte\_Result Api, an AsynchronousServerCallResultPoint is now required to generate the Rte\_Result Api
- ▶ ASCRTE-3827 Fixed known issue: The Bsw Os Task may be unnecessarily activated during Rte\_Start
- ▶ ASCRTE-3820 Fixed known issue: The Rte shall support definitions of limits in certain numerical formats
- ▶ Added support for SubElementMappings of VariableAndParameterInterfaceMappings

## Module version 6.1.152

2015-02-23

- ▶ ASCRTE-3662 Fixed known issue: Update flag is not cleared when Rte\_RestartPartition is called
- ▶ Added support for Internal Trigger Event Communication
- ▶ Implemented call of Com[Send/Receive]DynSignal only for ComSignalType UINT8\_DYN
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: usage of LdCom only)
- ▶ Corrected generated code for Sender / Receiver Serialization (use-case: LdComCbkTriggerTransmit)
- ▶ ASCRTE-3702 Fixed known issue: The Rte generator aborts with a fatal error if a Bsw trigger event is configured
- ▶ ASCRTE-3701 Fixed known issue: The Rte\_Write API behaves like the Rte\_Invalidate API if invalidation is configured and the invalid value is sent
- ▶ Added OS\_SPINLOCK as possible parameter for selection of exclusive area implementation mechanism.
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: transformerErrorHandling enabled)
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: transformerError prioritization)
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: Transformer Soft/Hard error in Rte\_Feedback/IStatus )
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: macro definition for RTE\_E\_-TRANSFORMER\_LIMIT)
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: consistent (de-)serialization error in case of multiple receiver software components)
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: compiler warning in case of (de-)serialization and array data types)
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: startup behavior (Rx - RTE\_E\_-NEVER\_RECEIVED+InitValues, Tx - RteFeedback))
- ▶ Improved generated code for Sender / Receiver Serialization (use-case: transformerError arg enabled + intra Ecu receiver)
- ▶ Added support for TextValueSpecifications to specify enumerations of the corresponding CompuMethod (category TEXTTABLE or BITFIELD\_TEXTTABLE)
- ▶ Added support for Bitfield Texttable PortInterfaceMapping for NvDataInterfaces.
- ▶ Added support for NvBlockSwComponentType NvBlockDescriptor romBlock and callback function Rte\_-NvMNotifyInitBlock.
- ▶ Added support for network representation conversion of primitive sender-receiver data elements.

## Module version 6.1.151

2015-01-08

- ▶ ASCRTE-3576 Fixed known issue: The Rte contract phase does not compile if implicit interrunnable variables are configured
- ▶ ASCRTE-3580 Fixed known issue: The Rte may try to lock an Os resource from within a partition that is not permitted to access the resource
- ▶ ASCRTE-3573 Fixed known issue: Rte\_ModeDisablingDep\_Type may use an inadequate type when using inter-partition mode management
- ▶ ASCRTE-3581 Fixed known issue: The Rte reports an internal error if update is enabled for n:1 S/R mixed inter- and intra-partition communication
- ▶ Improved generation of interrupt locks for shared buffers which are not required for tasks which have the highest priority
- ▶ ASCRTE-3457 Fixed known issue: The order of invalid values in Application Types header file is arbitrary
- ▶ Added support for server runnables which are triggered by a direct call that can now have implicit read/write access
- ▶ ASCRTE-3592 Fixed known issue: If a task chain is configured and the first task is of task mapping scenario A2, the Rte code does not compile
- ▶ Added algorithm to generate Buffer names based on element names to ensure predictable symbols
- ▶ ASCRTE-3375 Fixed known issue: If SchM\_Trigger or SchM\_ActMainFunction is called the Rte ignores implicit exclusive areas
- ▶ ASCRTE-3625 Fixed known issue: The software component might receive old data if S/R data transformation is used
- ▶ Implemented [SWS\_Rte\_08408] so that a runnable with both implicit read and write access to a variable data prototype in the context of a PR-port uses a single shared read/write buffer
- ▶ ASCRTE-3638 Fixed known issue: The Rte does not generate code for an implicit exclusive area if EB\_FAST\_LOCK is configured
- ▶ ASCRTE-3621 Fixed known issue: Rte\_SwitchAck returns RTE\_E\_TRANSMIT\_ACK although the transition status isn't done
- ▶ ASCRTE-3657 Fixed known issue: The Rte does not consider precedence of data properties for the generation of the McSupportData
- ▶ ASCRTE-3653 Fixed known issue: The Rte allocates memory for constant memory if it is referenced by a parameter access
- ▶ ASCRTE-3661 Fixed known issue: Inter-partition S/R communication lead to compiler warnings if used with two different but compatible structures
- ▶ ASCRTE-3664 Fixed known issue: A RunnableEntity might be triggered by a BswModuleEntity even if the Rte is not started
- ▶ Added support for External Trigger Event Communication
- ▶ Added basic support for NvBlockSwComponentType.

## Module version 6.1.150

2014-10-13

- ▶ ASCRTE-3474 Fixed known issue: The Rte contract phase generator fails with an error if implicit sender receiver communication is configured
- ▶ ASCRTE-3466 Fixed known issue: If the service needs assistant is executed the Rte treats disabled configuration parameters as enabled
- ▶ ASCRTE-3475 Fixed known issue: The Rte does not generate the BSW Module Description entities for measurement correctly in case of application array and application primitive data types
- ▶ Added invalidation support for inter-partition communication
- ▶ Added configuration parameter `DisableInvalidationDataConsistency` to disable data consistency for invalidation if invalidation policy is set to keep
- ▶ ASCRTE-3505 Fixed known issue: Complex inter-partition inter-ECU data may be corrupted if the received Com signal group is mapped to different data elements
- ▶ ASCRTE-3501 Fixed known issue: The Rte contract phase does not compile if implicit sender/receiver communication is configured
- ▶ ASCRTE-3508 Fixed known issue: The Rte generator fails in attempt to optimize the buffer handling for an implicit receiver connected to an explicit sender
- ▶ ASCRTE-3506 Fixed known issue: The Rte may exchange corrupted data in case of inter-partition inter-ECU sender/receiver communication using complex data types
- ▶ ASCRTE-3533 Fixed known issue: The generation of the Rte Bswmd file may take very long
- ▶ ASCRTE-3538 Fixed known issue: The Rte contract phase generator fails with an error if per-instance memory is configured
- ▶ ASCRTE-3539 Fixed known issue: The Bsw main function auto-mapping functionality maps ComM main functions to an inadequate task if multiple Flexray channels are configured
- ▶ ASCRTE-3543 Fixed known issue: The VFB Tracing tab of the Rte Editor does not list trace hook functions for the `Rte_SwitchAck` API
- ▶ ASCRTE-3458 Fixed known issue: If two or more `SwBaseTypes` with the same name exist, the Rte only considers one
- ▶ ASCRTE-3512 Fixed known issue: The Rte Generator throws an exception if data conversion with floating point coefficients is configured
- ▶ ASCRTE-3524 Fixed known issue: The Rte Editor removes assembly connectors between service components and application components
- ▶ Added support for combined provide and require ports as described by AUTOSAR 4.1.3
- ▶ Added support for data conversion of constants defined using an `ApplicationValueSpecification` in the context of an `ApplicationPrimitiveDataTypes` of category `VALUE`

- ▶ ASCRTE-3551 Fixed known issue: Missing parenthesis around `Rte_IWriteRef` definition
- ▶ Added partial support for serialized inter-ECU sender-receiver communication.
- ▶ ASCRTE-3561 Fixed known issue: The conversion of the schedule table activation offset into ticks may be imprecise
- ▶ ASCRTE-3547 Fixed known issue: Under some circumstances the `Rte_Write` API does not write the measurement buffer
- ▶ ASCRTE-3565 Fixed known issue: The Rte Bswmd generator removes certain information from data types and data prototypes which are enabled for measurement

## Module version 6.1.102

2014-08-08

- ▶ ASCRTE-3416 Fixed known issue: If the callback `Rte_COMCbRxTOut_<sn>` is called by Com, the Rte may not set a configured data receive error event
- ▶ ASCRTE-3386 Fixed known issue: If multiple events have been set for a BSW task, the Rte considers only one
- ▶ Added support for measurement of sender/receiver communication (intra-partition and inter-ECU), AUTOSAR-typed per-instance memory, inter-runnable variables, and parameter data prototypes
- ▶ ASCRTE-3355 Fixed known issue: The Rte Generator rejects configurations containing a `CompuMethod` of category `TEXTTABLE` that references a `Unit`
- ▶ Updated the Rte Generator to support chaining of tasks
- ▶ Updated the Rte Generator to optimize the allocation of receive buffers if they are not required for implicit communication
- ▶ Added function to disable the partition active checks
- ▶ Updated the Rte configuration to generate only one Rte schedule table for all partitions
- ▶ Updated the configuration of the Bsw task used for sending Com signals/signal groups to be time-triggered by setting the parameter `BswOsTaskPeriod`
- ▶ Added configurability to hook functions before/after Bsw schedulable entities are triggered. Additionally the Rte generates hook function before task termination
- ▶ ASCRTE-3423 Fixed known issue: Under certain conditions the software component header file does not include a required Rte header for development error checks
- ▶ ASCRTE-3415 Fixed known issue: Two client-server interfaces are not compatible if the server only defines a possible error with code 0 and the client defines other possible errors
- ▶ ASCRTE-3421 Fixed known issue: The Rte illegal invocation development error detection may use an inadequate unsigned integer type

- ▶ Removed generation of interrupt locks for Shared Memory IP channels if they are accessed by Com callbacks only and Com callbacks are configured as not interruptible
- ▶ Removed the generation of function `Rte_Det_CheckIllegalInvocation` from those partitions where the illegal invocation check is not applicable
- ▶ ASCRTE-3452 Fixed known issue: If inter-partition C/S communication is used with application errors, `Rte_Call/Rte_Result` return an undefined error code
- ▶ ASCRTE-3453 Fixed known issue: Under certain conditions the component data structure is not initialized

## Module version 6.1.101

2014-07-14

- ▶ Added support for data conversion for primitive type data in the context of intra partition sender receiver communication and the definition of constant values in the context of application data types that are transparent to the application
- ▶ Updated verification of more elements such as AutosarDataTypes, CompuMethods and SwPointerTarget-Props if the RteGeneratorOutput is set to BSW\_SCHEDULER\_ONLY
- ▶ Added function to configure whether only the first notification from Com shall be taken into account for transmission acknowledgment after a `Rte_Send` or `Rte_Write` request
- ▶ ASCRTE-3371 Fixed known issue: The return value of a blocking `Rte_Feedback` API is wrong when either a shutdown notification or a timeout occurred
- ▶ Updated the Rte Editor to correctly set the parameter `RteNvmRomBlockLocationSymbol` if the rom block location is empty
- ▶ ASCRTE-3374 Fixed known issue: If a component supports multiple instantiation the Rte implements only the `Rte_Enter/Rte_Exit` APIs of the first instance correctly
- ▶ ASCRTE-3379 Fixed known issue: If an array data element is used in queued sender receiver communication the Rte uses an incorrect type for the queue implementation
- ▶ ASCRTE-3156 Fixed known issue: The Rte generates non-compileable code if the short name of the BSW module description contains an underscore
- ▶ ASCRTE-3376 Fixed known issue: If the short name of certain elements are not unique the internal IDs for development error tracing may be duplicated
- ▶ ASCRTE-3387 Fixed known issue: If `Rte_Stop` is called the Rte may terminate a task that executes Bsw schedulable entities
- ▶ ASCRTE-3386 Fixed known issue: If multiple events have been set for a BSW task, the Rte considers only one
- ▶ ASCRTE-3381 Fixed known issue: The Rte may generate shared buffer groups incorrectly if a shared receive buffer must provide a status field

## Module version 6.1.100

2014-04-24

- ▶ ASCRTE-3244 Fixed known issue: The Rte may generate mode disabling dependencies for mode switch events even if they are not specified in the software component description
- ▶ ASCRTE-3282 Fixed known issue: The `Rte_Read` API may return the wrong initial value if multiple receive ports are connected to the same provide port and different initial values are defined at the receive ports
- ▶ ASCRTE-3285 Fixed known issue: The Rte Verifier does not report an error if two variable data prototypes with equal names but incompatible data types are connected
- ▶ ASCRTE-3288 Fixed known issue: The Rte Verifier does not check the compatibility of connected variable data prototypes if the categories of the data types are different
- ▶ ASCRTE-3284 Fixed known issue: The Rte may generate incorrect code if an implementation data type of category `TYPE_REFERENCE` is used to define an invalid value in context of a sender/receiver connection
- ▶ ASCRTE-3281 Fixed known issue: If an union type is used, the Rte Generator produces code that cannot be compiled
- ▶ ASCRTE-3307 Fixed known issue: No data consistency mechanism is applied to a variable data prototype with a complex data type that is sent inter-partition inter-ECU

## Module version 6.1.57

2014-03-24

- ▶ ASCRTE-3217 Fixed known issue: The `Rte_Read/Rte_IRead` API returns the invalid value if the invalidation policy is set to `KEEP` and the data element is invalidated
- ▶ ASCRTE-3296 Fixed known issue: The Rte does not send structure data to an intra-partition connected receiver if the names of the struct members are different on sender and receiver side
- ▶ Added support for reporting of development errors to the DET
- ▶ ASCRTE-3250 Fixed known issue: The Rte may not execute time triggered runnable entities if several timing events with both equal and different periods and mode disabling dependencies are mapped to the same task
- ▶ Removed the query of the current task ID in `Rte_Call` and `Rte_Result` APIs at runtime if the client task is well-known at code generation

## Module version 6.1.56

2014-02-14

- ▶ ASCRTE-3211 Fixed known issue: The Rte Generator does not consider mappings from calibration parameters to per instance memories if an `arTypedPerInstanceMemory` is used



- ▶ ASCRTE-3200 Fixed known issue: If the timeout of a waitpoint for a data received event is zero, the Rte\_Receive API performs timeout monitoring with zero ticks.
- ▶ Changed the parameter names for the runnable function signature, if runnable entity or service arguments are specified.

## Module version 6.1.55

2014-01-21

- ▶ Updated the event type (e.g. `BswTimingEvent`) to be shown in a separate table column on the Rte Editor's **Event Mapping** tab
- ▶ Implemented error report in Rte Verifier if multiple client server operations trigger the same runnable entity but define incompatible possible errors
- ▶ ASCRTE-2513 Fixed known issue: If two data received events referencing the same variable data prototype are mapped to different tasks, the Rte only considers one
- ▶ ASCRTE-3161 Fixed known issue: The Verifier wrongly reports an error if a server port of a composition is not implemented by a runnable entity
- ▶ ASCRTE-3177 Fixed known issue: If synchronous intra-partition client/server-communication is used and a server runnable of category 2 is mapped to a task, the client may receive a wrong result
- ▶ Added missing trace hook functions (e.g. for `Rte_IsUpdated`) and changed trace hook signature of `Rte_IRead`
- ▶ Updated compiler abstraction macros
- ▶ ASCRTE-3168 Fixed known issue: If the Rte editor is opened and the elements mode declaration, mode declaration group or mode declaration group prototype are not complete, an exception is reported

## Module version 6.1.54

2013-11-15

- ▶ ASCRTE-3099 Fixed known issue: If a runnable entity implicitly sends a variable data prototype with invalidation policy set to keep or replace in case of inter-partition inter-ECU communication, then the Rte Generator fails
- ▶ ASCRTE-2843 Fixed known issue: If a runnable entity with a blocking Rte API function is not mapped to a task, the Rte Generator crashes with an exception
- ▶ ASCRTE-2801 Fixed known issue: If an implementation data type of category structure contains an implementation data type element referencing its parent implementation data type the Rte Generator causes a stack overflow error

- ▶ ASCRTE-3106 Fixed known issue: An executable entity is never executed at runtime if it is triggered by a timing event that has an activation offset equal to its period which in turn is a multiple of the cycle length of the task to which it is mapped to
- ▶ Improved functionality that starting a schedule table with relative activation mechanism no longer fails if the configured schedule table offset is shorter than one Os counter tick. The minimal offset value 1 is used in this case
- ▶ ASCRTE-2947 Fixed known issue: The Rte does not support sender receiver to signal group mappings for array/record application data types
- ▶ Fixed known issue: Violations against the AUTOSAR meta model might be reported twice or elements are verified that are not used by the Rte generator.
- ▶ Updated the table columns **Period** and **Offset** on the Rte Editor's **Event mapping** tab to use numerical instead of alphabetical sort algorithm

## Module version 6.1.53

2013-10-22

- ▶ Updated the Rte Verifier to allow client/server application errors with `errorCode 0`. If the short name of the application error is not `E_OK`, though a warning is reported
- ▶ Updated the Rte Verifier to allow operation-invoked events triggering the same runnable which have array typed arguments of different length (see TPS\_SWCT\_1125)
- ▶ Changed the Rte types header file to use the compiler abstraction macros
- ▶ ASCRTE-3049 Fixed known issue: The Rte Generator shares internal buffers for non-queued implicit inter-partition sender/receiver communication across partition boundaries which leads to non-compilable code
- ▶ ASCRTE-3053 Fixed known issue: Mode-disabling dependencies are not working if more than eight mode-disabling dependencies for different events are specified for an inter-partition mode machine instance

## Module version 6.1.52

2013-09-18

- ▶ ASCRTE-2963 Fixed known issue: If the enhanced `Rte_Mode` API is configured for a software component which supports multiple instantiation or uses the indirect API, the generated Rte code does not compile
- ▶ ASCRTE-2931 Fixed known issue: The Rte editor removes connections between two service components
- ▶ ASCRTE-2920 Fixed known issue: The Rte Editor does not group elements of `requiredModeGroup` correctly in the BSW Mode Mapping window

- ▶ ASCRTE-2944 Fixed known issue: If `TS_IntDisableEnable` is configured as the interrupt blocking function in a shared memory inter-partition environment, the generated code does not compile
- ▶ ASCRTE-2934 Fixed known issue: If only the Bsw scheduler is generated and the Os parameter `OsSecondsPerTick` is not configured, then the Rte fails with an `ArithmeticException`
- ▶ ASCRTE-2976 Fixed known issue: The Rte Generator fails if an inter runnable variable is typed by an application data type which is not mapped to a corresponding implementation data type
- ▶ ASCRTE-3004 Fixed known issue: The Rte generates the API infix in upper case letters in memory mapping and compiler abstraction macros
- ▶ Updated the Rte Generator to support the category `EXPLICIT_ORDER` for mode declaration groups

## Module version 6.1.51

2013-08-20

- ▶ ASCRTE-2921 Fixed known issue: If only on-transition runnables are configured for one mode machine instance and no mode disabling dependencies are configured, the generated Rte code does not compile
- ▶ ASCRTE-2911 Fixed known issue: If data mappings are distributed among different system mappings, the Rte Editor does not save the data mapping correctly
- ▶ ASCRTE-2988 Fixed known issue: If timing events have a huge period, the Rte configures an empty schedule table in the Os ECU configuration
- ▶ ASCRTE-2914 Fixed known issue: If a system signal is mapped to a data element, but no Com signal references this system signal, the Rte Generator fails
- ▶ ASCRTE-2932 Fixed known issue: The **Data Mapping** tab of the Rte Editor does not show application record elements of category `BOOLEAN`
- ▶ ASCRTE-2940 Fixed known issue: The generated `Rte_IrvRead` and `Rte_IrvWrite` APIs ignore the given parameter if the inter-runnable variable is of complex type and the APIs are implemented as a C-macro
- ▶ Updated the Rte Generator to support `Rte_IsUpdated` for inter-partition and inter-partition/inter-ECU communication
- ▶ The Rte Verifier now reports an error if not all mode users of a provided mode declaration group are on the same partition
- ▶ ASCRTE-2938 Fixed known issue: The Rte Generator does not generate empty module interlink header files

## Module version 6.1.50

2013-06-25

- ▶ ASCRTE-2824 Fixed known issue: In task mapping scenario B2, the generated Rte code might not contain all events required by `Rte_WaitGetClearEvent()`
- ▶ ASCRTE-2697 Fixed known issue: Initial values for data elements of array types are not written to the Com configuration via the service needs assistant
- ▶ ASCRTE-2860 Fixed known issue: The generated Rte may not compile if the value of a text value specification contains a number of characters that is greater or equal to the array size of the underlying data type
- ▶ ASCRTE-2814 Fixed known issue: The Rte Editor reports an error if data prototype names collide with Rte Editor internal short names
- ▶ Updated the generated template code in the folder `src_appl` to use a pointer to the array base type in case of array data types

## Module version 6.1.13

2013-05-17

- ▶ Updated the Rte editor to ignore a local variable access which does not refer a valid local variable when opening the **VFB Tracing** tab
- ▶ ASCRTE-2815 Fixed known issue: If at least two mode switch points reference the same mode declaration group instance and one requires a blocking `Rte_SwitchAck` API while the other one is not referenced by a mode switch acknowledge event, the Rte generates no code

## Module version 6.1.12

2013-04-17

- ▶ Updated the Rte Editor tab **Service Port Mapping** to allow to connect one application port to multiple service ports and vice versa
- ▶ ASCRTE-2555 Fixed known issue: `Rte_Pim()` incorrectly returns a pointer to array type instead of pointer to the array base type for a per instance memory typed by an AUTOSAR data type
- ▶ ASCRTE-2529 Fixed known issue: The code which is generated in the contract phase depends on EB-specific header files
- ▶ ASCRTE-2640 Fixed known issue: The Rte does not generate any data filters if the underlying C-type is a char type
- ▶ Updated the Rte Verifier to report a warning if the resolution of the parameter `OsSecondsPerTick` of the used Os counter is higher than nano-seconds
- ▶ Updated the Rte Verifier to report an error if a configured data filter lacks required attributes, e.g. the `MIN` and `MAX` attributes

- ▶ ASCRTE-2672 Fixed known issue: The Rte generates incorrect code in case of intra-partition mode switch procedure
- ▶ ASCRTE-2450 Fixed known issue: If inter-partition mode management with IOC is used and no mode switch event is specified, the mode disabling dependencies are not set by the `Rte_Switch` API
- ▶ Updated the Rte mode defines to generate even if the corresponding mode declaration group is not used, but an included mode declaration group set is referencing the mode declaration group
- ▶ ASCRTE-2672 Fixed known issue: The Rte generates incorrect code in case of intra-partition mode switch procedure
- ▶ ASCRTE-2675 Fixed known issue: If a mode-disabling dependency references a mode declaration that is not part of the referenced mode declaration group prototype, the Rte Generator aborts with an exception
- ▶ ASCRTE-2685 Fixed known issue: If multiple clients are connected to the same server and an unconnected port triggers the same server runnable or the client operations consist of different argument, the Rte might report a null pointer exception or the generated code does not compile
- ▶ ASCRTE-2699 Fixed known issue: If the Rte Generator is executed multiple times without restarting EB tresos Studio, the Rte generates incorrect mode-disabling dependency macros
- ▶ ASCRTE-2745 Fixed known issue: The enhanced `Rte_Mode` API function does not set the given previous and next mode parameters if function elision is configured
- ▶ Updated the Rte to support the execution of runnable entities and BSW schedulable entities on the transition of two modes
- ▶ Added the Rte Editor and the plugin `SwcUtils` to the Rte. The release notes for both plugins are now within the release notes of the Rte
- ▶ Updated the combo boxes **Bsw Os application** and **BSW Os task** to always show the values of the Rte configuration after opening the Rte Editor page **Partitioning**.
- ▶ ASCRTE-2727 Fixed known issue: The Bsw mode machine instance is not correctly initialized and `Rte_Start()` overwrites the state of the mode machine instance
- ▶ ASCRTE-2733 Fixed known issue: The Rte does not generate enumeration macros for application data types that are referenced by an included data type set

## Module version 6.1.11

2013-02-08

- ▶ Updated the system description verifier to check for a proper Bsw mode management configuration as described by `constr_4022`, `constr_4059`, and `constr_4063`
- ▶ Added support of the BSW Scheduler name prefix
- ▶ ASCRTE-2523 Fixed known issue: If an initial value is specified for an implementation data type that references a structure, the Rte Generator wrongly reports an error

- ▶ Added support of background events
- ▶ Added support of Rte mode switch acknowledgment
- ▶ ASCRTEAS-431 Fixed known issue: The Rte Editor does not show elements of `SwcServiceDependency` which are mapped to per-instance memory
- ▶ ASCSWCUTILS-191 Fixed known issue: If an implementation data type of category `value` does not specify a base type, the Rte Generator reports a null pointer exception
- ▶ Added support of the enhanced mode API for inter-partition mode management
- ▶ ASCRTE-2478 Fixed known issue: If function elision is active and the port for a specific API function is unconnected, the Rte generates the return value `RTE_E_OK`

## Module version 6.1.10

2012-12-14

- ▶ Updated the Rte to use the pointer to array type passing scheme by default if the configuration parameter `ASR32RteWrapperis` set to true,
- ▶ ASCRTE-2458 Fixed known issue: If the enhanced Mode API is used and the corresponding port is unconnected, the Rte Generator does not generate any code
- ▶ ASCRTE-2306 Fixed known issue: The Rte Generator may produce duplicate code blocks for reading a signal group from COM within a reception callback
- ▶ ASCRTE-2452 Fixed known issue: The Rte Generator does not generate any code if a per instance memory is typed by an application data type
- ▶ Updated the Rte Generator to support elements of `InterRunnableVariable` with complex data types
- ▶ Added generation of enumeration constants for referenced application primitive data types to the application types header file
- ▶ ASCRTE-2479 Fixed known issue: The Rte cannot allocate more than one Os schedule table expiry point per millisecond

## Module version 6.1.9

2012-11-19

- ▶ ASCRTEAS-435 Fixed known issue: Rte Editor removes configured BSW Module instances from Rte configuration under certain circumstances
- ▶ Updated the editor fields **BSW Os task**, **BSW send signal queue length**, and **BSW send signal group queue length** on the **Partitioning** tab to be properly enabled depending on the check box value **BSW Os task required**

- ▶ ASCSWCUTILS-175 Fixed known issue: The compatibility check of two elements of `ImplementationDataType` or `ImplementationDataTypeElement` of category `ARRAY` is incorrect
- ▶ ASCRTE-2172 Fixed known issue: The Rte Generator generates non-compilable code when a local parameter is mapped to per-instance memory, but no parameter access is defined
- ▶ ASCRTEAS-439 Fixed known issue: If the Rte configuration is saved and the **Measurement and Calibration** tab was not touched, the calibration support configuration for parameter software component types is removed
- ▶ ASCRTE-2441 Fixed known issue: If partitioning is enabled, the Rte does not generate invocations of executable entities mapped to the BSW Os task
- ▶ ASCRTE-2442 Fixed known issue: If basic software (BSW) events are mapped to an OS task that is not part of the BSW partition, the OS task implementation is added to the BSW partition
- ▶ Updated the Rte Generator to support data element invalidation if the initial value equals the invalid value
- ▶ Updated enumeration constants for elements of `CompuMethod` to be generated as defined by AUTOSAR 4.0.3

## Module version 6.1.8

2012-10-15

- ▶ ASCSWCUTILS-167 Fixed known issue: If it is checked whether a variable data prototype is queued or not, the data type map should not be considered if the variable data prototype references an implementation data type
- ▶ ASCRTE-2408 Fixed known issue: The behavior of the Rte Generator is undefined when invalid `SwBaseType` native declarations are present in the data model
- ▶ ASCRTE-2386 Fixed known issue: The system model is not validated if the Rte Contract Phase Generator is executed via the command line
- ▶ Updated the system description verifier to check if the native declaration of a `SwBaseType` contains a valid C-data type
- ▶ Updated the Rte Generator to support the Basic Software Scheduler Mode Management as defined by AUTOSAR 4.0
- ▶ Updated the Rte Editor to support connecting of BSW required triggers with BSW released triggers
- ▶ ASCRTE-2412 Fixed known issue: Partial record support does not function in combination with system signal mappings
- ▶ Updated macro definitions for range data types, enumeration constants and invalid values according to AUTOSAR 4.0.3
- ▶ Fixed several issues with the component template generator

- ▶ Updated the Rte Editor to support mapping of provided BSW mode declaration group prototypes to required BSW mode declaration group prototypes
- ▶ Updated the Rte Generator to verify the Bsw Scheduler ECU configuration
- ▶ ASCRTEAS-425 Fixed known issue: The **Service Port Mapping** tab removes all port interface mappings if a service connector is modified
- ▶ Updated the Rte Generator to support the enhanced mode API for intra-partition mode management

## Module version 6.1.7

2012-09-18

- ▶ ASCRTE-2387 Fixed known issue: If an inter-runnable variable is typed by an application data type which does not have a mapped implementation data type, the Rte Generator will prematurely terminate without errors
- ▶ ASCRTE-2363 Fixed known issue: The system signal mapping is not working when a port interface mapping is specified
- ▶ ASCRTE-2313 Fixed known issue: The Rte Generator may produce code which causes compiler warnings like *Rte\_IsModeDisablingDepSet\_X defined but not used*
- ▶ Updated the Bsw Scheduler to support the `SchM_ActMainFunction` and `SchM_Trigger` API functions

## Module version 6.1.6

2012-08-17

- ▶ Updated the Bsw Scheduler to support Bsw called entities and Bsw interrupt entities
- ▶ Updated the names of the preprocessor defines for the Com Handle IDs according to AUTOSAR 4.0 Rev 3 naming scheme
- ▶ Updated the system description verifier to check Bsw schedulable entities, Bsw called entities and Bsw interrupt entities
- ▶ ASCRTE-2365 Fixed known issue: The Rte Generator may incorrectly initialize Rte data structures when the initial value is greater than `0x7FFFFFFF`
- ▶ ASCRTE-2368 Fixed known issue: The Rte Generator throws a null pointer exception when a software base type is incorrectly defined
- ▶ Improved the evaluation of task mapping for asynchronous client/server calling chains
- ▶ Updated the system description verifier to report an error if two different interfaces with the same short name have incompatible application errors or if they are referenced by a software component which supports multiple instantiation or the indirect API attribute is set to true for at least one port



## Module version 6.1.5

2012-07-16

- ▶ Removed configuration switch for the AUTOSAR 3.1 SchM Exclusive Area API support
- ▶ ASCRTE-2154 Fixed known issue: Inter-partition mode disabling dependencies do not work with non-timing events
- ▶ Updated the Bsw Scheduler to generate an AUTOSAR 3.1 SchM Exclusive Area API wrapper for individual Bsw module instances
- ▶ Updated the Rte Editor to disable exclusive areas with the implementation mechanism option *Disabled Exclusive Area*
- ▶ Removed the AUTOSAR 2.1 to AUTOSAR 3.1 Rte module transformer from the Rte add-on
- ▶ ASCRTE-2341 Fixed known issue: Wrong registration of transformer causes error log entry in Studio when using module upgrade functionality

## Module version 6.1.4

2012-06-21

- ▶ ASCRTEAS-411 Fixed known issue: The Rte Editor does not report any errors if the BSWMD is invalid
- ▶ ASCRTE-2332 Fixed known issue: Warnings from the Rte Generator are not reported to the user
- ▶ Updated the Rte Generator to support data mappings to system signals
- ▶ Updated the Rte to support the usage of partial records (signal degradation) for sender-receiver communication as specified by AUTOSAR 3.1
- ▶ Updated the Rte editor to support the mapping of system signals and system signal groups to data elements
- ▶ ASCRTE-2322 Fixed known issue: If a runnable entity makes an inter-partition server call and the runnable is not mapped to a task, the Rte Generator reports an error even if a SWC to BSW mapping exists for that runnable
- ▶ Updated the Rte Generator to provide the generation of the AUTOSAR 3.2 Rte Wrapper to support AUTOSAR 3.2 software components
- ▶ Updated the Rte Editor to offer a configuration switch to enable or disable the AUTOSAR 3.2 Rte Wrapper feature
- ▶ Removed legacy support of the ASR 3.1 SchM Exclusive Area API

## Module version 6.1.3

2012-05-16

- ▶ ASCRTE-2288 Fixed known issue: The generated Rte will not compile if a basic software module description name and a software component type name are equal
- ▶ Updated the Rte Generator to support the generation of empty `Rte_Start/Rte_Stop` functions if the configuration switch **Generate empty Rte\_Start/Rte\_Stop stubs** is enabled
- ▶ Updated the Rte Editor to offer a configuration switch to enable or disable the generation of an empty `Rte_Start/Rte_Stop` function
- ▶ ASCRTE-2297 Fixed known issue: The Rte Generator may produce duplicate code blocks for implicit reads and writes within the same task
- ▶ Updated the system description verifier to handle partial records and subelement mappings
- ▶ ASCRTE-2307 Fixed known issue: The Rte Generator may not consider the task position of timing events mapped to the same task

## Module version 6.1.2

2012-04-23

- ▶ ASCRTEAS-398 Fixed known issue: The configuration of the partitioning support may not be possible, even if the partitioning support is enabled
- ▶ ASCRTEAS-399 Fixed known issue: The drop-down list in the tables of the **Implementation Selection** and **Partitioning** tabs show the wrong list of items
- ▶ ASCSWCUTILS-116 Fixed known issue: The system description verifier does not generate an error when the offset and the period of a `ONEEVERYN` data filter are equal
- ▶ ASCRTE-2192 Fixed known issue: The Rte Generator will generate a non-blocking `Rte_Result` API function, if an asynchronous server call result point is defined, but no corresponding asynchronous server call returns event exists
- ▶ ASCRTE-2154 Fixed known issue: Inter-partition mode disabling dependencies do not function with non-timing events
- ▶ ASCRTE-2215 Fixed known issue: The Rte Generator will produce a wrong type definition for an implementation data type of category `STRUCTURE` that contains an implementation data type element of category `ARRAY`
- ▶ Changed implementation data type elements of category `ARRAY` with the basetype `uint8` to be grouped to byte arrays
- ▶ ASCRTE-2287 Fixed known issue: The memory mapping in the Module Interlink header files may cause linker errors

## Module version 6.1.1

2012-03-23

- ▶ ASCRTE-2178 Fixed known issue: If a data write access is specified for a variable data prototype which is not of category `ARRAY`, the resulting `Rte_IWriteRef` API will not return a pointer to the variable
- ▶ ASCRTE-2009 Fixed known issue: The Rte Generator may produce code which causes compiler warnings like *a value of type `x` cannot be used to initialize an entity of type `y`*
- ▶ ASCRTE-2189 Fixed known issue: The Rte Generator may generate code that will not compile due to unresolved symbols
- ▶ ASCRTE-2102 Fixed known issue: If a timeout is specified for an asynchronous server call point and no wait point is defined for the corresponding asynchronous server call result point, then the Rte Generator will produce non-compilable code
- ▶ Updated the Rte Generator to support the generation of the legacy AUTOSAR 3.1 SchM Exclusive Area API as function-like macros
- ▶ ASCRTE-2193 Fixed known issue: The generated Rte code will produce a compile error, if a Com signal group is mapped to a variable data element at a require port and the data can be directly read from Com
- ▶ Updated the Rte Editor to offer a configuration switch for enabling or disabling the AUTOSAR 3.1 SchM Exclusive Area API support
- ▶ ASCRTE-2196 Fixed known issue: The Rte Generator may produce a `NullPointerException`, if two tasks from different partitions hold runnable entities that specify a timeout for their `Rte_Call`, `Rte_Feedback`, `Rte_Receive` or `Rte_Result` API
- ▶ ASCRTE-2200 Fixed known issue: The `Rte_Start()` API will not correctly initialize the Rte on a multi-core system
- ▶ ASCRTE-2177 Fixed known issue: The Rte Contract Phase will fail without any error if an application data type is not mapped to an implementation data type
- ▶ Reception signal timeout callbacks no longer read Com signal values when the attribute *replace by initial value* is not configured for a Com signal
- ▶ Updated the Rte Editor to provide the feature to auto-map all Bsw timing events to a task
- ▶ Updated the Rte Generator to support the `SwcBswMapping` to determine the calling context for Bsw schedulable entities
- ▶ ASCSWCUTILS-130 Fixed known issue: The merge of the ECU resource properties fails with an error during the Rte generation or when running the Service Needs Assistant
- ▶ ASCRTE-2206 Fixed known issue: The generated Rte code will not compile if two or more runnable entities are mapped to different tasks and each of these has a synchronous server call point for the same operation on the same port
- ▶ ASCRTE-2197 Fixed known issue: Within the Rte default schedule table, the Rte Generator will not allocate any expiry point, which activates an Os task if there already exists an expiry point activating any other Os task with the same period and offset
- ▶ Updated the Rte Generator to support the generation of the BSW scheduler and its API functions as defined by AUTOSAR 4.0

- ▶ Implemented generic BSW scheduler verifier
- ▶ Updated the Rte Editor to support configuring the BSW scheduler specific parts in the Rte

## Module version 6.1.0

2012-02-17

- ▶ ASCRTE-2113 Fixed known issue: If a client port is connected to a server port, which belongs to the same software component instance, the generated Rte code will produce compiler warnings
- ▶ Updated the system description verifier to consider the port interface mapping if the compatibility between two interfaces is checked
- ▶ Updated the Rte Generator to support the port interface mapping as defined by AUTOSAR 4.0
- ▶ ASCRTE-2173 Fixed known issue: `Rte_NvMData.h` can cause compile errors due to the duplicate definition for the same ROM default value variable
- ▶ Added support for the `ImplementationDataType` attribute `typeEmitter`
- ▶ Removed support of the Rte Editor for the generation of the service component description via the **Service Port Mapping** tab.

## Module version 6.0.12

2012-01-20

- ▶ ASCSWCUTILS-107 Fixed known issue: If a delegation connector connects ports of different types, the system description verifier will stop the verification of the system model and report a class cast exception
- ▶ ASCRTE-2013 Fixed known issue: If inter-partition sender/receiver communication is used with data filters configured with the data filter algorithm `NEVER`, the Rte Generator produces code which causes compiler warnings
- ▶ ASCRTE-2115 Fixed known issue: The Rte Generator will report an error for application data types that refer to a valid application value specification
- ▶ ASCRTE-2121 Fixed known issue: The Rte will not update the mode of a mode machine instance after the execution of a related mode switch event
- ▶ ASCRTE-2150 Fixed known issue: The Rte Generator will produce compiler errors like *parse error before '}' token* if a runnable entity defines a server call or server call result point for an operation, for which no operation invoked event exists on server side
- ▶ Updated the system description verifier to check `CompuMethod` elements of the category `TEXTTABLE` according to the software component template constraint `constr_1134`
- ▶ Implemented partitioning support in the variant *Shared Memory*

## Module version 6.0.11

2011-12-09

- ▶ Added additional consistency checks
- ▶ ASCRTE-1193 Fixed known issue: An `Rte_Write`, `Rte_Feedback` or `Rte_Invalidate` might behave as if the port was not connected
- ▶ ASCRTE-2088 Fixed known issue: When a DBC, FIBEX or LDF file is imported with the option *Enable system model import* enabled, the Rte Generator will fail to generate the Rte
- ▶ Added generic Rte Verifier

## Module version 6.0.10

2011-11-11

- ▶ ASCRTE-2007 Fixed known issue: The Rte Generator produces non-compilable code when inter-ECU invalidation shall be handled by the Rte and the invalid value of a COM signal which shall be invalidated is specified in binary or array format
- ▶ ASCRTE-2020 Fixed known issue: If there are `CompuMethod` elements with category `TEXTTABLE` where at least one `CompuScale` defines no upper or lower limit, the Rte Generator will produce a null pointer exception
- ▶ ASCRTE-2018 Fixed known issue: If inter-partition client/server communication is used, the API function `Rte_Result()` will not return
- ▶ ASCRTE-2031 Fixed known issue: If optimization function elidation is enabled, the Rte will transmit a primitive data element only within its own partition
- ▶ ASCRTE-2008 Fixed known issue: When the optimization *use bit fields for internal buffers* is enabled, the Rte Generator may produce non-compilable code
- ▶ ASCSWCUTILS-91 Fixed known issue: The system description verifier fails to report an error for incorrect instance references, which refer to an entity that is not part of the enclosing software component type
- ▶ Updated the Rte Generator to support the update flag feature defined by AUTOSAR 4.0
- ▶ Updated the Rte Generator to support the `Rte_DRead` API function as defined by AUTOSAR 4.0
- ▶ Changed the value of the error code `RTE_E_SHUTDOWN_NOTIFICATION`
- ▶ Updated the Rte Generator to support the never received status for sender/receiver communication
- ▶ ASCRTE-2063 Fixed known issue: The Rte Generator may produce code that causes compiler warnings if `USE-VOID` is used as `ServerArgumentImplPolicy` for a complex `IN` argument of an operation
- ▶ ASCRTEAS-340 Fixed known issue: The Rte Editor will not save any changes which have been applied to the **General** tab if the partitioning support, event mapping, or service port mapping is configured afterwards

- ▶ Changed the content of the API mapping and declaration in the component header file to not be re-ordered if the configuration is unchanged

## Module version 6.0.9

2011-09-02

- ▶ Initial AUTOSAR 4.0 version

## 2.2. New features

- ▶ The Rte now supports multiple Com instances.

It is now possible to send/receive signal(group)s over multiple Com instances which are mapped to different partitions.

- ▶ The Bsw Scheduler now supports client/server communication.

Bsw modules can now exchange variable data over the Bsw Scheduler by using the SchM\_Call and SchM\_Result APIs.

- ▶ The Rte now supports autonomous error reaction for inter-ECU client server communication with data serialization

For client server communication it is now possible on the server side to trigger an autonomous error reaction which generates the response of the client server communication automatically without involvement of any runnable.

- ▶ The Rte now supports the asynchronous mode switch

It is now possible to configure the asynchronous mode switch communication. If all intra-partition require mode declaration group instances support the asynchronous mode switch behavior and if the mode switch events are either mapped to no task or if all of them are mapped to the task of the `Rte_Switch`, then the invocation of runnable entities will be made via direct function call.

- ▶ The Rte now supports intra-ECU sender-receiver communication with data transformation

For intra-ECU sender-receiver communication it is now possible to specify a DataTransformation between two interfaces by using a DataPrototypeMapping

- ▶ The Rte now supports the dirty flag mechanism for NvBlockSwComponentTypes

It is now possible to enable the `dirtyFlagSupport` for a `NvBlockDescriptor`. Different writing strategies like `storeImmediate`, `storeCyclic` and `storeAtShutdown` can be configured in the `NvBlockNeeds`.

- ▶ The Rte now supports memory mapping initialization strategy

For inter runnable variables, NvRam blocks, per instance memory, receive buffers and Smc buffers it is now possible to specify an initialization strategy mapped to a policy described by a `SwAddrMethod` corresponding to the variable data prototypes.

- ▶ The Rte now supports the external replacement invalidation for sender-receiver communication according to AUTOSAR 4.3.0.

For sender-receiver communication it is now possible on reception side to specify another variable data prototype to be used for replacement.

- ▶ The Rte now supports the feature to provide the activating event of an `ExecutableEntity` according to AUTOSAR 4.3.1.

It is now possible to configure an `ActivationReason` for an `RTE/BSW Event`. When configured, `RTE` shall pass an additional argument `activation` to the `ExecutableEntity`.

- ▶ The Rte now supports debounced activation of executables.
- ▶ The Rte now supports compound primitives

It is now possible to use compound primitives as long as their implementation is an array.

- ▶ The Rte now supports `InitEvents` triggering runnable entities via OS task according to AUTOSAR 4.3.1.

It is now possible to configure `InitEvents` triggering runnable entities via OS task for initialization purposes, i.e. for starting and restarting a partition.

- ▶ The Rte now supports Transport Protocol for Serialization for LdCom.
- ▶ The Rte now supports the implementation of a basic task for tasks shared by Rte and Bsw timing events.

It is now possible to enable a `OneScheduleTablePerPartition` option to implement a basic task for tasks shared by Rte and Bsw timing events.

- ▶ The Rte now supports mapping of external trigger events for category 1 executables to category 2 ISRs.
- ▶ The Rte now supports RTE Implementation Plugins (RIPS) for explicit/implicit sender receiver communication.
- ▶ The Rte now supports `InitEvents` triggering runnable entities via `RteInitializationRunnableBatch` according to AUTOSAR 4.3.1.

It is now possible to configure `InitEvents` triggering runnable entities for initialization purposes via `RteInitializationRunnableBatch`. i.e. for starting and restarting a partition.

- ▶ The Rte now supports the generation of human readable global variable names instead of cryptic ones

It is now possible to set the new configuration parameter `HumanReadableBufferNames` to true to let the Rte generator use a human readable naming approach for the generated global variables.

- ▶ The Rte now supports timeout monitoring for asynchronous client/server communication with non-blocking result calls.

- ▶ The Rte now supports partitioning for communication timeout in case of inter-partition inter-ecu communication
- ▶ The Rte now supports multiple mode user partitions.

It is now possible to configure a mode manager connected to multiple mode user which are mapped to different partitions which in turn are scheduled on different micro-controller cores.

- ▶ The Rte now supports intra-ECU sender-receiver communication with SubElementMappings to primitive DataPrototypes.

It is now possible to map a structure member on a sender side to a primitive data element on the receiver side.

- ▶ The Rte now supports the usage of LdCom without transformers.
- ▶ The Rte now supports mapping of timing events for category 1 executables to category 2 ISRs.

It is now possible to map a timing event or a BSW timing event to an ISR.

- ▶ The Rte now supports transmission acknowledgment for implicit sender receiver communication.

The information whether a value has been successfully passed to the communication infrastructure can be known by using the Rte\_IFeedback API.

- ▶ The Rte now supports synchronous mode switching procedure over multiple partitions/cores.

It is now possible to have the mode switching procedure steps synchronized over multiple partitions/cores.

- ▶ The Rte now Supports mapping of variable data prototypes to parameter data prototypes.
- ▶ The Rte now supports the access to a reference to the VariableDataPrototype referenced by VariableAccess in the dataReadAccess role.

This can be done by using the Rte\_IReadRef API.

- ▶ The Rte now supports retrieving partition context for Com on a per-PDU basis.
- ▶ The Rte now supports retrieving partition context for LdCom on a per-PDU basis.
- ▶ The Rte now supports graphical visualization for exclusive area optimization.
- ▶ The Rte now supports the improvement of start-up and initialization of NvBlockSwComponents. Restore default data if both ROM and an initialization callback are configured.
- ▶ The Rte now supports the access to a reference to the VariableDataPrototype defined with the implicitInterRunnableVariable role.

This can be done by using the Rte\_IrvIWriteRef API.

- ▶ The Rte now supports the transmission preparation callback (Rte\_COMCbTxPrep\_mn) for inter-partition inter-ECU communication.
- ▶ The Rte now supports the generation of review instructions to easily identify memory writes access for freedom from interference analyses (released).



The configuration parameter `GenerateRteFreedomFromInterferenceReviewInstructions` can be used to generate review instructions and an html report to easily detect where a write access occurs.

- ▶ The Rte now supports the I-PDU based time-out according to SWS\_Rte\_08103.

For all signals/signal groups without update bits the timeout occurs according to the greatest multiple of the I-PDU based time-out value lower than or equal to the `aliveTimeout` value in the `NonqueuedReceiver-ComSpec`.

- ▶ The Rte now supports the generation of implicit sender-receiver APIs as C Function when `enableTakeAddress` for the port is set to true.

## 2.3. Elektrobit-specific enhancements

This chapter lists the enhancements provided by the module.

- ▶ On demand, you are able to add the OS objects required by the Rte automatically to the OS configuration using the Service Needs Assistant.
- ▶ On demand, the Service Needs Assistant automatically configures the required Com callbacks in the Com configuration.
- ▶ The Rte provides an additional configuration option *OSEK OS compatibility mode*. This option enables the usage of the Rte in connection with an operating system which is not compliant to AUTOSAR, but only compliant to OSEK OS.
- ▶ When applying data consistency mechanisms (Os resources, interrupt locking etc.), the Rte Generator considers whether the hardware can make an atomic access. In this case, no data consistency mechanism is applied.
- ▶ You are able to configure the data consistency mechanism which the Rte Generator shall apply if a data consistency mechanism is required. You can choose between interrupt locking and the usage of Os resources.
- ▶ You are able to configure the interrupt blocking function which the Rte Generator shall use. The following options are provided: `SuspendResumeAllInterrupts`, `DisableEnableAllInterrupts`, fast interrupt locking (EB-specific) or you can use a user-specific interrupt locking function.
- ▶ You can configure whether the Com callbacks are interruptible or not. If they are not interruptible, the Rte Generator does not need to lock interrupts at all in the Com callbacks.
- ▶ When allocating buffers for data elements, the Rte Generator shares buffers if possible to reduce the RAM consumption.
- ▶ The Rte Generator supports function elidation. If function elidation is enabled, several API functions are realized as macros instead of functions.

- ▶ The Rte Generator provides an option to directly read data from the Com module if possible. If this option is switched on, the Rte Generator does not allocate an additional receive buffer. This reduces the RAM consumption. You may disable this option if your application reads the data element more frequently than the mapped signal is updated by Com.
- ▶ The Rte supports additional trace hooks for the implicit `IRead` and `IWrite` API.
- ▶ You can use the component-specific memory mapping if you want to map the code of your software components to other memory sections than the default memory section `RTE_APPL_CODE`.
- ▶ In addition to the specified return values, the blocking Rte API calls return `RTE_E_SHUTDOWN_NOTIFICATION` if the Rte received a shutdown notification:

- ▶ `Rte_Receive`
- ▶ `Rte_Feedback`
- ▶ `Rte_Call` (synchronous invocation)
- ▶ `Rte_Result`

This happens while the Rte waits for the wait point or synchronous server call point to be resolved.

- ▶ You can configure whether the Rte shall handle inter-ECU signal invalidation or not. If the Rte shall not handle inter-ECU invalidation, it is handled by the Com as defined in the specification.
- ▶ The Rte Generator now supports the partitioning of software components. Software components and their runnable entities can be mapped to different partitions. The Rte realizes the communication between software components mapped to different partitions using the Inter OS Application Communicator (IOC) which is part of the Os or the Shared Memory Communicator (SMC) which is part of the Rte.

The partitioning support enables the usage of the Rte in multi-core and memory-protected systems.

- ▶ It is possible to have up to 256 clients per operation in a client/server communication.

The Rte Generator allows to connect 256 clients with the same server, i.e. operation.

- ▶ The Rte Generator used different Os events for timing events, that were mapped to the same task, had the same period, but different mode disabling dependencies.

Now, the Rte Generator uses only one Os event for the timing events of a task, if they have the same period, but different mode disabling dependencies.

- ▶ The Rte supports the usage of partial records (signal degradation) for sender/receiver communication. This increases the flexibility of a sender-receiver interface by allowing new elements to be added to a record, while still being compatible. See Bugzilla issue [http://www.autosar.org/bugzilla/show\\_bug.cgi?id=44863](http://www.autosar.org/bugzilla/show_bug.cgi?id=44863).
- ▶ It is possible to have multiple wait points that reference the same data send completed event or mode switched ack event. This implies that runnable entities mapped to different tasks can now call a blocking `Rte_SwitchAck` or `Rte_Feedback` at the same time.
- ▶ The Rte generator now supports `Rte_IsUpdated` for inter-partition and inter-partition/inter-ECU communication.

- ▶ The Rte provides an additional configuration parameter `SingleScheduleTablePartitionRef`. If this parameter is set and multiple partitions are used, only one schedule table will be generated for the referenced Os Application. Therefore a call to `Rte_Start/Rte_Stop()/Rte_Restart()` in one of other partitions has no effect on the lifetime of the schedule table.
- ▶ The Rte provides an additional configuration parameter `DisablePartitionActiveChecks`. If set to true, the Rte will not fulfill requirements `rte_sws_2538`, `rte_sws_2535`, and `rte_sws_2536` anymore. This means that the Rte does not check for each API function if the current partition is active. Ensure that no callbacks and no API functions are called before `Rte_Start()/after Rte_Stop()` has been executed. The effect of this optimization depends on the number of generated API functions and how often they are called by the application.
- ▶ The Rte generates further hook functions than specified by AUTOSAR 4.0.3:

At the end of each task, the hook function `Rte_Task_EndHook(OsTask)` is called.

Before each Bsw schedulable entity call, the hook function `Rte_Schedulable_<bsnp>[_<vi>_<ai>]_Start()` is generated.

After each Bsw schedulable entity call, the hook function `Rte_Schedulable_<bsnp>[_<vi>_<ai>]_Return()` is generated.

The header file `SchM_<bsnp>[_<vi>_<ai>]Hook.h` contains all hook functions accessible by that basic software module.

- ▶ The Rte supports combined provide and require ports as described by AUTOSAR 4.1.3:

The Rte generator now supports combined provide and require ports (i.e. `PRPortPrototypes`) by means of AUTOSAR 4.1. Therefore the following additional requirements are fulfilled by the Rte verifier and generator:

Requirement / constraint	Origin	Document Version
SWS_Rte_06030	Specification of RTE	3.5.0
constr_1200	Software Component Template	4.5.0
constr_1202	Software Component Template	4.5.0
constr_1203	Software Component Template	4.5.0
constr_1204	Software Component Template	4.5.0
constr_1205	Software Component Template	4.5.0

Table 2.1. Introduced requirements for `PRPortPrototypes`

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**NOTE**



The Rte considers an `AssemblySwConnector` between two `PRPortPrototypes` as a directed connection where the direction is specified by means of the provider and requester role. If a bi-directional connector is needed, two `AssemblySwConnectors` for each direction must be specified.

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- ▶ The Rte supports the concept of Data Transformation via IF-API according to AUTOSAR version 4.2.-2. A list of supported scenarios is documented in section *Supported features* of the EB tresos AutoCore Generic 8 RTE documentation.
- ▶ The Rte supports fire-and-forget asynchronous client/server calls as specified by RfC 70295 (planned for AUTOSAR version 4.3.0). It is not required anymore to define an `AsynchronousServerCallResultPoint` for each `AsynchronousServerCallPoint`. The Rte generates no `Rte_Result` API if the `AsynchronousServerCallResultPoint` is omitted. This enables the client to initiate a subsequent asynchronous server call without the need to first fetch the result as soon as the server processed the last request.
- ▶ The Rte supports cooperative tasks which is a list of `OsTasks` that cannot interrupt each other regardless of their configured priorities, schedule, application assignment, and core assignment. This list will be taken into account by the Rte generator to optimize locks and to reduce the number of implicit buffers.
- ▶ The Rte supports the `dirtyFlag` mechanism as described by AUTOSAR 4.3.0.

The following additional requirements are fulfilled by the Rte generator: `SWS_Rte_08080`, `SWS_Rte_08081`, `SWS_Rte_08082`, `SWS_Rte_08083`, `SWS_Rte_08084`, `SWS_Rte_08085`, `SWS_Rte_08086`, `SWS_Rte_08087`, `SWS_Rte_08088`, `SWS_Rte_08089`, `SWS_Rte_08090`.

- ▶ The Rte supports the `InitEvents` via OS task as described by AUTOSAR 4.3.1.

The following additional requirements are fulfilled by the Rte generator: `SWS_Rte_06748`, `SWS_Rte_06761`, `SWS_Rte_06762`.

- ▶ The Rte supports the configuration parameters `RteServerQueueLength` and `RteBswServerQueueLength` as specified by RfC #79150. This enables projects to specify the server queue length for BSW module client-server communications and to overrule the `queueLength` attribute of the `ServerComSpec` for Rte client-server communications. The `RteServerQueueLength` resp. `RteBswServerQueueLength` can be configured in the event-to-task-mapping configuration for the particular `OperationInvokedEvent` which starts the server executable.
- ▶ The Rte supports the configuration parameters `RtePeriod` and `RteBswPeriod` as suggested by RfC #80049. This enables the time triggered activation of executable(s) assigned to an `RteEvent`/`BswEvent` and disables any event triggered activation. The `RtePeriod` resp. `RteBswPeriod` can be configured in the event-to-task-mapping configuration for the particular `RteEvent`/`BswEvent` which starts the executable. Rte currently supports the time triggered activation of the following types of event:
  - ▶ `OperationInvokedEvent`
  - ▶ `DataReceivedEvent`
  - ▶ `DataSendCompletedEvent`

- ▶ SwcModeSwitchEvent
  - ▶ BswModeSwitchEvent
  - ▶ InternalTriggerOccurredEvent
  - ▶ BswInternalTriggerOccurredEvent
  - ▶ AsynchronousServerCallReturnsEvent
- ▶ The Rte supports the configuration parameter `RteMaxNrOfProcessedRequests` as specified by RfC AR-107921. This enables projects to specify the maximum number of processed client requests per server executable entity within one task cycle. The `RteMaxNrOfProcessedRequests` can be configured in the event-to-task-mapping configuration for the particular `OperationInvokedEvent` resp. `BswOperationInvokedEvent` which starts the server executable, and it is only applicable if the parameter `RtePeriod` resp. `RteBswPeriod` is also configured.
- ▶ The Rte supports including additional type header files for the `ImplementationDataTypes` which provide an ADMIN-DATA container as shown below:

```
<ADMIN-DATA>
<SDGS>
  <SDG GID="EB:PROVIDING-HEADER-FILE">
    <SD GID="PROVIDING-HEADER-FILE">FileName.h</SD>
  </SDG>
</SDGS>
```

If the `TypeEmitter` of an `ImplementationDataType` is available and set to a value different from `RTE`, the Rte will evaluate the `AdminData` of the `ImplementationDataType` and include the provided header file in the `RTE Types Header File`.

- ▶ The Rte supports value-based data filtering for primitive implementation data types on the sender side. The `DataFilter` algorithms `ALWAYS` and `MASKED-NEW-DIFFERS-MASKED-OLD` are only supported.

Since the AUTOSAR metamodel doesn't support to describe `DataFilter` on sender side at the moment, the following `AdminData` structure has to be used in the context of the `PPortPrototype`.

```
<P-PORT-PROTOTYPE>
  <SHORT-NAME>pPort1</SHORT-NAME>
  <ADMIN-DATA>
    <SDGS>
      <SDG GID="EB:DATA-FILTER">
        <SD GID="VARIABLE-DATA-PROTOTYPE">/interfaces/if1/de1</SD>
        <SD GID="DATA-FILTER-TYPE">MASKED-NEW-DIFFERS-MASKED-OLD</SD>
        <SD GID="MASK">0b11111111</SD>
      </SDG>
      <SDG GID="EB:DATA-FILTER">
        <SD GID="VARIABLE-DATA-PROTOTYPE">/interfaces/if1/de2</SD>
        <SD GID="DATA-FILTER-TYPE">MASKED-NEW-DIFFERS-MASKED-OLD</SD>
        <SD GID="MASK">0xffff</SD>
      </SDG>
    </SDGS>
  </ADMIN-DATA>
</P-PORT-PROTOTYPE>
```

```
</SDGS>  
</ADMIN-DATA>  
<PROVIDED-INTERFACE-TREF DEST="SENDER-RECEIVER-INTERFACE">/interfaces/if1</PROVIDED-INTERFACE-  
</P-PORT-PROTOTYPE>
```

## 2.4. Deviations

This chapter lists the deviations of the module from the AUTOSAR standard.

- By pass is not supported

Description:

By pass is not supported

Requirements:

SWS\_Rte\_01252, SWS\_Rte\_03550, SWS\_Rte\_08500, SWS\_Rte\_01111, SWS\_Rte\_01354, SWS\_Rte\_03553, SWS\_Rte\_01118, SWS\_Rte\_02628, SWS\_Rte\_02725, SWS\_Rte\_07200, SWS\_Rte\_07122, SWS\_Rte\_01083, SWS\_Rte\_01120, SWS\_Rte\_03560, SWS\_Rte\_01003, SWS\_Rte\_01102, SWS\_Rte\_02631, SWS\_Rte\_07367, SWS\_Rte\_02599, SWS\_Rte\_03565, SWS\_Rte\_03741, SWS\_Rte\_01123, SWS\_Rte\_07203, SWS\_Rte\_01206, SWS\_Rte\_02614, SWS\_Rte\_05509, SWS\_Rte\_01126, SWS\_Rte\_03800, SWS\_Rte\_03744, SWS\_Rte\_02619, SWS\_Rte\_07390, SWS\_Rte\_03928, SWS\_Rte\_01091, SWS\_Rte\_01072, SWS\_Rte\_07194, SWS\_Rte\_01150, SWS\_Rte\_01092, SWS\_Rte\_07394, SWS\_Rte\_01071

- Std\_TransformerForwardCode type is used in the RTE Send/Write/IWrite APIs instead of Std\_TransformerForward

Description:

Std\_TransformerForwardCode type is used in the RTE Send/Write/IWrite APIs instead of Std\_TransformerForward

Requirements:

SWS\_Rte\_06721, SWS\_Rte\_07207, SWS\_Rte\_01102, SWS\_Rte\_02631, SWS\_Rte\_02599, SWS\_Rte\_01111, SWS\_Rte\_01166, SWS\_Rte\_03741, SWS\_Rte\_06720, SWS\_Rte\_07203, SWS\_Rte\_01206, SWS\_Rte\_05509, SWS\_Rte\_03744, SWS\_Rte\_03608, SWS\_Rte\_01091, SWS\_Rte\_07200, SWS\_Rte\_01072, SWS\_Rte\_01092, SWS\_Rte\_07394, SWS\_Rte\_01071

- Std\_TransformerError is not supported

Description:

Rte\_TransformerError is used as the argument of RTE APIs instead of Std\_TransformerError

Requirements:

SWS\_Rte\_06721, SWS\_Rte\_07207, SWS\_Rte\_01102, SWS\_Rte\_02631, SWS\_Rte\_02599, SWS\_Rte\_01111, SWS\_Rte\_01166, SWS\_Rte\_03741, SWS\_Rte\_06720, SWS\_Rte\_07203, SWS\_Rte\_01206, SWS\_Rte\_05509, SWS\_Rte\_03744, SWS\_Rte\_03608, SWS\_Rte\_01091, SWS\_Rte\_07200, SWS\_Rte\_01072, SWS\_Rte\_01092, SWS\_Rte\_07394, SWS\_Rte\_01071, SWS\_Rte\_08791

- Metadata forwarding is not supported

Description:

Metadata forwarding of a data element to/from COM or LdCom to/from the application is not supported

Requirements:

SWS\_Rte\_06721, SWS\_Rte\_07207, SWS\_Rte\_01102, SWS\_Rte\_02631, SWS\_Rte\_02599, SWS\_Rte\_01111, SWS\_Rte\_01166, SWS\_Rte\_03741, SWS\_Rte\_06720, SWS\_Rte\_07203, SWS\_Rte\_01206, SWS\_Rte\_05509, SWS\_Rte\_03744, SWS\_Rte\_03608, SWS\_Rte\_01091, SWS\_Rte\_07200, SWS\_Rte\_01072, SWS\_Rte\_01092, SWS\_Rte\_07394, SWS\_Rte\_01071

- returnValueProvision of RTE API is not supported

Description:

This attribute controls the provision of return values for RTE APIs that correspond to the enclosing access point. The attribute is not evaluated.

Requirements:

SWS\_Rte\_06721, SWS\_Rte\_07207, SWS\_Rte\_01102, SWS\_Rte\_02631, SWS\_Rte\_02599, SWS\_Rte\_01111, SWS\_Rte\_01166, SWS\_Rte\_03741, SWS\_Rte\_06720, SWS\_Rte\_07203, SWS\_Rte\_01206, SWS\_Rte\_05509, SWS\_Rte\_03744, SWS\_Rte\_03608, SWS\_Rte\_01091, SWS\_Rte\_07200, SWS\_Rte\_01072, SWS\_Rte\_01092, SWS\_Rte\_07394, SWS\_Rte\_01071

- SwcExclusiveAreaPolicy.apiPrinciple is not evaluated

Description:

SwcExclusiveAreaPolicy.apiPrinciple is not evaluated

Requirements:

SWS\_Rte\_01120, SWS\_Rte\_01123, SWS\_Rte\_07253, SWS\_Rte\_03739, SWS\_Rte\_07250

- The symbol attribute of is not evaluated.

Description:

The symbol attribute of is not evaluated.

Requirements:

SWS\_Rte\_06713, SWS\_Rte\_01126, SWS\_Rte\_06714, SWS\_Rte\_07194, SWS\_Rte\_07163

- ▶ Client prefixes for VFB trace hook functions are not supported

Description:

Client prefixes for VFB trace hook functions are not supported

Requirements:

SWS\_Rte\_01250, SWS\_Rte\_01245, SWS\_Rte\_01246, SWS\_Rte\_01243, SWS\_Rte\_01244, SWS\_Rte\_01247

- ▶ Hook functions for the task are not supported according to R20-11

Description:

Hook functions for the task do not contain the os application and task name.

Requirements:

SWS\_Rte\_01250, SWS\_Rte\_01245, SWS\_Rte\_01246, SWS\_Rte\_01243, SWS\_Rte\_01244, SWS\_Rte\_01247

- ▶ Trusted function calls are not supported

Description:

Trusted function calls are not supported

Requirements:

SWS\_Rte\_07155, SWS\_Rte\_02527, SWS\_Rte\_02528

- ▶ Attributes HandleOutOfRange and HandleDataStatus is not evaluated

Description:

Attributes HandleOutOfRange and HandleDataStatus is not evaluated

Requirements:

SWS\_Rte\_02608, SWS\_Rte\_02600

- ▶ BindingTimeEnum is not evaluated

Description:

BindingTimeEnum is not evaluated



Requirements:

SWS\_Rte\_02613, SWS\_Rte\_03854, SWS\_Rte\_02615

- ▶ ShortName of the EcucPartition container is not used for determining the partition name.

Description:

ShortName of the EcucPartition container is not used for determining the partition name.

Requirements:

SWS\_Rte\_02712, SWS\_Rte\_02713, SWS\_Rte\_05088, SWS\_Rte\_07140

- ▶ Rte\_Ports API do not return NULL\_PTR if the port data structure for the specific port interface does not exist

Description:

Rte\_Ports API do not return NULL\_PTR if the port data structure for the specific port interface does not exist

Requirements:

SWS\_Rte\_03602

- ▶ Rte\_NPorts API do not return 0 if the port data structure for the port interface does not exist

Description:

Rte\_NPorts API do not return 0 if the port data structure for the port interface does not exist

Requirements:

SWS\_Rte\_03603

- ▶ BswModuleEntity.implementedEntry is not set/evaluated.

Description:

BswModuleEntity.implementedEntry is not set/evaluated.

Requirements:

SWS\_Rte\_05106, SWS\_Rte\_05177, SWS\_Rte\_05179, SWS\_Rte\_05180

- ▶ Det for some API are not supported

Description:

Det for some API are not supported

Requirements:

#### SWS\_Rte\_06632

- ▶ ServerArgument of primitive type is set to void when the ServerArgumentPolicy is set to useVoid

Description:

If the ServerArgumentImplPolicy is set to useVoid the data type of the &lt;

Requirements:

#### SWS\_Rte\_07027

- ▶ No support for self-referencing struct/union

Description:

No support for self-referencing struct/union

Requirements:

#### SWS\_Rte\_07114, SWS\_Rte\_07144

- ▶ PostBuild variant handling not supported

Description:

PostBuild variant handling not supported

Requirements:

#### SWS\_Rte\_07270

- ▶ It's not allowed to distribute RunnableEntities of a SWC to tasks or ISRs belonging to different partition

Description:

It's not allowed to distribute RunnableEntities of a SWC to tasks or ISRs belonging to different partition

Requirements:

#### SWS\_Rte\_07347

- ▶ SchM\_StartTiming API is not supported

Description:

SchM\_StartTiming API is not supported

Requirements:

#### SWS\_Rte\_07574

- ▶ Rte\_NvMNotifyJobFinished is not implemented as per R20-11

Description:

Rte\_NvMNotifyJobFinished do not accept the NvM\_BlockRequestType as argument, instead the serviceId of uint8 type is used.

Requirements:

SWS\_Rte\_07623

- ▶ LiteralPrefix attribute defined by the IncludedDataSet of the Referring AutosarDataType is not evaluated

Description:

LiteralPrefix attribute defined by the IncludedDataSet of the Referring AutosarDataType is not evaluated

Requirements:

SWS\_Rte\_08403

- ▶ Compu method with category RAT\_FUNC is not supported

Description:

Data conversion for compu method with category RAT\_FUNC is not supported

Requirements:

SWS\_Rte\_07928

- ▶ Rte\_LdComCbktTxConfirmation call back is not implemented according to R20-11

Description:

Rte\_LdComCbktTxConfirmation function has a new argument of Std\_ReturnType which informs RTE if the PDU transmission was successful. According to our current implementation, Rte\_LdComCbktTxConfirmation function do not accept any arguments.

Requirements:

SWS\_Rte\_01411

- ▶ NvM\_WritePRAMBlock API is not supported

Description:

NvM\_WritePRAMBlock API is not supported

Requirements:

SWS\_Rte\_08083, SWS\_Rte\_08082, SWS\_Rte\_08085, SWS\_Rte\_08084

- ▶ SWS\_Rte\_08795 is supported for both blocking and non blocking Rte\_Receive API

Description:

According to SWS\_Rte\_08795, RTE shall execute all transformers of the chain even when the queue is empty if the executeDespiteDataUnavailability of DataTransformation is enabled and the Rte\_Receive API has non-blocking characteristics. This behavior is also supported for blocking Rte\_Receive API

Requirements:

SWS\_Rte\_08795

- ▶ Rte\_Rips\_DRead API is not supported

Description:

Rte\_Rips\_DRead API is not supported

Requirements:

SWS\_Rte\_80075, SWS\_Rte\_80065

- ▶ Rips for parameter interfaces is not supported

Description:

Rips for parameter interfaces is not supported

Requirements:

SWS\_Rte\_80079

- ▶ Cross cluster communication is not supported

Description:

Cross cluster communication is not supported

Requirements:

SWS\_Rte\_CONSTR\_80002

- ▶ Changed functionalities

Description:

The listed requirement have been changed compared to previous AUTOSAR versions. They are currently not support and can be requested on demand.

### Requirements:

SWS\_Rte\_06712

► Partly implemented functionalities

Description:

The listed changed requirements are partly not support and can be requested on demand.

### Requirements:

SWS\_Rte\_06546, SWS\_Rte\_06547, SWS\_Rte\_06548, SWS\_Rte\_02741, SWS\_Rte\_03831, SWS\_Rte\_06541, SWS\_Rte\_02500, SWS\_Rte\_06542, SWS\_Rte\_07630, SWS\_Rte\_06543, SWS\_Rte\_02740, SWS\_Rte\_07632, SWS\_Rte\_02745, SWS\_Rte\_02746, SWS\_Rte\_02743, SWS\_Rte\_03833, SWS\_Rte\_02744, SWS\_Rte\_02749, SWS\_Rte\_03839, SWS\_Rte\_06549, SWS\_Rte\_07639, SWS\_Rte\_03837, SWS\_Rte\_06550, SWS\_Rte\_07640, SWS\_Rte\_07089, SWS\_Rte\_07083, SWS\_Rte\_07085, SWS\_Rte\_06534, SWS\_Rte\_06535, SWS\_Rte\_06536, SWS\_Rte\_06537, SWS\_Rte\_06530, SWS\_Rte\_08038, SWS\_Rte\_06531, SWS\_Rte\_08037, SWS\_Rte\_06532, SWS\_Rte\_06533, SWS\_Rte\_08039, SWS\_Rte\_02738, SWS\_Rte\_02739, SWS\_Rte\_06539, SWS\_Rte\_02736, SWS\_Rte\_02737, SWS\_Rte\_07190, SWS\_Rte\_08045, SWS\_Rte\_06540, SWS\_Rte\_08041, SWS\_Rte\_08040, SWS\_Rte\_07196, SWS\_Rte\_08042, SWS\_Rte\_06523, SWS\_Rte\_06765, SWS\_Rte\_06524, SWS\_Rte\_06525, SWS\_Rte\_06526, SWS\_Rte\_08027, SWS\_Rte\_06520, SWS\_Rte\_07179, SWS\_Rte\_08026, SWS\_Rte\_06521, SWS\_Rte\_08029, SWS\_Rte\_06522, SWS\_Rte\_07610, SWS\_Rte\_08028, SWS\_Rte\_02727, SWS\_Rte\_03817, SWS\_Rte\_06527, SWS\_Rte\_03816, SWS\_Rte\_06528, SWS\_Rte\_07616, SWS\_Rte\_03936, SWS\_Rte\_06529, SWS\_Rte\_07619, SWS\_Rte\_03935, SWS\_Rte\_02729, SWS\_Rte\_07181, SWS\_Rte\_07180, SWS\_Rte\_08034, SWS\_Rte\_07065, SWS\_Rte\_07186, SWS\_Rte\_08033, SWS\_Rte\_07068, SWS\_Rte\_08036, SWS\_Rte\_08035, SWS\_Rte\_02051, SWS\_Rte\_08030, SWS\_Rte\_07061, SWS\_Rte\_07064, SWS\_Rte\_07185, SWS\_Rte\_08032, SWS\_Rte\_08031, SWS\_Rte\_06512, SWS\_Rte\_06633, SWS\_Rte\_03001, SWS\_Rte\_06513, SWS\_Rte\_06634, SWS\_Rte\_06514, SWS\_Rte\_06515, SWS\_Rte\_08016, SWS\_Rte\_06510, SWS\_Rte\_04577, SWS\_Rte\_06511, SWS\_Rte\_08017, SWS\_Rte\_02710, SWS\_Rte\_02711, SWS\_Rte\_06516, SWS\_Rte\_07606, SWS\_Rte\_06517, SWS\_Rte\_06638, SWS\_Rte\_06518, SWS\_Rte\_06519, SWS\_Rte\_07055, SWS\_Rte\_08022, SWS\_Rte\_08025, SWS\_Rte\_07056, SWS\_Rte\_08024, SWS\_Rte\_07053, SWS\_Rte\_07173, SWS\_Rte\_08020, SWS\_Rte\_06501, SWS\_Rte\_06502, SWS\_Rte\_06503, SWS\_Rte\_06504, SWS\_Rte\_07399, SWS\_Rte\_08004, SWS\_Rte\_06620, SWS\_Rte\_06500, SWS\_Rte\_07038, SWS\_Rte\_06509, SWS\_Rte\_06505, SWS\_Rte\_06507, SWS\_Rte\_06508, SWS\_Rte\_07044, SWS\_Rte\_07043, SWS\_Rte\_07045, SWS\_Rte\_07042, SWS\_Rte\_07041, SWS\_Rte\_06611, SWS\_Rte\_06612, SWS\_Rte\_07029, SWS\_Rte\_06613, SWS\_Rte\_03582, SWS\_Rte\_07026, SWS\_Rte\_01169, SWS\_Rte\_06610, SWS\_Rte\_07148, SWS\_Rte\_03901, SWS\_Rte\_05090, SWS\_Rte\_05091, SWS\_Rte\_08001, SWS\_Rte\_07032, SWS\_Rte\_07035, SWS\_Rte\_07398, SWS\_Rte\_05099, SWS\_Rte\_07034, SWS\_Rte\_01292, SWS\_Rte\_05092, SWS\_Rte\_05093, SWS\_Rte\_07031, SWS\_Rte\_05094, SWS\_Rte\_07032, SWS\_Rte\_07033, SWS\_Rte\_07034, SWS\_Rte\_07035, SWS\_Rte\_07036, SWS\_Rte\_07037, SWS\_Rte\_07038, SWS\_Rte\_07039, SWS\_Rte\_07040, SWS\_Rte\_07041, SWS\_Rte\_07042, SWS\_Rte\_07043, SWS\_Rte\_07044, SWS\_Rte\_07045, SWS\_Rte\_07046, SWS\_Rte\_07047, SWS\_Rte\_07048, SWS\_Rte\_07049, SWS\_Rte\_07050, SWS\_Rte\_07051, SWS\_Rte\_07052, SWS\_Rte\_07053, SWS\_Rte\_07054, SWS\_Rte\_07055, SWS\_Rte\_07056, SWS\_Rte\_07057, SWS\_Rte\_07058, SWS\_Rte\_07059, SWS\_Rte\_07060, SWS\_Rte\_07061, SWS\_Rte\_07062, SWS\_Rte\_07063, SWS\_Rte\_07064, SWS\_Rte\_07065, SWS\_Rte\_07066, SWS\_Rte\_07067, SWS\_Rte\_07068, SWS\_Rte\_07069, SWS\_Rte\_07070, SWS\_Rte\_07071, SWS\_Rte\_07072, SWS\_Rte\_07073, SWS\_Rte\_07074, SWS\_Rte\_07075, SWS\_Rte\_07076, SWS\_Rte\_07077, SWS\_Rte\_07078, SWS\_Rte\_07079, SWS\_Rte\_07080, SWS\_Rte\_07081, SWS\_Rte\_07082, SWS\_Rte\_07083, SWS\_Rte\_07084, SWS\_Rte\_07085, SWS\_Rte\_07086, SWS\_Rte\_07087, SWS\_Rte\_07088, SWS\_Rte\_07089, SWS\_Rte\_07090, SWS\_Rte\_07091, SWS\_Rte\_07092, SWS\_Rte\_07093, SWS\_Rte\_07094, SWS\_Rte\_07095, SWS\_Rte\_07096, SWS\_Rte\_07097, SWS\_Rte\_07098, SWS\_Rte\_07099, SWS\_Rte\_07100, SWS\_Rte\_07101, SWS\_Rte\_07102, SWS\_Rte\_07103, SWS\_Rte\_07104, SWS\_Rte\_07105, SWS\_Rte\_07106, SWS\_Rte\_07107, SWS\_Rte\_07108, SWS\_Rte\_07109, SWS\_Rte\_07110, SWS\_Rte\_07111, SWS\_Rte\_07112, SWS\_Rte\_07113, SWS\_Rte\_07114, SWS\_Rte\_07115, SWS\_Rte\_07116, SWS\_Rte\_07117, SWS\_Rte\_07118, SWS\_Rte\_07119, SWS\_Rte\_07120, SWS\_Rte\_07121, SWS\_Rte\_07122, SWS\_Rte\_07123, SWS\_Rte\_07124, SWS\_Rte\_07125, SWS\_Rte\_07126, SWS\_Rte\_07127, SWS\_Rte\_07128, SWS\_Rte\_07129, SWS\_Rte\_07130, SWS\_Rte\_07131, SWS\_Rte\_07132, SWS\_Rte\_07133, SWS\_Rte\_07134, SWS\_Rte\_07135, SWS\_Rte\_07136, SWS\_Rte\_07137, SWS\_Rte\_07138, SWS\_Rte\_07139, SWS\_Rte\_07140, SWS\_Rte\_07141, SWS\_Rte\_07142, SWS\_Rte\_07143, SWS\_Rte\_07144, SWS\_Rte\_07145, SWS\_Rte\_07146, SWS\_Rte\_07147, SWS\_Rte\_07148, SWS\_Rte\_07149, SWS\_Rte\_07150, SWS\_Rte\_07151, SWS\_Rte\_07152, SWS\_Rte\_07153, SWS\_Rte\_07154, SWS\_Rte\_07155, SWS\_Rte\_07156, SWS\_Rte\_07157, SWS\_Rte\_07158, SWS\_Rte\_07159, SWS\_Rte\_07160, SWS\_Rte\_07161, SWS\_Rte\_07162, SWS\_Rte\_07163, SWS\_Rte\_07164, SWS\_Rte\_07165, SWS\_Rte\_07166, SWS\_Rte\_07167, SWS\_Rte\_07168, SWS\_Rte\_07169, SWS\_Rte\_07170, SWS\_Rte\_07171, SWS\_Rte\_07172, SWS\_Rte\_07173, SWS\_Rte\_07174, SWS\_Rte\_07175, SWS\_Rte\_07176, SWS\_Rte\_07177, SWS\_Rte\_07178, SWS\_Rte\_07179, SWS\_Rte\_07180, SWS\_Rte\_07181, SWS\_Rte\_07182, SWS\_Rte\_07183, SWS\_Rte\_07184, SWS\_Rte\_07185, SWS\_Rte\_07186, SWS\_Rte\_07187, SWS\_Rte\_07188, SWS\_Rte\_07189, SWS\_Rte\_07190, SWS\_Rte\_07191, SWS\_Rte\_07192, SWS\_Rte\_07193, SWS\_Rte\_07194, SWS\_Rte\_07195, SWS\_Rte\_07196, SWS\_Rte\_07197, SWS\_Rte\_07198, SWS\_Rte\_07199, SWS\_Rte\_07200, SWS\_Rte\_07201, SWS\_Rte\_07202, SWS\_Rte\_07203, SWS\_Rte\_07204, SWS\_Rte\_07205, SWS\_Rte\_07206, SWS\_Rte\_07207, SWS\_Rte\_07208, SWS\_Rte\_07209, SWS\_Rte\_07210, SWS\_Rte\_07211, SWS\_Rte\_07212, SWS\_Rte\_07213, SWS\_Rte\_07214, SWS\_Rte\_07215, SWS\_Rte\_07216, SWS\_Rte\_07217, SWS\_Rte\_07218, SWS\_Rte\_07219, SWS\_Rte\_07220, SWS\_Rte\_07221, SWS\_Rte\_07222, SWS\_Rte\_07223, SWS\_Rte\_07224, SWS\_Rte\_07225, SWS\_Rte\_07226, SWS\_Rte\_07227, SWS\_Rte\_07228, SWS\_Rte\_07229, SWS\_Rte\_07230, SWS\_Rte\_07231, SWS\_Rte\_07232, SWS\_Rte\_07233, SWS\_Rte\_07234, SWS\_Rte\_07235, SWS\_Rte\_07236, SWS\_Rte\_07237, SWS\_Rte\_07238, SWS\_Rte\_07239, SWS\_Rte\_07240, SWS\_Rte\_07241, SWS\_Rte\_07242, SWS\_Rte\_07243, SWS\_R

Rte\_07030, SWS\_Rte\_07811, SWS\_Rte\_06601, SWS\_Rte\_06602, SWS\_Rte\_06603, SWS\_Rte\_06724, SWS\_Rte\_07812, SWS\_Rte\_07137, SWS\_Rte\_06604, SWS\_Rte\_06725, SWS\_Rte\_06605, SWS\_Rte\_06726, SWS\_Rte\_06606, SWS\_Rte\_07022, SWS\_Rte\_07021, SWS\_Rte\_07020, SWS\_Rte\_07261, SWS\_Rte\_05500, SWS\_Rte\_07800, SWS\_Rte\_07921, SWS\_Rte\_07920, SWS\_Rte\_07802, SWS\_Rte\_07923, SWS\_Rte\_02593, SWS\_Rte\_07801, SWS\_Rte\_07922, SWS\_Rte\_05188, SWS\_Rte\_05189, SWS\_Rte\_06718, SWS\_Rte\_07808, SWS\_Rte\_06719, SWS\_Rte\_07807, SWS\_Rte\_07809, SWS\_Rte\_07804, SWS\_Rte\_06715, SWS\_Rte\_07803, SWS\_Rte\_07924, SWS\_Rte\_06716, SWS\_Rte\_07806, SWS\_Rte\_06717, SWS\_Rte\_07805, SWS\_Rte\_05191, SWS\_Rte\_05192, SWS\_Rte\_03793, SWS\_Rte\_06700, SWS\_Rte\_06701, SWS\_Rte\_06702, SWS\_Rte\_CONSTR\_09092, SWS\_Rte\_07116, SWS\_Rte\_CONSTR\_09094, SWS\_Rte\_06707, SWS\_Rte\_06708, SWS\_Rte\_04526, SWS\_Rte\_06705, SWS\_Rte\_06706, SWS\_Rte\_05184, SWS\_Rte\_05064, SWS\_Rte\_05185, SWS\_Rte\_05065, SWS\_Rte\_05182, SWS\_Rte\_05183, SWS\_Rte\_07349, SWS\_Rte\_01242, SWS\_Rte\_02573, SWS\_Rte\_07109, SWS\_Rte\_02697, SWS\_Rte\_07344, SWS\_Rte\_05168, SWS\_Rte\_05169, SWS\_Rte\_07104, SWS\_Rte\_07346, SWS\_Rte\_01249, SWS\_Rte\_01248, SWS\_Rte\_07110, SWS\_Rte\_05053, SWS\_Rte\_05174, SWS\_Rte\_05175, SWS\_Rte\_05176, SWS\_Rte\_07111, SWS\_Rte\_05170, SWS\_Rte\_05172, SWS\_Rte\_05159, SWS\_Rte\_03771, SWS\_Rte\_08305, SWS\_Rte\_05155, SWS\_Rte\_08302, SWS\_Rte\_05156, SWS\_Rte\_08301, SWS\_Rte\_05157, SWS\_Rte\_07336, SWS\_Rte\_08304, SWS\_Rte\_05158, SWS\_Rte\_01238, SWS\_Rte\_01239, SWS\_Rte\_05162, SWS\_Rte\_07583, SWS\_Rte\_07582, SWS\_Rte\_07585, SWS\_Rte\_05160, SWS\_Rte\_05161, SWS\_Rte\_05148, SWS\_Rte\_05144, SWS\_Rte\_03764, SWS\_Rte\_07564, SWS\_Rte\_07685, SWS\_Rte\_05146, SWS\_Rte\_02679, SWS\_Rte\_03526, SWS\_Rte\_01346, SWS\_Rte\_01105, SWS\_Rte\_01347, SWS\_Rte\_02557, SWS\_Rte\_05152, SWS\_Rte\_07692, SWS\_Rte\_05153, SWS\_Rte\_05154, SWS\_Rte\_07694, SWS\_Rte\_05150, SWS\_Rte\_05137, SWS\_Rte\_01330, SWS\_Rte\_05138, SWS\_Rte\_05139, SWS\_Rte\_08400, SWS\_Rte\_02662, SWS\_Rte\_01332, SWS\_Rte\_02663, SWS\_Rte\_05136, SWS\_Rte\_05140, SWS\_Rte\_07682, SWS\_Rte\_05141, SWS\_Rte\_07681, SWS\_Rte\_05142, SWS\_Rte\_07684, SWS\_Rte\_05143, SWS\_Rte\_07680, SWS\_Rte\_03860, SWS\_Rte\_03980, SWS\_Rte\_07300, SWS\_Rte\_07663, SWS\_Rte\_08510, SWS\_Rte\_03620, SWS\_Rte\_03861, SWS\_Rte\_03868, SWS\_Rte\_02658, SWS\_Rte\_07670, SWS\_Rte\_08088, SWS\_Rte\_07673, SWS\_Rte\_05115, SWS\_Rte\_02760, SWS\_Rte\_07535, SWS\_Rte\_08742, SWS\_Rte\_03731, SWS\_Rte\_02640, SWS\_Rte\_02761, SWS\_Rte\_05113, SWS\_Rte\_07534, SWS\_Rte\_05114, SWS\_Rte\_07533, SWS\_Rte\_03978, SWS\_Rte\_03977, SWS\_Rte\_03976, SWS\_Rte\_03975, SWS\_Rte\_08507, SWS\_Rte\_03979, SWS\_Rte\_08509, SWS\_Rte\_07662, SWS\_Rte\_05121, SWS\_Rte\_07525, SWS\_Rte\_07645, SWS\_Rte\_02752, SWS\_Rte\_03842, SWS\_Rte\_07642, SWS\_Rte\_02753, SWS\_Rte\_03841, SWS\_Rte\_05101, SWS\_Rte\_03840, SWS\_Rte\_05102, SWS\_Rte\_02756, SWS\_Rte\_03846, SWS\_Rte\_02757, SWS\_Rte\_03845, SWS\_Rte\_02754, SWS\_Rte\_02755, SWS\_Rte\_03843, SWS\_Rte\_02639, SWS\_Rte\_03849, SWS\_Rte\_03848, SWS\_Rte\_02759, SWS\_Rte\_03847, SWS\_Rte\_07091, SWS\_Rte\_07093, SWS\_Rte\_07092, SWS\_Rte\_07099, SWS\_Rte\_07095, SWS\_Rte\_07094, SWS\_Rte\_07097, SWS\_Rte\_07096

► Unsupported functionalities

Description:

The listed requirements are currently not support and can be requested on demand.

Requirements:

SWS\_Rte\_08603, SWS\_Rte\_08605, SWS\_Rte\_08604, SWS\_Rte\_08601, SWS\_Rte\_08600, ECUC\_Rte\_09129, ECUC\_Rte\_09136, ECUC\_Rte\_09137, ECUC\_Rte\_09134, ECUC\_Rte\_09135, ECUC\_Rte\_09132, ECUC\_Rte\_09133, ECUC\_Rte\_09130, ECUC\_Rte\_09131, ECUC\_Rte\_09149, ECUC\_Rte\_09147, ECUC\_Rte\_09148, ECUC\_Rte\_09145, ECUC\_Rte\_09146, ECUC\_Rte\_09143, ECUC\_Rte\_09144, ECUC\_Rte\_09142, ECUC\_Rte\_09116, ECUC\_Rte\_09117, ECUC\_Rte\_09114, ECUC\_Rte\_09115, ECUC\_Rte\_09112, ECUC\_Rte\_09113, SWS\_Rte\_05300, ECUC\_Rte\_09118, ECUC\_Rte\_09119, ECUC\_Rte\_09127, ECUC\_Rte\_09128, ECUC\_Rte\_09125, ECUC\_Rte\_79004, ECUC\_Rte\_09126, ECUC\_Rte\_79003, ECUC\_Rte\_09123, ECUC\_Rte\_79006, ECUC\_Rte\_09124, ECUC\_Rte\_79005, ECUC\_Rte\_09121, ECUC\_Rte\_79008, ECUC\_Rte\_09122, ECUC\_Rte\_79007, ECUC\_Rte\_09120, SWS\_Rte\_08801, SWS\_Rte\_08800, SWS\_Rte\_08803, SWS\_Rte\_08802, SWS\_Rte\_01164, ECUC\_Rte\_09105, ECUC\_Rte\_09106, ECUC\_Rte\_09103, ECUC\_Rte\_09104, ECUC\_Rte\_09101, ECUC\_Rte\_09102, ECUC\_Rte\_09100, SWS\_Rte\_01393, SWS\_Rte\_01394, SWS\_Rte\_06840, SWS\_Rte\_06832, SWS\_Rte\_06833, SWS\_Rte\_06834, SWS\_Rte\_01389, SWS\_Rte\_06830, SWS\_Rte\_06839, SWS\_Rte\_07925, SWS\_Rte\_06835, SWS\_Rte\_06836, SWS\_Rte\_07927, SWS\_Rte\_06837, SWS\_Rte\_07926, SWS\_Rte\_06838, SWS\_Rte\_01391, SWS\_Rte\_01392, SWS\_Rte\_01390, SWS\_Rte\_CONSTR\_09091, SWS\_Rte\_CONSTR\_09090, SWS\_Rte\_CONSTR\_09093, SWS\_Rte\_CONSTR\_09095, SWS\_Rte\_06031, SWS\_Rte\_CONSTR\_09086, SWS\_Rte\_CONSTR\_09085, SWS\_Rte\_CONSTR\_09088, SWS\_Rte\_06033, SWS\_Rte\_CONSTR\_09087, SWS\_Rte\_06034, SWS\_Rte\_CONSTR\_09089, SWS\_Rte\_08423, SWS\_Rte\_08424, SWS\_Rte\_70133, SWS\_Rte\_70134, SWS\_Rte\_70135, SWS\_Rte\_70136, SWS\_Rte\_70130, SWS\_Rte\_70131, SWS\_Rte\_70132, SWS\_Rte\_70137, SWS\_Rte\_70138, SWS\_Rte\_70139, SWS\_Rte\_08416, SWS\_Rte\_08417, SWS\_Rte\_08413, SWS\_Rte\_70140, SWS\_Rte\_08419, SWS\_Rte\_70141, SWS\_Rte\_70142, SWS\_Rte\_08405, SWS\_Rte\_08407, SWS\_Rte\_08406, SWS\_Rte\_06204, SWS\_Rte\_06205, SWS\_Rte\_06206, SWS\_Rte\_06200, SWS\_Rte\_06201, SWS\_Rte\_06203, SWS\_Rte\_06210, SWS\_Rte\_80116, SWS\_Rte\_80117, SWS\_Rte\_80114, SWS\_Rte\_80115, SWS\_Rte\_80118, SWS\_Rte\_80119, SWS\_Rte\_CONSTR\_09006, SWS\_Rte\_CONSTR\_09005, SWS\_Rte\_CONSTR\_09008, SWS\_Rte\_CONSTR\_09007, SWS\_Rte\_CONSTR\_09000, SWS\_Rte\_CONSTR\_09002, SWS\_Rte\_80112, SWS\_Rte\_CONSTR\_09001, SWS\_Rte\_80113, SWS\_Rte\_80110, SWS\_Rte\_80111, SWS\_Rte\_80127, SWS\_Rte\_80128, SWS\_Rte\_80125, SWS\_Rte\_80126, SWS\_Rte\_80129, SWS\_Rte\_91000, SWS\_Rte\_CONSTR\_09017, SWS\_Rte\_CONSTR\_09016, SWS\_Rte\_CONSTR\_09019, SWS\_Rte\_CONSTR\_09018, SWS\_Rte\_80120, SWS\_Rte\_CONSTR\_09011, SWS\_Rte\_CONSTR\_09010, SWS\_Rte\_CONSTR\_09013, SWS\_Rte\_80123, SWS\_Rte\_CONSTR\_09012, SWS\_Rte\_80124, SWS\_Rte\_CONSTR\_09015, SWS\_Rte\_80121, SWS\_Rte\_CONSTR\_09014, SWS\_Rte\_80122, SWS\_Rte\_CONSTR\_09028, SWS\_Rte\_CONSTR\_09027, SWS\_Rte\_CONSTR\_09029, SWS\_Rte\_CONSTR\_09020, SWS\_Rte\_CONSTR\_09022, SWS\_Rte\_CONSTR\_09021, SWS\_Rte\_CONSTR\_09024, SWS\_Rte\_CONSTR\_09023, SWS\_Rte\_CONSTR\_09026, SWS\_Rte\_CONSTR\_09025, SWS\_Rte\_80105, SWS\_Rte\_80106, SWS\_Rte\_80103, SWS\_Rte\_80104, SWS\_Rte\_80109, SWS\_Rte\_CONSTR\_09040, SWS\_Rte\_80107, SWS\_Rte\_80108, SWS\_Rte\_CONSTR\_09039, SWS\_Rte\_CONSTR\_09038, SWS\_Rte\_CONSTR\_09031, SWS\_Rte\_CONSTR\_09030, SWS\_Rte\_CONSTR\_09033, SWS\_Rte\_CONSTR\_09032, SWS\_Rte\_CONSTR\_09035, SWS\_Rte\_80101, SWS\_Rte\_CONSTR\_09034, SWS\_Rte\_80102, SWS\_Rte\_CONSTR\_09037, SWS\_Rte\_CONSTR\_09036, SWS\_Rte\_80100, SWS\_Rte\_CONSTR\_09051, SWS\_Rte\_CONSTR\_09050, SWS\_Rte\_70111, SWS\_Rte\_70112, SWS\_Rte\_70113, SWS\_Rte\_70114, SWS\_Rte\_70110, SWS\_Rte\_

70119, SWS\_Rte\_CONSTR\_09049, SWS\_Rte\_70115, SWS\_Rte\_70116, SWS\_Rte\_70117, SWS\_Rte\_70118, SWS\_Rte\_CONSTR\_09042, SWS\_Rte\_80152, SWS\_Rte\_CONSTR\_09041, SWS\_Rte\_80153, SWS\_Rte\_80150, SWS\_Rte\_CONSTR\_09043, SWS\_Rte\_80151, SWS\_Rte\_CONSTR\_09046, SWS\_Rte\_CONSTR\_09045, SWS\_Rte\_CONSTR\_09048, SWS\_Rte\_80154, SWS\_Rte\_CONSTR\_09047, SWS\_Rte\_CONSTR\_09060, SWS\_Rte\_08598, SWS\_Rte\_CONSTR\_09061, SWS\_Rte\_70122, SWS\_Rte\_70123, SWS\_Rte\_70124, SWS\_Rte\_70125, SWS\_Rte\_70120, SWS\_Rte\_70121, SWS\_Rte\_70126, SWS\_Rte\_70127, SWS\_Rte\_70128, SWS\_Rte\_70129, SWS\_Rte\_CONSTR\_09053, SWS\_Rte\_CONSTR\_09052, SWS\_Rte\_CONSTR\_09055, SWS\_Rte\_CONSTR\_09054, SWS\_Rte\_CONSTR\_09057, SWS\_Rte\_CONSTR\_09056, SWS\_Rte\_CONSTR\_09059, SWS\_Rte\_CONSTR\_09058, SWS\_Rte\_80138, SWS\_Rte\_80139, SWS\_Rte\_80136, SWS\_Rte\_80137, SWS\_Rte\_CONSTR\_09064, SWS\_Rte\_08111, SWS\_Rte\_80130, SWS\_Rte\_CONSTR\_09063, SWS\_Rte\_08594, SWS\_Rte\_80131, SWS\_Rte\_CONSTR\_09066, SWS\_Rte\_CONSTR\_09065, SWS\_Rte\_08596, SWS\_Rte\_80134, SWS\_Rte\_80135, SWS\_Rte\_08593, SWS\_Rte\_80132, SWS\_Rte\_08592, SWS\_Rte\_80133, SWS\_Rte\_80149, SWS\_Rte\_CONSTR\_09080, SWS\_Rte\_80147, SWS\_Rte\_80148, SWS\_Rte\_08577, SWS\_Rte\_CONSTR\_09081, SWS\_Rte\_08576, SWS\_Rte\_CONSTR\_09084, SWS\_Rte\_CONSTR\_09083, SWS\_Rte\_70100, SWS\_Rte\_70101, SWS\_Rte\_70102, SWS\_Rte\_70103, SWS\_Rte\_70108, SWS\_Rte\_70109, SWS\_Rte\_70104, SWS\_Rte\_70105, SWS\_Rte\_70106, SWS\_Rte\_70107, SWS\_Rte\_80141, SWS\_Rte\_08583, SWS\_Rte\_CONSTR\_09077, SWS\_Rte\_CONSTR\_09076, SWS\_Rte\_08101, SWS\_Rte\_80140, SWS\_Rte\_CONSTR\_09079, SWS\_Rte\_80145, SWS\_Rte\_CONSTR\_09078, SWS\_Rte\_80146, SWS\_Rte\_08582, SWS\_Rte\_01373, SWS\_Rte\_01374, SWS\_Rte\_01371, SWS\_Rte\_01372, SWS\_Rte\_01375, SWS\_Rte\_06828, SWS\_Rte\_06810, SWS\_Rte\_06811, SWS\_Rte\_06812, SWS\_Rte\_06813, SWS\_Rte\_06814, SWS\_Rte\_06815, SWS\_Rte\_06816, SWS\_Rte\_06800, SWS\_Rte\_06801, SWS\_Rte\_06806, SWS\_Rte\_06807, SWS\_Rte\_06808, SWS\_Rte\_06809, SWS\_Rte\_06802, SWS\_Rte\_06803, SWS\_Rte\_06804, SWS\_Rte\_06805, ECUC\_Rte\_09198, ECUC\_Rte\_09199, ECUC\_Rte\_09196, ECUC\_Rte\_09197, ECUC\_Rte\_09194, ECUC\_Rte\_09195, ECUC\_Rte\_09192, ECUC\_Rte\_09193, ECUC\_Rte\_09190, ECUC\_Rte\_09191, SWS\_Rte\_04612, SWS\_Rte\_04611, SWS\_Rte\_04610, SWS\_Rte\_04614, SWS\_Rte\_04613, SWS\_Rte\_03992, SWS\_Rte\_03991, SWS\_Rte\_03990, SWS\_Rte\_04601, SWS\_Rte\_04600, SWS\_Rte\_03994, SWS\_Rte\_03993, SWS\_Rte\_04605, SWS\_Rte\_03999, SWS\_Rte\_04604, SWS\_Rte\_03998, SWS\_Rte\_04603, SWS\_Rte\_04602, SWS\_Rte\_04609, SWS\_Rte\_04608, SWS\_Rte\_04607, SWS\_Rte\_04606, ECUC\_Rte\_09179, ECUC\_Rte\_09176, ECUC\_Rte\_09177, ECUC\_Rte\_09174, ECUC\_Rte\_09175, ECUC\_Rte\_09173, ECUC\_Rte\_09170, ECUC\_Rte\_09171, SWS\_Rte\_03985, SWS\_Rte\_03988, SWS\_Rte\_03987, SWS\_Rte\_03986, ECUC\_Rte\_09189, ECUC\_Rte\_09187, ECUC\_Rte\_09188, ECUC\_Rte\_09185, ECUC\_Rte\_09186, ECUC\_Rte\_09183, ECUC\_Rte\_09184, ECUC\_Rte\_09181, ECUC\_Rte\_09182, ECUC\_Rte\_09180, ECUC\_Rte\_09158, ECUC\_Rte\_09159, ECUC\_Rte\_09156, ECUC\_Rte\_09157, ECUC\_Rte\_09154, ECUC\_Rte\_09155, ECUC\_Rte\_09152, ECUC\_Rte\_09153, ECUC\_Rte\_09150, ECUC\_Rte\_09151, ECUC\_Rte\_89014, ECUC\_Rte\_89013, ECUC\_Rte\_89011, ECUC\_Rte\_89010, ECUC\_Rte\_09169, ECUC\_Rte\_09167, ECUC\_Rte\_09168, ECUC\_Rte\_09165, ECUC\_Rte\_89009, ECUC\_Rte\_09166, ECUC\_Rte\_89008, ECUC\_Rte\_09163, ECUC\_Rte\_89007, ECUC\_Rte\_09164, ECUC\_Rte\_89006, ECUC\_Rte\_09161, ECUC\_Rte\_89005, ECUC\_Rte\_09162, ECUC\_Rte\_89004, ECUC\_Rte\_89003, ECUC\_Rte\_09160, ECUC\_Rte\_89002, ECUC\_Rte\_89001, ECUC\_Rte\_89000, SWS\_Rte\_06787, SWS\_Rte\_06788, SWS\_Rte\_06789, SWS\_Rte\_06783, SWS\_Rte\_06784, SWS\_Rte\_06785, SWS\_Rte\_06786, ECUC\_Rte\_09008, ECUC\_Rte\_09009, ECUC\_





Rte\_09017, ECUC\_Rte\_09018, ECUC\_Rte\_09015, ECUC\_Rte\_09013, ECUC\_Rte\_09014, ECUC\_Rte\_09011, ECUC\_Rte\_09012, SWS\_Rte\_06790, SWS\_Rte\_06791, ECUC\_Rte\_09010, SWS\_Rte\_06792, SWS\_Rte\_06551, SWS\_Rte\_06793, SWS\_Rte\_06776, SWS\_Rte\_04597, SWS\_Rte\_06777, SWS\_Rte\_04596, SWS\_Rte\_06778, SWS\_Rte\_04595, SWS\_Rte\_06779, SWS\_Rte\_04594, SWS\_Rte\_06772, SWS\_Rte\_06773, SWS\_Rte\_06774, SWS\_Rte\_04599, SWS\_Rte\_06775, SWS\_Rte\_04598, ECUC\_Rte\_09019, ECUC\_Rte\_09028, ECUC\_Rte\_09029, ECUC\_Rte\_09026, ECUC\_Rte\_09027, ECUC\_Rte\_09024, ECUC\_Rte\_09025, ECUC\_Rte\_09022, ECUC\_Rte\_09023, ECUC\_Rte\_09020, SWS\_Rte\_06780, ECUC\_Rte\_09021, SWS\_Rte\_06781, SWS\_Rte\_06782, SWS\_Rte\_70092, SWS\_Rte\_04586, SWS\_Rte\_70093, SWS\_Rte\_04585, SWS\_Rte\_70094, SWS\_Rte\_04584, SWS\_Rte\_70095, SWS\_Rte\_04583, SWS\_Rte\_07851, SWS\_Rte\_07850, SWS\_Rte\_70090, SWS\_Rte\_04588, SWS\_Rte\_70091, SWS\_Rte\_04587, SWS\_Rte\_70096, SWS\_Rte\_70097, SWS\_Rte\_70098, SWS\_Rte\_70099, SWS\_Rte\_06771, SWS\_Rte\_04593, SWS\_Rte\_04592, SWS\_Rte\_04591, SWS\_Rte\_04575, SWS\_Rte\_07843, SWS\_Rte\_04574, SWS\_Rte\_07846, SWS\_Rte\_04573, SWS\_Rte\_06757, SWS\_Rte\_04572, SWS\_Rte\_07840, SWS\_Rte\_04579, SWS\_Rte\_07842, SWS\_Rte\_07841, SWS\_Rte\_04576, SWS\_Rte\_07848, SWS\_Rte\_07847, SWS\_Rte\_07849, ECUC\_Rte\_09006, ECUC\_Rte\_09007, ECUC\_Rte\_09004, ECUC\_Rte\_09005, ECUC\_Rte\_09002, ECUC\_Rte\_09003, ECUC\_Rte\_09000, ECUC\_Rte\_09001, SWS\_Rte\_04581, SWS\_Rte\_04580, SWS\_Rte\_06743, SWS\_Rte\_07833, SWS\_Rte\_04564, SWS\_Rte\_06744, SWS\_Rte\_07832, SWS\_Rte\_04563, SWS\_Rte\_06745, SWS\_Rte\_07835, SWS\_Rte\_04562, SWS\_Rte\_06746, SWS\_Rte\_07834, SWS\_Rte\_04561, SWS\_Rte\_04568, SWS\_Rte\_06740, SWS\_Rte\_04567, SWS\_Rte\_06741, SWS\_Rte\_07831, SWS\_Rte\_04566, SWS\_Rte\_06742, SWS\_Rte\_07830, SWS\_Rte\_04565, SWS\_Rte\_04569, SWS\_Rte\_06747, SWS\_Rte\_07837, SWS\_Rte\_07836, SWS\_Rte\_07839, ECUC\_Rte\_09206, SWS\_Rte\_07838, SWS\_Rte\_04571, SWS\_Rte\_04570, SWS\_Rte\_06732, SWS\_Rte\_04553, SWS\_Rte\_08911, SWS\_Rte\_04552, SWS\_Rte\_08910, SWS\_Rte\_04551, SWS\_Rte\_04550, SWS\_Rte\_08912, SWS\_Rte\_04557, SWS\_Rte\_04556, SWS\_Rte\_06730, SWS\_Rte\_04554, SWS\_Rte\_04559, SWS\_Rte\_04558, SWS\_Rte\_06739, SWS\_Rte\_04560, SWS\_Rte\_04542, SWS\_Rte\_08900, SWS\_Rte\_04541, SWS\_Rte\_04540, SWS\_Rte\_08902, SWS\_Rte\_08901, SWS\_Rte\_04546, SWS\_Rte\_04545, SWS\_Rte\_04543, SWS\_Rte\_06729, SWS\_Rte\_08908, SWS\_Rte\_04549, SWS\_Rte\_04548, SWS\_Rte\_04547, SWS\_Rte\_08909, SWS\_Rte\_08904, SWS\_Rte\_08903, SWS\_Rte\_06727, SWS\_Rte\_08906, SWS\_Rte\_06728, SWS\_Rte\_08905, SWS\_Rte\_04531, SWS\_Rte\_04530, SWS\_Rte\_04532, SWS\_Rte\_04538, SWS\_Rte\_04536, ECUC\_Rte\_09204, ECUC\_Rte\_09205, ECUC\_Rte\_09202, ECUC\_Rte\_09203, ECUC\_Rte\_09200, ECUC\_Rte\_09201, SWS\_Rte\_82018, SWS\_Rte\_82017, SWS\_Rte\_08565, SWS\_Rte\_08568, SWS\_Rte\_70034, SWS\_Rte\_70035, SWS\_Rte\_70036, SWS\_Rte\_70037, SWS\_Rte\_70030, SWS\_Rte\_70031, SWS\_Rte\_70032, SWS\_Rte\_70033, SWS\_Rte\_70038, SWS\_Rte\_80071, SWS\_Rte\_70039, SWS\_Rte\_80072, SWS\_Rte\_80070, SWS\_Rte\_82012, SWS\_Rte\_82011, SWS\_Rte\_82010, SWS\_Rte\_80074, SWS\_Rte\_82016, SWS\_Rte\_82015, SWS\_Rte\_82014, SWS\_Rte\_80078, SWS\_Rte\_82013, SWS\_Rte\_08317, SWS\_Rte\_08316, SWS\_Rte\_08319, SWS\_Rte\_08318, SWS\_Rte\_70040, SWS\_Rte\_08313, SWS\_Rte\_08312, SWS\_Rte\_08315, SWS\_Rte\_08314, SWS\_Rte\_70045, SWS\_Rte\_70046, SWS\_Rte\_70047, SWS\_Rte\_70048, SWS\_Rte\_70042, SWS\_Rte\_70043, SWS\_Rte\_70044, SWS\_Rte\_70049, SWS\_Rte\_80082, SWS\_Rte\_80083, SWS\_Rte\_80080, SWS\_Rte\_80081, SWS\_Rte\_08320, SWS\_Rte\_08322, SWS\_Rte\_08321, SWS\_Rte\_80085, SWS\_Rte\_08789, SWS\_Rte\_80059, SWS\_Rte\_08786, SWS\_Rte\_08785, SWS\_Rte\_08788, SWS\_Rte\_08787, SWS\_Rte\_70012, SWS\_Rte\_70013, SWS\_Rte\_70015, SWS\_Rte\_08309, SWS\_Rte\_70010, SWS\_Rte\_70011, SWS\_Rte\_70016,



SWS\_Rte\_70017, SWS\_Rte\_70018, SWS\_Rte\_70019, SWS\_Rte\_08793, SWS\_Rte\_08311, SWS\_Rte\_80057, SWS\_Rte\_80058, SWS\_Rte\_08790, SWS\_Rte\_80056, SWS\_Rte\_82009, SWS\_Rte\_08778, SWS\_Rte\_82008, SWS\_Rte\_82007, SWS\_Rte\_82006, SWS\_Rte\_08775, SWS\_Rte\_06112, SWS\_Rte\_08774, SWS\_Rte\_06113, SWS\_Rte\_06114, SWS\_Rte\_08776, SWS\_Rte\_06115, SWS\_Rte\_70023, SWS\_Rte\_70024, SWS\_Rte\_70025, SWS\_Rte\_70026, SWS\_Rte\_70020, SWS\_Rte\_70021, SWS\_Rte\_70022, SWS\_Rte\_70027, SWS\_Rte\_80060, SWS\_Rte\_70028, SWS\_Rte\_80061, SWS\_Rte\_70029, SWS\_Rte\_08782, SWS\_Rte\_80064, SWS\_Rte\_82001, SWS\_Rte\_06120, SWS\_Rte\_82000, SWS\_Rte\_08784, SWS\_Rte\_80063, SWS\_Rte\_80068, SWS\_Rte\_82005, SWS\_Rte\_80069, SWS\_Rte\_82004, SWS\_Rte\_08780, SWS\_Rte\_82003, SWS\_Rte\_80067, SWS\_Rte\_82002, SWS\_Rte\_08768, SWS\_Rte\_06105, SWS\_Rte\_70070, SWS\_Rte\_08767, SWS\_Rte\_06106, SWS\_Rte\_70071, SWS\_Rte\_06107, SWS\_Rte\_70072, SWS\_Rte\_08769, SWS\_Rte\_06108, SWS\_Rte\_70073, SWS\_Rte\_08764, SWS\_Rte\_06101, SWS\_Rte\_08763, SWS\_Rte\_06102, SWS\_Rte\_08766, SWS\_Rte\_06103, SWS\_Rte\_CONSTR\_04559, SWS\_Rte\_06104, SWS\_Rte\_CONSTR\_04558, SWS\_Rte\_70078, SWS\_Rte\_72015, SWS\_Rte\_70079, SWS\_Rte\_72014, SWS\_Rte\_72017, SWS\_Rte\_72016, SWS\_Rte\_06109, SWS\_Rte\_70074, SWS\_Rte\_72011, SWS\_Rte\_70075, SWS\_Rte\_72010, SWS\_Rte\_70076, SWS\_Rte\_72013, SWS\_Rte\_70077, SWS\_Rte\_72012, SWS\_Rte\_72019, SWS\_Rte\_CONSTR\_80031, SWS\_Rte\_72018, SWS\_Rte\_CONSTR\_80030, SWS\_Rte\_08771, SWS\_Rte\_08770, SWS\_Rte\_08773, SWS\_Rte\_06110, SWS\_Rte\_08772, SWS\_Rte\_06111, SWS\_Rte\_07426, SWS\_Rte\_70081, SWS\_Rte\_08514, SWS\_Rte\_07425, SWS\_Rte\_70082, SWS\_Rte\_70083, SWS\_Rte\_72020, SWS\_Rte\_07427, SWS\_Rte\_70084, SWS\_Rte\_08511, SWS\_Rte\_07422, SWS\_Rte\_07421, SWS\_Rte\_08513, SWS\_Rte\_07424, SWS\_Rte\_08512, SWS\_Rte\_07423, SWS\_Rte\_70080, SWS\_Rte\_70089, SWS\_Rte\_70085, SWS\_Rte\_72022, SWS\_Rte\_70086, SWS\_Rte\_72021, SWS\_Rte\_70087, SWS\_Rte\_70088, SWS\_Rte\_72023, SWS\_Rte\_06100, SWS\_Rte\_07415, SWS\_Rte\_07414, SWS\_Rte\_70050, SWS\_Rte\_70051, SWS\_Rte\_70056, SWS\_Rte\_70057, SWS\_Rte\_70058, SWS\_Rte\_70059, SWS\_Rte\_70052, SWS\_Rte\_70053, SWS\_Rte\_70054, SWS\_Rte\_70055, SWS\_Rte\_06799, SWS\_Rte\_70060, SWS\_Rte\_70061, SWS\_Rte\_70062, SWS\_Rte\_06552, SWS\_Rte\_06794, SWS\_Rte\_06553, SWS\_Rte\_06795, SWS\_Rte\_06796, SWS\_Rte\_06797, SWS\_Rte\_70067, SWS\_Rte\_72004, SWS\_Rte\_70068, SWS\_Rte\_72003, SWS\_Rte\_70069, SWS\_Rte\_72006, SWS\_Rte\_72005, SWS\_Rte\_08739, SWS\_Rte\_70063, SWS\_Rte\_72000, SWS\_Rte\_70064, SWS\_Rte\_70065, SWS\_Rte\_72002, SWS\_Rte\_70066, SWS\_Rte\_72001, SWS\_Rte\_72008, SWS\_Rte\_72007, SWS\_Rte\_72009, SWS\_Rte\_CONSTR\_04584, SWS\_Rte\_08049, SWS\_Rte\_01413, SWS\_Rte\_08050, SWS\_Rte\_CONSTR\_09121, SWS\_Rte\_CONSTR\_09120, SWS\_Rte\_CONSTR\_09123, SWS\_Rte\_CONSTR\_09122, SWS\_Rte\_CONSTR\_09124, SWS\_Rte\_91122, SWS\_Rte\_91121, SWS\_Rte\_CONSTR\_80003, SWS\_Rte\_CONSTR\_80000, SWS\_Rte\_CONSTR\_80009, SWS\_Rte\_CONSTR\_80006, SWS\_Rte\_CONSTR\_80004, SWS\_Rte\_CONSTR\_80005, SWS\_Rte\_91111, SWS\_Rte\_91110, SWS\_Rte\_91113, SWS\_Rte\_91112, SWS\_Rte\_CONSTR\_80013, SWS\_Rte\_91115, SWS\_Rte\_06090, SWS\_Rte\_CONSTR\_80014, SWS\_Rte\_91114, SWS\_Rte\_06091, SWS\_Rte\_CONSTR\_80011, SWS\_Rte\_89023, SWS\_Rte\_91117, SWS\_Rte\_06092, SWS\_Rte\_CONSTR\_80012, SWS\_Rte\_91116, SWS\_Rte\_89021, SWS\_Rte\_91119, SWS\_Rte\_CONSTR\_80010, SWS\_Rte\_89022, SWS\_Rte\_89020, SWS\_Rte\_06097, SWS\_Rte\_06098, SWS\_Rte\_06099, SWS\_Rte\_CONSTR\_80019, SWS\_Rte\_06093, SWS\_Rte\_CONSTR\_80017, SWS\_Rte\_06094, SWS\_Rte\_06095, SWS\_Rte\_CONSTR\_80015, SWS\_Rte\_06096, SWS\_Rte\_CONSTR\_80016, SWS\_Rte\_06079, SWS\_Rte\_91100, SWS\_Rte\_91102, SWS\_Rte\_91101, SWS\_Rte\_91104, SWS\_Rte\_CONSTR\_80024, SWS\_Rte\_91103, SWS\_Rte\_CONSTR\_80025,

SWS\_Rte\_06080, SWS\_Rte\_91106, SWS\_Rte\_CONSTR\_80022, SWS\_Rte\_06081, SWS\_Rte\_91105, SWS\_Rte\_CONSTR\_80023, SWS\_Rte\_91108, SWS\_Rte\_CONSTR\_80020, SWS\_Rte\_91107, SWS\_Rte\_CONSTR\_80021, SWS\_Rte\_91109, SWS\_Rte\_06086, SWS\_Rte\_06087, SWS\_Rte\_06088, SWS\_Rte\_06089, SWS\_Rte\_06082, SWS\_Rte\_CONSTR\_80028, SWS\_Rte\_06083, SWS\_Rte\_CONSTR\_80029, SWS\_Rte\_06084, SWS\_Rte\_CONSTR\_80026, SWS\_Rte\_06085, SWS\_Rte\_CONSTR\_80027, SWS\_Rte\_80039, SWS\_Rte\_80037, SWS\_Rte\_80038, SWS\_Rte\_06068, SWS\_Rte\_06069, SWS\_Rte\_06075, SWS\_Rte\_06076, SWS\_Rte\_06077, SWS\_Rte\_80030, SWS\_Rte\_80035, SWS\_Rte\_80036, SWS\_Rte\_06073, SWS\_Rte\_80033, SWS\_Rte\_06074, SWS\_Rte\_80034, SWS\_Rte\_06057, SWS\_Rte\_06058, SWS\_Rte\_06059, SWS\_Rte\_70001, SWS\_Rte\_70002, SWS\_Rte\_70003, SWS\_Rte\_70004, SWS\_Rte\_70000, SWS\_Rte\_70009, SWS\_Rte\_CONSTR\_80200, SWS\_Rte\_CONSTR\_80201, SWS\_Rte\_70005, SWS\_Rte\_70006, SWS\_Rte\_70007, SWS\_Rte\_70008, SWS\_Rte\_06064, SWS\_Rte\_06065, SWS\_Rte\_CONSTR\_03874, SWS\_Rte\_06066, SWS\_Rte\_CONSTR\_03873, SWS\_Rte\_CONSTR\_80206, SWS\_Rte\_06067, SWS\_Rte\_80041, SWS\_Rte\_06060, SWS\_Rte\_CONSTR\_80204, SWS\_Rte\_06061, SWS\_Rte\_CONSTR\_03870, SWS\_Rte\_CONSTR\_80205, SWS\_Rte\_CONSTR\_80202, SWS\_Rte\_80045, SWS\_Rte\_CONSTR\_80203, SWS\_Rte\_06046, SWS\_Rte\_06047, SWS\_Rte\_06048, SWS\_Rte\_06049, SWS\_Rte\_06053, SWS\_Rte\_06054, SWS\_Rte\_06055, SWS\_Rte\_06056, SWS\_Rte\_06050, SWS\_Rte\_06051, SWS\_Rte\_06052, SWS\_Rte\_06039, SWS\_Rte\_80029, SWS\_Rte\_06035, SWS\_Rte\_06036, SWS\_Rte\_06037, SWS\_Rte\_06038, SWS\_Rte\_06042, SWS\_Rte\_06043, SWS\_Rte\_06044, SWS\_Rte\_06045, SWS\_Rte\_06040, SWS\_Rte\_06041, ECUC\_Rte\_09090, SWS\_Rte\_04528, SWS\_Rte\_04529, ECUC\_Rte\_09099, ECUC\_Rte\_09097, ECUC\_Rte\_09098, ECUC\_Rte\_09095, ECUC\_Rte\_09096, ECUC\_Rte\_09093, ECUC\_Rte\_09091, ECUC\_Rte\_09092, SWS\_Rte\_02320, SWS\_Rte\_03891, SWS\_Rte\_03890, SWS\_Rte\_02321, SWS\_Rte\_02322, ECUC\_Rte\_09079, ECUC\_Rte\_09077, ECUC\_Rte\_09078, ECUC\_Rte\_09075, ECUC\_Rte\_09076, ECUC\_Rte\_09073, ECUC\_Rte\_09074, ECUC\_Rte\_09071, ECUC\_Rte\_09072, ECUC\_Rte\_09070, SWS\_Rte\_03882, SWS\_Rte\_03881, SWS\_Rte\_03880, SWS\_Rte\_03886, SWS\_Rte\_03885, SWS\_Rte\_02310, SWS\_Rte\_03884, SWS\_Rte\_02311, SWS\_Rte\_03883, SWS\_Rte\_02316, SWS\_Rte\_02317, SWS\_Rte\_03889, SWS\_Rte\_03888, SWS\_Rte\_03887, SWS\_Rte\_02318, SWS\_Rte\_02319, ECUC\_Rte\_09088, ECUC\_Rte\_09089, ECUC\_Rte\_09086, ECUC\_Rte\_09087, ECUC\_Rte\_09084, ECUC\_Rte\_09085, ECUC\_Rte\_09082, ECUC\_Rte\_09083, ECUC\_Rte\_09080, ECUC\_Rte\_09081, SWS\_Rte\_03871, SWS\_Rte\_03875, SWS\_Rte\_03632, SWS\_Rte\_03631, SWS\_Rte\_03630, SWS\_Rte\_03879, SWS\_Rte\_03878, SWS\_Rte\_03877, SWS\_Rte\_03876, SWS\_Rte\_02309, SWS\_Rte\_02307, SWS\_Rte\_02308, SWS\_Rte\_89003, SWS\_Rte\_08091, SWS\_Rte\_89004, ECUC\_Rte\_09059, SWS\_Rte\_89001, SWS\_Rte\_89002, ECUC\_Rte\_09057, ECUC\_Rte\_09058, SWS\_Rte\_89000, ECUC\_Rte\_09055, ECUC\_Rte\_09056, ECUC\_Rte\_09053, ECUC\_Rte\_09054, ECUC\_Rte\_09051, SWS\_Rte\_89009, ECUC\_Rte\_09052, SWS\_Rte\_89007, ECUC\_Rte\_09050, SWS\_Rte\_89008, SWS\_Rte\_89005, SWS\_Rte\_89006, SWS\_Rte\_03864, SWS\_Rte\_03622, SWS\_Rte\_03863, SWS\_Rte\_03621, SWS\_Rte\_03626, SWS\_Rte\_03867, SWS\_Rte\_03625, SWS\_Rte\_03866, SWS\_Rte\_03624, SWS\_Rte\_03865, SWS\_Rte\_03623, SWS\_Rte\_03629, SWS\_Rte\_03628, SWS\_Rte\_03869, SWS\_Rte\_03627, SWS\_Rte\_89014, SWS\_Rte\_89015, SWS\_Rte\_89012, SWS\_Rte\_89013, ECUC\_Rte\_09068, SWS\_Rte\_89010, ECUC\_Rte\_09069, SWS\_Rte\_89011, ECUC\_Rte\_09066, ECUC\_Rte\_09067, ECUC\_Rte\_09064, ECUC\_Rte\_09065, ECUC\_Rte\_09062, ECUC\_Rte\_09063, ECUC\_Rte\_09060, SWS\_Rte\_89018, ECUC\_Rte\_09061, SWS\_Rte\_89019, SWS\_Rte\_89016, SWS\_Rte\_89017, SWS\_Rte\_03610, SWS\_Rte\_02523, SWS\_Rte\_03619, SWS\_Rte\_08070, ECUC\_

Rte\_09039, ECUC\_Rte\_09037, ECUC\_Rte\_09038, ECUC\_Rte\_09035, ECUC\_Rte\_09036, ECUC\_Rte\_09033, ECUC\_Rte\_09034, ECUC\_Rte\_09031, SWS\_Rte\_08077, ECUC\_Rte\_09032, SWS\_Rte\_08079, ECUC\_Rte\_09030, SWS\_Rte\_CONSTR\_09101, SWS\_Rte\_08073, SWS\_Rte\_CONSTR\_09100, SWS\_Rte\_08076, SWS\_Rte\_CONSTR\_09102, SWS\_Rte\_08061, ECUC\_Rte\_09048, ECUC\_Rte\_09049, SWS\_Rte\_03609, ECUC\_Rte\_09046, ECUC\_Rte\_09047, ECUC\_Rte\_09044, ECUC\_Rte\_09045, SWS\_Rte\_08067, ECUC\_Rte\_09042, SWS\_Rte\_08066, ECUC\_Rte\_09043, SWS\_Rte\_08069, ECUC\_Rte\_09040, SWS\_Rte\_08068, ECUC\_Rte\_09041, SWS\_Rte\_08065

## 2.5. Limitations

This chapter lists the limitations of the module. Refer to the module references chapter *Integration notes*, subsection *Integration requirements* for requirements on integrating this module.

- ▶ The value RTE\_E\_COM\_STOPPED is not returned for inter-partition/inter-ECU communication

Description:

If the Com module is not able to successfully send a ComSignal or ComSignalGroup and the calling software component is located in another partition as the Com module, an Rte Api function will not return the value RTE\_E\_COM\_STOPPED to the calling software component.

Rationale:

When the Com module is located in a different partition as the SWC, the ComSignal must be sent asynchronously via the configured BswTask. The Rte can therefore not access the result of the Com function execution before returning control to the software component.

- ▶ The Rte expects an IOC configuration and IOC API which is not fully compliant to AUTOSAR 4.0

Description:

The Rte expects an EB-specific IOC configuration. Moreover, the Rte expects the IOC to provide an `IoC_ReInit` function. Also the Rte expects that the IOC API is reentrant.

Rationale:

The AUTOSAR 4.0 specification of the IOC still has some gaps. The EB-specific solution allows more optimizations.

- ▶ When converting an AUTOSAR 3.1 system description to AUTOSAR 4.0, not all initial value types for parameters are supported

Description:

When converting an AUTOSAR 3.1 system description to AUTOSAR 4.0, initial values for parameters, that represent a nested record or array type, are not supported. Only primitive types, simple record, and array types are supported.

Rationale:

The converter cannot perform a deep copy on the value specification of the parameter's initial value. Thus, it is not supported that the initial value is a nested record or array, etc.

- ▶ When converting an AUTOSAR 3.1 system description to AUTOSAR 4.0, an opaque type will lose the byte size information

Description:

When converting an AUTOSAR 3.1 system description to AUTOSAR 4.0, an opaque type from AUTOSAR 3.1 will lose its byte size information.

Rationale:

The opaque type is converted to a primitive type. Thus the byte size information gets lost.

- ▶ If inter-partition mode management is used, it cannot be guaranteed that the initialization of the inter-partition mode users' partition is complete once its initialization routine returns

Description:

Because partitions execute independently, it may be possible that the initialization routine of the mode users' partition returns before that of the mode provider's partition. In this situation, the `OnEntry` runnables of the inter-partition mode users will not have been executed after the initialization of the partition has been completed. Also, the mode disabling dependencies of the initial mode would not be enabled.

Reliance on inter-partition modes for initialization should be avoided. If inter-partition modes are to be used for initialization, a check of the mode provider's partition state should be made before accessing any resources which require initialization.

Rationale:

Partitions execute, terminate, and start independently.

- ▶ Rte Enhanced Mode Api for the mode manager in case of inter-partition mode management

Description:

Rte Mode Api for the enhanced mode provider inter-partition mode management, will return the current mode for the previous and next mode in case all mode users have the same current mode.

- ▶ The data filter types `NEWISEQUAL`, `NEWISDIFFERENT`, `MASKEDNEWEQUALSMASKEDOLD`, `NEWISGREATER`, `NEWISLESSOREQUAL`, `NEWISLESS`, and `NEWISGREATEROREQUAL` for a require port typed by sender receiver interfaces are not supported

Description:

The EB tresos Studio supports AUTOSAR 4.0.3 where the mentioned data filter types are not supported any longer.

Rationale:

In AUTOSAR 4.0.3, the mentioned data filter types have been removed.

- ▶ Partial record support (signal degradation) is not available for inter-partition communication

Description:

Sender/receiver communication with the IOC or Shared memory communicator (Smc) will not function if the destination data element is a partial record.

- ▶ Segmentation fault checks are not supported

Description:

The segmentation fault check performed by the Rte in case of an inter-partition communication is not supported.

- ▶ Minimal offset of allocated Os schedule table expiry points is one microsecond

Description:

When allocating an Os schedule table, the offset of an expiry point must be divisible by one microsecond.

Rationale:

The expiry point names consist of the prefix `EP_` and the offset value in microseconds.

- ▶ Implementation data types of category function reference are not supported

Description:

Implementation data types or implementation data type elements of category `FUNCTION_REFERENCE` are not supported.

- ▶ Limited calibration support on S12X with Metroworks compiler

Description:

When a mixed memory model is used on the S12X, the following applies:

- ▶ `RTE_VAR` is mapped to far and `RTE_CONST` is mapped to near.
- ▶ `RTE_VAR` is mapped to near and `RTE_CONST` is mapped to far.

The following limitations apply when calibration is used:

1. `Rte_CData` and `Rte_CalPrm` API do not work with record and array types.

2. Online calibration using single pointered or double pointered methods is not possible.

Rationale:

1. For complex types, the APIs return pointers. This requires nested compiler abstraction. The Metroworks compiler cannot handle this correctly. Although nested compiler abstraction is forbidden by the AUTOSAR Compiler Abstraction specification (COMPILER058), nested compiler abstraction must be used because the RTE SWS specifies that the APIs shall return pointers. Here, the RTE SWS has to be changed. See Bugzilla #42952.
2. The pointers in the calibration reference table point to constants in ROM. For double pointered method, the calibration base pointer points to a calibration reference table in ROM. When `RTE_CONST` is mapped to near, the pointers cannot be redirected to variables located in RAM which would have to be addressed far in this case.

- The activation offset of a timing event cannot be greater or equal to the period

Description:

The Rte does not support timing events with an activation offset greater or equal to the period. If you configure an activation offset greater or equal to the period anyway, then the offset is automatically shortened to be within the period by a modulo division (e.g. period is 100ms, original activation offset is 150ms and the resulting offset will be 50ms).

- If pointer to array type is used, linker type checking cannot be enabled

Description:

The function signature of API functions that are implemented by the Rte always uses pointer to array base type for array arguments. If pointer to array type is used, the array type in the function declaration of the software component header does not match with the array type of the Rte implementation. On object code level, both variants are equivalent. However a linker where type checking is enabled, may report an error.

- Limited support for NvBlockSwComponentType data consistency

Description:

The NvBlockSwComponentType currently only supports a simple interrupt blocking mechanism to assure data consistency. Other mechanisms, e.g. "Usage of OS resources", are not supported.

- Calibration access to NvBlockDescriptor romBlock is not supported

Description:

The NvBlockSwComponentType does not support calibration access for the NvBlockDescriptor romBlock.

- Limited support for NvBlockSwComponentType NvBlockDescriptor parameters

Description:



The `NvBlockSwComponentType` does not support the `NvBlockDescriptor` parameters, "`constantValueMapping`", "`dataTypeMapping`" and "`instantiationDataDefProps`".

- ▶ Limited support for `PortInterfaceMapping`

Description:

`NvDataInterfaces` currently support only Bitfield Texttable `PortInterfaceMapping`.

- ▶ Limited support for `SubElementMapping` of `VariableAndParameterInterfaceMapping` without transformers

Description:

Currently the following limitations apply when using `SubElementMappings` without transformers:

Only mapping a composite element to primitive `DataPrototype` is supported

Only the mapping of struct elements is supported (no array elements)

Data conversion is not supported

Data invalidation is not supported

Mapping of variable data prototypes to parameter data prototypes is not supported

- ▶ Limited support for `SubElementMapping` of `VariableAndParameterInterfaceMapping` with transformers

Description:

Currently the following limitations apply when using `SubElementMappings` with transformers:

If optional structure elements are used, only `SubElementMappings` from one `ImplementationDataType` to another `ImplementationDataType` are supported

Mapping of variable data prototypes to parameter data prototypes is not supported

- ▶ Limited support for inter-ECU trigger communication

Description:

Currently the following limitations apply when using inter-ECU trigger communication:

Only supported with data transformation

Inter-partition communication is not supported (the mapped signal must be on the same partition as the SWC)

- ▶ The blocking `Rte_Receive` API is not supported for inter-partition communication

Description:



The Rte generator logs an error if all of the following conditions apply:

1. Sender and receiver are located in different partitions.
  2. A blocking `Rte_Receive` API is configured.
- ▶ The `Com_InvalidateSignalGroup` API is not supported

Description:

The Rte does not use the `Com_InvalidateSignalGroup` API to invalidate a signal group. Instead, it always sends the signal group with the invalid value.

- ▶ The `Com_InvalidateSignal` API is not used in case of inter-partition inter-ECU signal invalidation

Description:

Regardless of the value of parameter `InterECUInvalidationHandledByRte` the Rte always sends the invalid value in case of inter-partition inter-ECU sender/receiver communication instead of using the `Com_InvalidateSignal` API.

- ▶ The operation invoked event of the server which is called by the `Rte_NvMNotifyJobFinished` callback shall not be mapped to a task

Description:

The `Rte_NvMNotifyJobFinished` callback supports only direct calls. Please note that the behavior of the Rte is undefined if this runnable calls any Rte API, because it's running in an interrupt context (see also SWS\_Rte\_03600).

- ▶ Mode disabling dependencies for direct call dirtyFlag runnables of a `NvBlockSwComponent` are not supported.

Description:

Mode disabling dependencies for dirtyFlag runnables of a `NvBlockSwComponent` are only supported if the `DataReceivedEvent` is mapped to a task.

- ▶ Data consistency is not guaranteed for sender-receiver invalidation external replacement

Description:

If a sender-receiver data element has been received invalidated and the attribute `handleInvalid` is set to `externalReplacement` the Rte doesn't guarantee data consistency for the external replacement value.

- ▶ Sender-receiver invalidation external replacement with `ParameterDataPrototype` not fully supported

Description:

Data element invalidation with the attribute `handleInvalid` is set to `externalReplacement` and `replaceWith` references a `ParameterDataPrototype` is not fully supported in case of inter-partition sender-receiver communication.

- ▶ Debounced activation of executables is not supported for certain events

Description:

The `minimumStartInterval` is ignored for executables started by `ModeSwitchEvents`, `ModeSwitchedAckEvents`, `OperationInvokedEvents` or only `TimingEvents`.

- ▶ Event order cannot be guaranteed for debounced activation of executables

Description:

The Rte cannot guarantee that a delayed event (i.e. the first event occurrence within the `minimumStartInterval` period) will finally lead to the start of the executable if another event for the same executable occurred in close succession to the expiration of the debounced alarm of the delayed event.

Rationale:

It depends on the internal scheduling of the Os which task of the executable will enter running state first when multiple events are set at once.

- ▶ Distribution of the Com onto multiple partitions

Description:

The following parameters cannot be configured separately for each Com partition:

- ▶ `SendSignalQueueStrategy`
- ▶ `BswOsTaskPeriod`

For client/server communication, the call and result signal must be mapped onto the same partition.

For client/server communication, an operation fan-in/out (multiple call/result signals mapped to the same operation) is not supported.

- ▶ Transport protocol for serialization

Description:

The non-AUTOSAR API `LdCom_TpTransmit` is used by the Rte.

- ▶ `XfrmBufferLengthType` `UINT32` not supported for inter-partition communication via IOC

Description:

For inter-partition communication, IOC does not support an `XfrmBufferLengthType` value other than `UINT16`.

Rationale:

IOC does not provide an API for UINT32 variable length data.

- ▶ The following Signal fan-in/out scenario is not supported for inter-ECU sender/receiver communication

Description:

ISignal(1) to (Ld)Com (n)

- ▶ The following Signal fan-in/out scenarios are not supported for inter-ECU client/server communication

Description:

ISignal(1) to (Ld)Com (n)

SystemSignal(1) to ISignal (n)

Operation(n) to SystemSignal (1)

Server port(n) to SystemSignal (1)

- ▶ Asynchronous client/server communication with two clients mapped to Os Interrupt and OsTask is not supported

Description:

Asynchronous client/server communication with two clients where one client is mapped to an Os Interrupt and the other to an OsTask is not supported.

- ▶ Limited support for writing data to different NvRamBlocks

Description:

Writing data to different NvRamBlocks using the same RPortPrototype of a NvBlockSwComponentType is not supported.

- ▶ Limited support for Coherency Groups

Description:

Currently the following limitations apply when using Coherency Groups:

DirectReadFromCom is not supported

NvBlockSwComponentTypes are not supported

Grouping of accesses across execution conditions (e.g. different data received events or mode disabling dependencies) is not supported

For read/write accesses not in a Coherency Group, which share the same receive buffer with accesses in a Coherency Group, the same locking mechanisms are applied

Grouping of the status members for senders with transmission acknowledgement enabled is not supported

- ▶ Limited support for mapping events to ISRs

Description:

Currently the following limitations apply when mapping events to ISRs:

Only (BSW)TimingEvent and (BSW)ExternalTriggerOccurredEvent are supported.

It is allowed to map either timing or external trigger occurred events to an ISR but not the both event types.

- ▶ Limited support for mode switched acknowledgment event

Description:

Currently the following limitations apply when using mode switched acknowledgment event:

one mode switch may lead to more than one mode switched acknowledgment event in case of mode communication with multiple mode user partitions on several cores.

- ▶ Limited support for Det error reporting when multiple partitions, and Bsw Modules multiple instances with shared exclusive area are used

Description:

Currently the following limitations apply when multiple partitions are used. In this case the Det error reporting is not supported.

- ▶ Limited support for rule based value specification

Description:

Currently the following limitations apply when using RuleBasedValueSpecifications:

Compound Primitive Data Types are not supported.

RULE-ARGUMENTS of type VTF are not supported.

- ▶ Limited support for variable size array inter-ECU communication without transformers

Description:

Currently the following limitations apply when variable size array inter-ECU communication without transformers is used:

Data invalidation is not supported.

Multiple variable size arrays with different data types can not be received by the same TP LdCom PDU.

- ▶ Limited support for RIPS with parameter interface

Description:

Currently the following limitations apply when using RIPS with parameter interfaces:

Rte Online Calibration must be deactivated (RteCalibrationSupport = NONE)

RteRipsGlobalCopyInstantiationPolicy must be set to RTE\_RIPS\_INSTANTIATION\_BY\_PLUGIN

RTE\_PTR2ARRAYTYPE\_PASSING is not supported for Rte\_Prm API

Only 1:n communication is supported

- ▶ Limited support for synchronous mode switching procedure over multi partitions/cores

Description:

Currently the following limitations apply when using synchronous mode switching procedure over multi partitions/cores:

Partition restarts are not considered. That means if a partition is not yet started or stopped then a mode switch during this time could lead to infinite waiting in transition of the active partitions. So we assume that all mode user partitions are either active or about to start so that the application will not get stuck in a mode transition.

- ▶ Limited support for mapping of variable data prototypes to parameter data prototypes

Description:

Currently the following limitations apply when mapping of variable data prototypes to parameter data prototypes:

PR port is not supported

Inter ECU is not supported

Only 1:n communication is supported

Transformers, SubElementMapping and DataConversion are not supported

Timeout monitoring, handleNeverReceived, enable update, handleOutOfRange, and invalidation are not applicable for this case

DataReceivedEvents/DataReceiveErrorEvents are not supported

RIPS is not supported

- ▶ Limited support for the initialization of the Rte implicit write buffers according to SWS\_Rte\_08418

Description:

Currently the following limitations apply when the Rte implicit write buffers are initialized according to SWC\_Rte\_08418:

For a pure Inter-ECU communication, the Rte implicit write buffers might be uninitialized before the first invocation of the runnable with Rte\_IWrite API.

- ▶ Limited support for transformer code forwarding according to SWS\_Rte\_04572

Description:

Currently the following limitations apply when transformer code forwarding is used:

Only the Rte\_Send and Rte\_Write APIs are supported.

## 2.6. Open-source software

The software that is delivered with EB tresos AutoCore Generic can be classified into the following two categories:

- ▶ Software that is executed on the electronic control unit (ECU).
- ▶ Software that is used for the development infrastructure (configuration, generation, building) and thus executed on the development platform.

### 2.6.1. Open-source software in software executed on the ECU

No open-source software that runs on the ECU is delivered with Rte.

### 2.6.2. Open-source software in software used for the development infrastructure

The following list of open-source software that is used in development is delivered with Rte:

- ▶ Commons Math: The Apache Commons Mathematics Library  
3.2.  
<https://commons.apache.org/proper/commons-math/>



List of licenses:

- ▶ Apache License Version 2.0  
commons-math.txt

List of copyrights:

- ▶ Copyright (C) The Apache Software Foundation.