# User Manual

# for S32K1 CRC Driver

Document Number: UM2CRCASR4.4 Rev0000R1.0.1 Rev. 1.0

1	Revision History	2
2	Introduction	3
	2.1 Supported Derivatives	3
	2.2 Overview	4
	2.3 About This Manual	5
	2.4 Acronyms and Definitions	6
	2.5 Reference List	6
3	Driver	7
	3.1 Requirements	7
	3.2 Driver Design Summary	7
	3.3 Hardware Resources	8
	3.4 Deviations from Requirements	8
	3.5 Driver Limitations	8
	3.6 Driver usage and configuration tips	8
	3.6.1 CRC Calculation Type	9
	3.6.2 CRC Initialization	10
	3.6.3 CRC Channel Configuration	10
	3.6.4 CRC Channel Calculation	10
	3.6.5 CRC Channel Get Result	10
	3.6.6 CRC Examples for using the driver	10
	3.7 Runtime errors	18
	3.8 Symbolic Names Disclaimer	19
4	Tresos Configuration Plug-in	20
	4.1 Module Crc	21
	4.2 Container CrcGeneral	21
	4.3 Parameter CrcDetectError	23
	4.4 Parameter CrcEnableUserModeSupport	23
	4.5 Parameter CrcDmaSupportEnable	24
	4.6 Parameter CrcMultiCoreEnable	24
	4.7 Parameter CrcVersionInfoApi	25
	4.8 Parameter Crc8Mode	25
	4.9 Parameter Crc8H2FMode	26
	4.10 Parameter Crc16Mode	26
	4.11 Parameter Crc16ARCMode	27
	4.12 Parameter Crc32Mode	27
	4.13 Parameter Crc32P4Mode	28
	4.14 Parameter Crc64Mode	28
	4.15 Container CrcChannelConfig	29

4.16 Parameter CrcLogicChannelName	29
4.17 Parameter CrcAutosarSelect	30
4.18 Parameter CrcCalculationType	30
4.19 Reference CrcPartitionRefOfChannel	31
4.20 Container CrcHardwareConfig	31
4.21 Parameter CrcHwInstance	32
4.22 Parameter CrcHwChannel	32
4.23 Parameter CrcDmaChannelEnable	33
4.24 Reference CrcDmaLogicChannelName	33
4.25 Container CrcProtocolInfo	34
4.26 Parameter CrcProtocolType	34
4.27 Parameter CrcPolynomialValue	35
4.28 Parameter CrcWriteBitSwap	35
4.29 Parameter CrcWriteByteSwap	36
4.30 Parameter CrcReadBitSwap	36
4.31 Parameter CrcReadByteSwap	37
4.32 Parameter CrcInversionEnable	37
4.33 Container CrcEcucPartitionRefArray	37
4.34 Reference CrcEcucPartitionRef	38
4.35 Container CommonPublishedInformation	38
4.36 Parameter ArReleaseMajorVersion	39
4.37 Parameter ArReleaseMinorVersion	39
4.38 Parameter ArReleaseRevisionVersion	40
4.39 Parameter ModuleId	40
4.40 Parameter SwMajorVersion	41
4.41 Parameter SwMinorVersion	41
4.42 Parameter SwPatchVersion	42
4.43 Parameter VendorApiInfix	42
4.44 Parameter VendorId	43
N. J1. T J	4.4
Module Index	44
5.1 Software Specification	44
Module Documentation	45
6.1 CRC HLD Driver	45
6.1.1 Detailed Description	45
6.1.2 Data Structure Documentation	46
6.1.3 Macro Definition Documentation	47
6.1.4 Types Reference	50
6.1.5 Function Reference	50
6.2 CRC IPL Driver	53

**5** 

6

	6.2.1 Detailed Description	53
	6.2.2 Data Structure Documentation	53
	6.2.3 Macro Definition Documentation	55
	6.2.4 Enum Reference	55
	6.2.5 Function Reference	56
6.3	CRC IPW Driver	59
	6.3.1 Detailed Description	59
	6.3.2 Macro Definition Documentation	59

# Chapter 1

# **Revision History**

Revision	Date	Author	Description
1.0	24.02.2022	NXP RTD Team	Prepared for release RTD S32K1 Version 1.0.1

# **Chapter 2**

# Introduction

- Supported Derivatives
- Overview
- About This Manual
- Acronyms and Definitions
- Reference List

This User Manual describes NXP Semiconductor AUTOSAR CRC for S32K1XX. AUTOSAR CRC driver configuration parameters and deviations from the specification are described in Driver chapter of this document. AUTOSAR CRC driver requirements and APIs are described in the AUTOSAR CRC driver software specification document.

# 2.1 Supported Derivatives

The software described in this document is intended to be used with the following microcontroller devices of NXP Semiconductors:

- s32k116\_qfn32
- s32k116\_lqfp48
- $s32k118\_lqfp48$
- s32k118\_lqfp64
- $s32k142\_lqfp48$
- s32k142\_lqfp64
- s32k142\_lqfp100
- $s32k142w_lqfp48$
- s32k142w\_lqfp64
- s32k144 lqfp48

#### Introduction

- s32k144\_lqfp64
- s32k144\_lqfp100
- s32k144\_mapbga100
- s32k144w lqfp48
- s32k144w\_lqfp64
- s32k146\_lqfp64
- $s32k146_lqfp100$
- s32k146\_mapbga100
- $s32k146\_lqfp144$
- s32k148\_lqfp100
- s32k148\_mapbga100
- $s32k148_lqfp144$
- s32k148\_lqfp176

All of the above microcontroller devices are collectively named as S32K1.

#### 2.2 Overview

AUTOSAR (AUTomotive Open System ARchitecture) is an industry partnership working to establish standards for software interfaces and software modules for automobile electronic control systems.

#### AUTOSAR:

- paves the way for innovative electronic systems that further improve performance, safety and environmental friendliness.
- is a strong global partnership that creates one common standard: "Cooperate on standards, compete on implementation".
- is a key enabling technology to manage the growing electrics/electronics complexity. It aims to be prepared for the upcoming technologies and to improve cost-efficiency without making any compromise with respect to quality.
- facilitates the exchange and update of software and hardware over the service life of the vehicle.

#### **About This Manual** 2.3

This Technical Reference employs the following typographical conventions:

- Boldface style: Used for important terms, notes and warnings.
- Italic style: Used for code snippets in the text. Note that C language modifiers such "const" or "volatile" are sometimes omitted to improve readability of the presented code.

Notes and warnings are shown as below:

Note

This is a note.

Warning

This is a warning

# 2.4 Acronyms and Definitions

Term	Definition
API	Application Programming Interface
ASM Assembler	
BSMI	Basic Software Make file Interface
CAN	Controller Area Network
C/CPP	C and C++ Source Code
CS	Chip Select
CTU	Cross Trigger Unit
DEM	Diagnostic Event Manager
DET	Development Error Tracer
DMA	Direct Memory Access
ECU	Electronic Control Unit
FIFO	First In First Out
LSB	Least Signifigant Bit
MCU	Micro Controller Unit
MIDE	Multi Integrated Development Environment
MSB	Most Significant Bit
N/A	Not Applicable
RAM Random Access Memory	
SIU Systems Integration Unit	
SWS Software Specification	
VLE	Variable Length Encoding
XML	Extensible Markup Language

# 2.5 Reference List

#	Title	Version
1	Specification of CRC Driver	AUTOSAR Release 4.←
		4.0
2	S32K1XX Series Reference Manual	Rev. 14, 09/2021
3	S32K1xx Data Sheet	Rev. 14, 08/2021
4	Errata S32K116_0N96V	Rev. 22/OCT/2021
5	Errata S32K118_0N97V	Rev. 22/OCT/2021
6	Errata S32K142_0N33V	Rev. 22/OCT/2021
7	Errata S32K144_0N57U	Rev. 22/OCT/2021
8	Errata S32K144W_0P64A	Rev. 22/OCT/2021
9	Errata S32K146_0N73V	Rev. 22/OCT/2021
10	Errata S32K148_0N20V	Rev. 22/OCT/2021

# **Chapter 3**

#### **Driver**

- Requirements
- Driver Design Summary
- Hardware Resources
- Deviations from Requirements
- Driver Limitations
- Driver usage and configuration tips
- Runtime errors
- Symbolic Names Disclaimer

# 3.1 Requirements

Requirements for this driver are detailed in the Autosar Driver Software Specification document (See Table Reference List ).

For CDD: CRC is a both Complex Device Driver (CDD) and support AUTOSAR requirements regarding this module.

It has vendor-specific requirements and implementation.

# 3.2 Driver Design Summary

- The CRC driver is implemented as an complex device driver. It uses the CRC hardware peripheral which provides support for implementing the CRC calculations.
- The driver offers: software Calculation, Lookup Table Calculation and hardware Calculation API to the upper layer that can be used to configure the CRC and initiate CRC calculations.
- Hardware and software settings can be configured using an Autosar standard configuration tool. The information required for a CRC data calculation will be configured in a data structure that will be sent as parameter to the API of the driver.

Driver

# 3.3 Hardware Resources

The CRC Driver consists of:

1. CRC IP

# 3.4 Deviations from Requirements

The driver deviates from the AUTOSAR CRC Driver software specification in some places. The table identifies the AUTOSAR requirements that are not fully implemented, implemented differently, not available, not testable or out of scope for the CRC Driver.

Term	Definition	
N/S	Out of scope	
N/I	Not implemented	
N/F	Not fully implemented	

Below table identifies the AUTOSAR requirements that are not fully implemented, implemented differently, not available, not testable or out of scope for the driver.

Requirement	Status	Description	Notes
SWS_Crc_00065	N/S	If the CRC calculation within the function Crc_CalculateCRC64 is performed by hardware, then the CRC module's imple-menter shall ensure reentrancy of this function by implementing a (software based) locking mechanism	There are no platforms supporting Crc64 Hardware calculation (Hardware limitation).

## 3.5 Driver Limitations

The CRC Driver has the following limitations:

- Post Build Compiler not implemented.
- For each CRC type, the user can configure only one of the three methods: Hardware, Look-up-Table, Runtime( $\leftarrow$  Software).
- Crc64Mode hardware not supported.

# 3.6 Driver usage and configuration tips

This driver is an Complex Device Driver. Complete driver functionality together with API description can be found below.

#### 3.6.1 CRC Calculation Type

- When Calculation Type is CRC\_IP\_LOOKUP\_TABLES\_CALCULATION or CRC\_IP\_HARDWARE\_← CALCULATION, the Protocol shall be selected from the supported ones.
- Autosar Library supports only the Autosar Protocols.
- Polynomial configuration is enabled when the following Protocols are selected: CRC\_PROTOCOL\_8BIT, CRC\_PROTOCOL\_16BIT, CRC\_PROTOCOL\_32BIT, CRC\_PROTOCOL\_64BIT.
- Note: Each Calculation Type supports a specific list of Protocols. If out of range there will be an error message.

#### 3.6.1.1 Calculation Type supported by CRC\_IP\_LOOKUP\_TABLES\_CALCULATION

- CRC PROTOCOL 8BIT SAE J1850
- CRC\_PROTOCOL\_8BIT\_H2F
- CRC PROTOCOL 16BIT CCITT
- CRC\_PROTOCOL\_16BIT\_ARC
- CRC\_PROTOCOL\_32BIT\_ETHERNET
- CRC\_PROTOCOL\_32BIT\_E2EP4
- CRC\_PROTOCOL\_64BIT\_ECMA

#### 3.6.1.2 Calculation Type supported by CRC\_IP\_SOFTWARE\_CALCULATION

- CRC PROTOCOL 8BIT CUSTOM
- CRC\_PROTOCOL\_16BIT\_CUSTOM
- CRC PROTOCOL 32BIT CUSTOM
- CRC\_PROTOCOL\_8BIT\_SAE\_J1850
- CRC\_PROTOCOL\_8BIT\_H2F
- CRC\_PROTOCOL\_16BIT\_CCITT
- CRC\_PROTOCOL\_16BIT\_ARC
- CRC PROTOCOL 32BIT ETHERNET
- CRC\_PROTOCOL\_32BIT\_E2EP4
- CRC\_PROTOCOL\_64BIT\_ECMA

#### 3.6.1.3 Calculation Type supported by CRC\_IP\_HARDWARE\_CALCULATION

- CRC\_PROTOCOL\_16BIT\_CUSTOM
- CRC PROTOCOL 32BIT CUSTOM
- CRC\_PROTOCOL\_16BIT\_CCITT
- CRC\_PROTOCOL\_32BIT\_ETHERNET

#### Driver

#### 3.6.2 CRC Initialization.

• The Crc\_Init() function shall initialize the CRC hardware peripheral(s) and the internal driver context, according to the input configuration data. The application shall ensure that the Crc\_Init() function is called first. Only the Crc\_GetVersionInfo() can be called before Crc\_Init().

#### 3.6.3 CRC Channel Configuration.

• The function receives pointer to a configuration structure that shall be loaded into the Logic Channel. According to input parameters this function configures channel's CRC width, polynomial and whether Swap and/or Inversion functionality is applied on CRC input data and/or signature or not.

#### 3.6.4 CRC Channel Calculation.

• The CRC driver provides the function of calculating the CRC of a data stream of a certain length. The CRC calculation uses the CRC width, polynomial, and previously set swap/inversion options for the channel using the Crc\_SetChannelConfig () function. The CRC is computed on an input vector data pointed by pCrcData and has a length set to CrcLength. The function synchronously calculates the CRC and returns the resulting CRC.

#### 3.6.5 CRC Channel Get Result.

• Driver provides functionality for reading CRC result from required channel. The Crc\_GetChannelResult() function reads CRC Results (CRC checksum).

#### 3.6.6 CRC Examples for using the driver

• This sub-chapter lists some examples of how to use the CRC Driver.

#### 3.6.6.1 Example Calculate Autosar CRC16 CCITT.

To compute CRC checksum according to CCITT-FALSE CRC16 standard, please follow the steps below:

1. Open EB Tresos then create New Configuration Project

2. In Tresos GUI, add CRC Configuration in tab "Module Configuration"

# Module Configurations

Edit the table below to change the set of module configurations for this project.

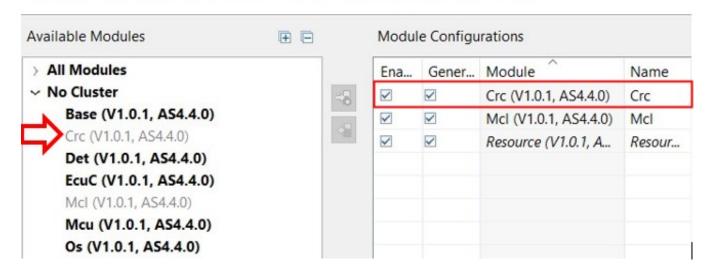


Figure 3.1 CRC add Configuration

3. In "CRC Channel Configuration", add new CRC logic channel with default config.

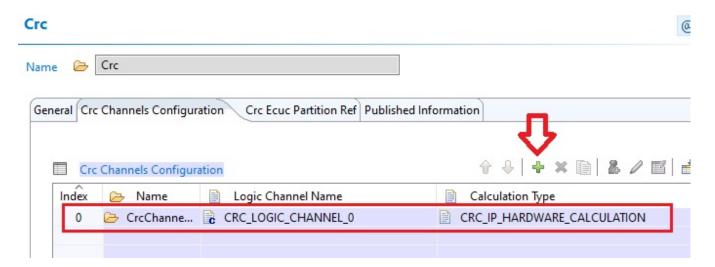


Figure 3.2 CRC add logic Channel with default config

- 4. In CRC logic channel configuration, using the following values for the attributes.
  - Select "Calculates Type" is CRC\_IP\_HARDWARE\_CALCULATION
  - Set checkbox "Autosar Library Enable" is Enable.

S32K1 CRC Driver

#### Driver

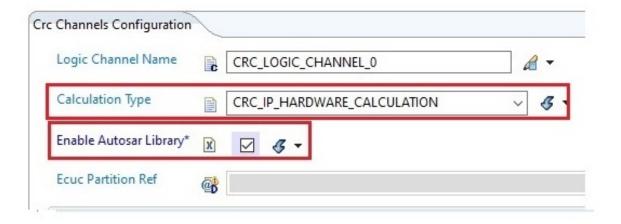


Figure 3.3 CRC config Calculates Type and Enable Autosar

• Set "Protocol Type" is CRC\_PROTOCOL\_16BIT\_CCITT\_FALSE.

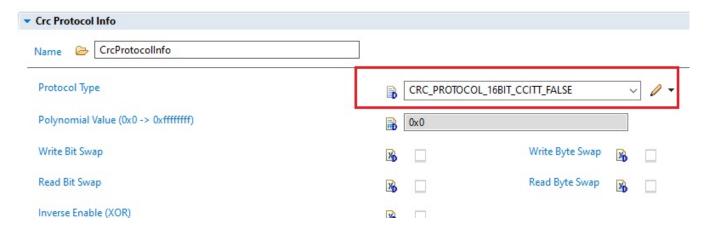


Figure 3.4 CRC config protocol is  $16BIT\_CCITT\_FALSE$ 

5. Generate the configuration files from Tresos without errors or warnings.

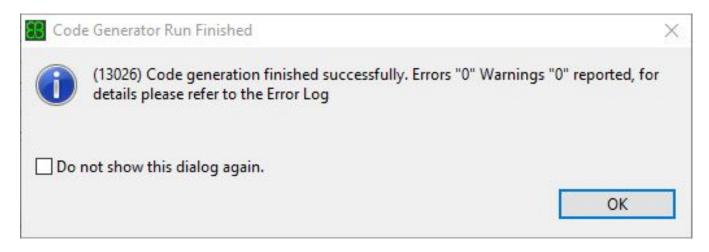


Figure 3.5 Status Generate Config

- 6. Write the application code, following the steps.
  - Call the function Crc\_Init() providing it as parameter a NULL pointer.
  - Call the function Crc Calculator CRC16 () to start calculation. This function will use the pre-configured Logic Channels in Auto Mode. After the calculation is completed, it will return the results

```
/* Initialize CRC driver */
Crc_Init(NULL_PTR);
/* Start calculate then get result */
result = Crc_CalculateCRC16 (const uint8_t *)CRC_data, CRC_DATA_SIZE, 0xFFFF, true);
```

Figure 3.6 CRC application code

#### 3.6.6.2 Example Calculate Autosar CRC32 ETHERNET.

To compute CRC checksum according to IEEE 802.3 Ethernet standard, please follow the steps below:

- 1. -> 3. Please refer "Example Calculate Autosar CRC16 CCITT"
- 2. In CRC logic channel configuration, using the following values for the attributes.
  - Select "Calculates Type" is CRC\_IP\_HARDWARE\_CALCULATION
  - Set checkbox "Autosar Library Enable" is Enable.

S32K1 CRC Driver

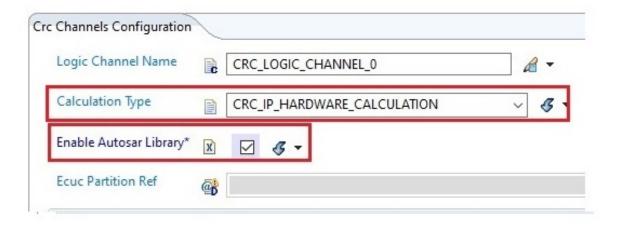


Figure 3.7 CRC config CRC config Calculates Type and Enable Autosar

• Set "Protocol Type" is CRC PROTOCOL 32BIT ETHERNET.

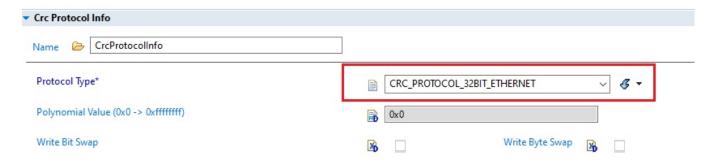


Figure 3.8 CRC config protocol is CRC\_PROTOCOL\_32BIT\_ETHERNET

- 3. Generate the configuration files from Tresos without errors or warnings.
- 4. Write the application code, following the steps.
  - Call the function Crc\_Init() providing it as parameter a NULL pointer.
  - Call the function Crc\_CalculateCRC32() to start calculation. This function will use the pre-configured Logic Channels in Auto Mode. After the calculation is completed, it will return the results

```
/* Initialize CRC driver */
Crc_Init(NULL_PTR);
/* Start calculate then get result */
result = Crc_CalculateCRC32 ((const uint8_t *)CRC_data, CRC_DATA_SIZE, 0xFFFF, true);
```

Figure 3.9 CRC application code

#### 3.6.6.3 Example Calculate CRC CUSTOM.

To compute any CRC CUSTOM Type is CRC\_PROTOCOL\_8BIT\_CUSTOM (or CRC\_PROTOCOL\_16BIT CUSTOM, CRC\_PROTOCOL\_32BIT\_CUSTOM), please follow the steps below:

- 1. -> 3. Please refer "Example Calculate Autosar CRC16 CCITT"
- 2. In CRC logic channel configuration, using the following values for the attributes.
  - Select "Calculates Type" is CRC\_IP\_SOFTWARE\_CALCULATION

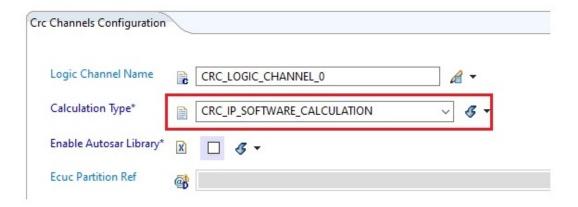


Figure 3.10 CRC Calculate Tye is CRC\_IP\_SOFTWARE\_CALCULATION

• Set "Protocol Type" is CRC\_PROTOCOL\_8BIT\_CUSTOM and config Polynomial Value, Swap Option and Inverse Enable

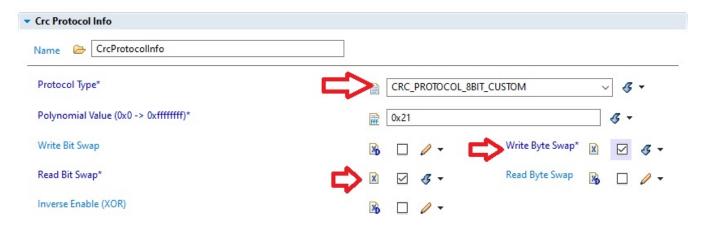


Figure 3.11 CRC config protocol CRC\_PROTOCOL\_8BIT\_CUSTOM

- 3. Generate the configuration files from Tresos without errors or warnings.
- 4. Write the application code, following the steps.

S32K1 CRC Driver

#### Driver

- Call the function Crc\_Init() providing it as parameter a NULL pointer.
- Call the function Crc\_SetChannelCalculate() to start calculation. Calculation is based on User's logic channel configuration, Crc data path(pCrcData), Crc length (CrcLength) and start value (CrcStartValue). After the calculation is complete, this function will return the result

```
/* Initialize CRC driver */
Crc_Init(NULL_PTR);
/* Start calculate then get result */
result = Crc_SetChannelCalculate((const uint8_t *)CRC_data, CRC_DATA_SIZE, 0xFFFF, true);
```

Figure 3.12 CRC application code

# 3.6.6.4 Example Calculate CRC with DMA support (only support CRC\_IP\_HARDWARE\_CALCULATION).

To compute CRC checksum use DMA support according to CCITT-FALSE CRC16 standard, please follow the steps below:

- 1. -> 3. Please refer "Example Calculate Autosar CRC16 CCITT"
- 2. In CRC logic channel configuration, using the following values for the attributes.
  - Note: Autosar Library Function does not support DMA
  - Set checkbox "DMA support Calculate" is Enable.



Figure 3.13 CRC DMA support Calculate Enable

• Select "Calculates Type" is CRC\_IP\_HARDWARE\_CALCULATION

• If checkbox "Autosar Library Enable" is selected and "DMA Channel Enable" is selected Tresos will show "ERROR"

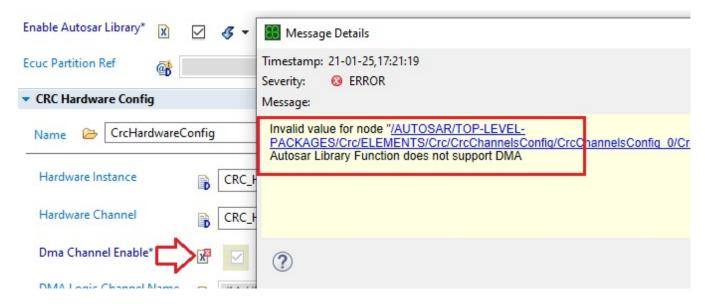


Figure 3.14 CRC config Enable "Autosar Library" and "DMA Channel"

• Set "Protocol Type" is CRC\_PROTOCOL\_16BIT\_CCITT\_FALSE.

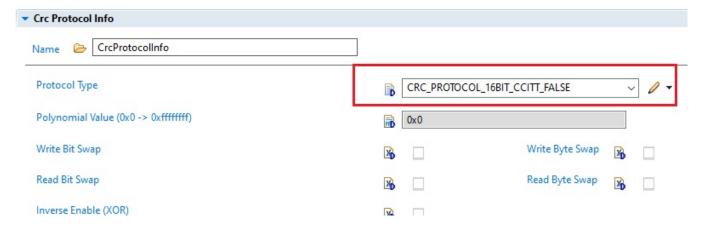


Figure 3.15 CRC config protocol is 16BIT\_CCITT\_FALSE

• Config hardware Channel CRC and DMA

#### Driver

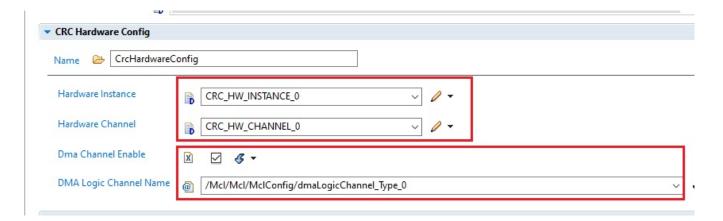


Figure 3.16 Config hardware Channel CRC and DMA

- 3. Generate the configuration files from Tresos without errors or warnings.
- 4. Write the application code, following the steps.
  - Call the function Dma Ip Init() to initialize the DMA driver.
  - Call the function Crc\_Init() providing it as parameter a NULL pointer.
  - Call the function Crc\_SetChannelCalculate() to start calculation. Calculation is based on User's logic channel configuration, Crc data path(pCrcData), Crc length (CrcLength) and start value (CrcStartValue). After the calculation is complete, this function will return the result

```
/* Initialize Mcl driver */
Mcl_Init(NULL_PTR);

/* Initialize CRC driver */
Crc_Init(NULL_PTR);

/* Start calculate with CRC_PROTOCOL_16BIT_CCITT_FALSE with SOFTWARE_CALCULATION */
CrcResult = Crc_SetChannelCalculate(CRC_LOGIC_CHANNEL_1, &CRC_data[0], CRC_DATA_SIZE, OU, TRUE);
```

Figure 3.17 CRC application code

• Note for Crc\_SetChannelCalculate(): "When DMA is used, the returned result shall be 0U if the DMA Channel did not done transferring all data."

#### 3.7 Runtime errors

18

The driver generates the following DET errors at runtime.

Function	Error Code	Condition triggering the error
Crc_Init()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_SetChannelConfig()	CRC_E_INVALID_CHANNEL	API is called with invalid channel ID parameter.

Function	Error Code	Condition triggering the error
Crc_SetChannelCalculate()	CRC_E_INVALID_CHANNEL	API is called with invalid channel ID parameter.
Crc_SetChannelCalculate()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_GetChannelResult()	CRC_E_INVALID_CHANNEL	API is called with invalid channel ID parameter.
Crc_CalculateCRC8()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC8()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_CalculateCRC8H2F()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC8H2F()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_CalculateCRC16()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC16()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_CalculateCRC16ARC()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC16ARC()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_CalculateCRC32()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC32()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_CalculateCRC32P4()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC32P4()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_CalculateCRC64()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.
Crc_CalculateCRC64()	CRC_E_INVALID_WIDTH_TYPE	API is called with invalid with type support.
Crc_GetVersionInfo()	CRC_E_INVALID_POINTER	API is called with a NULL pointer as parameter.

# 3.8 Symbolic Names Disclaimer

All containers having symbolicNameValue set to TRUE in the AUTOSAR schema will generate defines like:

 $\# define < Mip > Conf \_ < Container \_ ShortName > \_ < Container \_ ID >$ 

For this reason it is forbidden to duplicate the names of such containers across the RTD configurations or to use names that may trigger other compile issues (e.g. match existing #ifdefs arguments).

# **Chapter 4**

# **Tresos Configuration Plug-in**

This chapter describes the Tresos configuration plug-in for the driver. All the parameters are described below.

- Module Crc
  - Container CrcGeneral
    - \* Parameter CrcDetectError
    - \* Parameter CrcEnableUserModeSupport
    - \* Parameter CrcDmaSupportEnable
    - \* Parameter CrcMultiCoreEnable
    - \* Parameter CrcVersionInfoApi
    - \* Parameter Crc8Mode
    - \* Parameter Crc8H2FMode
    - \* Parameter Crc16Mode
    - \* Parameter Crc16ARCMode
    - \* Parameter Crc32Mode
    - \* Parameter Crc32P4Mode
    - \* Parameter Crc64Mode
  - Container CrcChannelConfig
    - \* Parameter CrcLogicChannelName
    - \* Parameter CrcAutosarSelect
    - \* Parameter CrcCalculationType
    - \* Reference CrcPartitionRefOfChannel
    - \* Container CrcHardwareConfig
      - · Parameter CrcHwInstance
      - · Parameter CrcHwChannel
      - · Parameter CrcDmaChannelEnable
      - · Reference CrcDmaLogicChannelName
    - \* Container CrcProtocolInfo
      - · Parameter CrcProtocolType
      - · Parameter CrcPolynomialValue
      - · Parameter CrcWriteBitSwap
      - · Parameter CrcWriteByteSwap
      - · Parameter CrcReadBitSwap

- · Parameter CrcReadByteSwap
- · Parameter CrcInversionEnable
- Container CrcEcucPartitionRefArray
  - \* Reference CrcEcucPartitionRef
- Container CommonPublishedInformation
  - \* Parameter ArReleaseMajorVersion
  - \* Parameter ArReleaseMinorVersion
  - \* Parameter ArReleaseRevisionVersion
  - \* Parameter ModuleId
  - \* Parameter SwMajorVersion
  - \* Parameter SwMinorVersion
  - \* Parameter SwPatchVersion
  - \* Parameter VendorApiInfix
  - \* Parameter VendorId

## 4.1 Module Crc

Vendor specific: Configuration of the Crc (Cyclic Redundancy Check) module.

Included containers:

- CrcGeneral
- CrcChannelConfig
- CrcEcucPartitionRefArray
- CommonPublishedInformation

Property	Value
type	ECUC-MODULE-DEF
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantSupport	false
supportedConfigVariants	VARIANT-PRE-COMPILE

## 4.2 Container CrcGeneral

Crc General

All general parameters of the Crc driver are collected here.

Included subcontainers:

S32K1 CRC Driver

# Tresos Configuration Plug-in

• None

Property	Value
type	ECUC-PARAM-CONF-CONTAINER-DEF
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A

## 4.3 Parameter CrcDetectError

CRC Development Error Detect

Compile switch to enable/disable development error detection for this module.

Unchecked: Crc Development error detection disabled

Checked: Crc Development error detection enabled

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	true

# ${\bf 4.4}\quad {\bf Parameter~CrcEnable User Mode Support}$

Crc User Mode Support

When this parameter is enabled, the Crc module will adapt to run from User Mode, with the following measures: Configuring REG\_PROT for Crc IPs so that the registers under protection can be accessed from user mode by setting UAA bit in REG\_PROT\_GCR to 1

For more information and availability on this platform, please see chapter User Mode Support in IM

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF

#### Tresos Configuration Plug-in

Property	Value
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.5 Parameter CrcDmaSupportEnable

Dma Support Calculate

Check this in order to be able to use DMA in the Crc driver. Leaving this unchecked will allow the Crc driver to compile with no dependencies from the Mcl driver.

Note: Implementation Specific Parameter.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.6 Parameter CrcMultiCoreEnable

Crc Multicore Enable

This parameter globally enables the possibility to support multicore. If this parameter is enabled, at least one EcucPartition needs to be defined (in all variants).

Note: This is an Implementation Specific Parameter.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.7 Parameter CrcVersionInfoApi

CRC VersionInfo Api

Compile switch to enable/disable the version information API.

Checked: API enabled

Unchecked: API disabled

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.8 Parameter Crc8Mode

Switch to select one of the available Crc 8-bit (SAE J1850) calculation methods

Note: If large data blocks have to be calculated (>32 bytes, depending on performance of processor platform), the table based calculation or hardware method should be configured for the function Crc\_CalculateCRC8 in order to decrease the calculation time.

#### Tresos Configuration Plug-in

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
symbolicNameValue	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_8_TABLE
literals	['CRC_8_TABLE', 'CRC_8_HARDWARE', 'CRC_8_RUNTIME']

## 4.9 Parameter Crc8H2FMode

Switch to select one of the available Crc 8-bit (2Fh polynomial) calculation methods

Note: If large data blocks have to be calculated (>32 bytes, depending on performance of processor platform), the table based calculation or hardware method should be configured for the function Crc\_CalculateCRC8H2F in order to decrease the calculation time.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
${\it symbolic} Name Value$	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_8H2F_TABLE
literals	['CRC_8H2F_TABLE', 'CRC_8H2F_HARDWARE', 'CRC_8H2F_RUNTIME']

## 4.10 Parameter Crc16Mode

Switch to select one of the available Crc 16-bit (CCITT-FALSE) calculation methods

Note: If large data blocks have to be calculated (>32 bytes, depending on performance of processor platform), the table based calculation or hardware method should be configured for the function Crc\_CalculateCRC16 in order to decrease the calculation time.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
symbolicNameValue	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_16_TABLE
literals	['CRC_16_TABLE', 'CRC_16_HARDWARE', 'CRC_16_RUNTIME']

## 4.11 Parameter Crc16ARCMode

Switch to select one of the available CRC-16/ARC (polynomial 8005) calculation methods

Note: If large data blocks have to be calculated (>32 bytes, depending on performance of processor platform), the table based calculation method should be configured for the function Crc\_CalculateCRC16ARC in order to decrease the calculation time.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
${\it symbolicNameValue}$	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_16ARC_TABLE
literals	['CRC_16ARC_TABLE', 'CRC_16ARC_HARDWARE', 'CRC_16ARC_RU $\leftarrow$ NTIME']

## 4.12 Parameter Crc32Mode

Switch to select one of the available Crc 32-bit (IEEE-802.3 CRC32 Ethernet Standard) calculation methods

Note: If large data blocks have to be calculated (>32 bytes, depending on performance of processor platform), the table based calculation or hardware method should be configured for the function Crc\_CalculateCRC32 in order to decrease the calculation time.

#### Tresos Configuration Plug-in

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
symbolicNameValue	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_32_TABLE
literals	['CRC_32_TABLE', 'CRC_32_HARDWARE', 'CRC_32_RUNTIME']

## 4.13 Parameter Crc32P4Mode

Switch to select one of the available Crc 32-bit E2E Profile 4 calculation methods.

Note: If large data blocks have to be calculated (>32 bytes, depending on performance of processor platform), the table based calculation method should be configured for the function Crc\_CalculateCRC32P4 in order to decrease the calculation time.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
symbolicNameValue	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_32P4_TABLE
literals	['CRC_32P4_TABLE', 'CRC_32P4_HARDWARE', 'CRC_32P4_RUNTIME']

## 4.14 Parameter Crc64Mode

Switch to select one of the available Crc 64-bit calculation methods

Note: If large data blocks have to be calculated (>64 bytes, depending on performance of processor platform), the table based calculation method should be configured for the function Crc\_CalculateCRC64 in order to decrease the calculation time.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	AUTOSAR_ECUC
symbolicNameValue	False
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_64_TABLE
literals	['CRC_64_TABLE', 'CRC_64_HARDWARE', 'CRC_64_RUNTIME']

# 4.15 Container CrcChannelConfig

Crc Channels Configuration

Configuration of an individual Crc channel. Symbolic names will be generated for each channel.

Included subcontainers:

- CrcHardwareConfig
- CrcProtocolInfo

Property	Value
type	ECUC-PARAM-CONF-CONTAINER-DEF
lowerMultiplicity	1
upperMultiplicity	Infinite
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE

# 4.16 Parameter CrcLogicChannelName

Logic Channel Name

Channel used for Crc calculation

Property	Value
type	ECUC-STRING-PARAM-DEF
origin	NXP

## Tresos Configuration Plug-in

Property	Value
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_LOGIC_CHANNEL_0

# 4.17 Parameter CrcAutosarSelect

Autosar Selection

Select the Autosar Mode to run.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	NON_AUTOSAR
literals	['NON_AUTOSAR', 'AUTOSAR_CRC_8', 'AUTOSAR_CRC_8H2F', 'AUT $\hookrightarrow$ OSAR_CRC_16', 'AUTOSAR_CRC_16ARC', 'AUTOSAR_CRC_32', 'AU $\hookrightarrow$ TOSAR_CRC_32P4', 'AUTOSAR_CRC_64']

# ${\bf 4.18}\quad {\bf Parameter}\ {\bf CrcCalculation Type}$

Calculation Type

Select Crc Calculation Type.

Note: Implementation Specific Parameter.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF

Property	Value
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_IP_TABLE_CALCULATION
literals	['CRC_IP_TABLE_CALCULATION', 'CRC_IP_HARDWARE_CALCUL↔ ATION', 'CRC_IP_RUNTIME_CALCULATION']

## 4.19 Reference CrcPartitionRefOfChannel

Partition Ref Of Channel

Maps a Crc hardware unit to zero or one ECUC partition to limit the access to this hardware unit. The ECUC partitions referenced are a subset of the ECUC partitions where the Crc driver is mapped to.

Tags: atp.Status=draft

Property	Value
type	ECUC-REFERENCE-DEF
origin	NXP
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
requiresSymbolicNameValue	False
destination	/AUTOSAR/EcucDefs/EcuC/EcucPartitionCollection/EcucPartition

# 4.20 Container CrcHardwareConfig

This container contains the hardware configuration parameters of the Crc module.

Included subcontainers:

• None

## Tresos Configuration Plug-in

Property	Value
type	ECUC-PARAM-CONF-CONTAINER-DEF
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A

# 4.21 Parameter CrcHwInstance

Hardware Instance

Identifies the Crc Hardware Instance.

Note: Implementation Specific Parameter.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_HW_INSTANCE_0
literals	['CRC_HW_INSTANCE_0']

# 4.22 Parameter CrcHwChannel

Hardware Channel

Selects one of the Crc hardware channels available on the device.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A

Property	Value
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	CRC_HW_CHANNEL_0
literals	['CRC_HW_CHANNEL_0']

# 4.23 Parameter CrcDmaChannelEnable

Dma Channel Enable

Checked: Enabled

Unchecked: Disabled

Note: DMA mode does not support for CRC Autosar Library

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.24 Reference CrcDmaLogicChannelName

DMA Logic Channel Name

DMA Logic Channel is used for this Crc logic channel.

Property	Value
type	ECUC-REFERENCE-DEF
origin	NXP
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false

### Tresos Configuration Plug-in

Property	Value
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
requiresSymbolicNameValue	False
destination	$/ AUTOSAR/EcucDefs/Mcl/MclConfig/dmaLogicChannel\_Type$

### 4.25 Container CrcProtocolInfo

This container configuration parameters of protocol type

Included subcontainers:

• None

Property	Value
type	ECUC-PARAM-CONF-CONTAINER-DEF
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A

# 4.26 Parameter CrcProtocolType

Protocol Type

Identifies the Crc Protocol Type.

Note: Implementation Specific Parameter.

Property	Value
type	ECUC-ENUMERATION-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE

Property	Value
defaultValue	CRC_PROTOCOL_8BIT_SAE_J1850
literals	['CRC_PROTOCOL_8BIT_SAE_J1850', 'CRC_PROTOCOL_8BIT_H2F', 'CRC_PROTOCOL_16BIT_CCITT_FALSE', 'CRC_PROTOCOL_16BIT← _ARC', 'CRC_PROTOCOL_32BIT_ETHERNET', 'CRC_PROTOCOL_32← BIT_E2E_P4', 'CRC_PROTOCOL_64BIT_ECMA', 'CRC_PROTOCOL_← 8BIT_CUSTOM', 'CRC_PROTOCOL_16BIT_CUSTOM', 'CRC_PROTOCOL_CCOL_32BIT_CUSTOM', 'CRC_PROTOCOL_64BIT_CUSTOM']

# 4.27 Parameter CrcPolynomialValue

Polynomial Value

Start value when the algorithm starts in process Calculate CRC

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	0
max	18446744073709551615
min	0

# 4.28 Parameter CrcWriteBitSwap

Write Bit Swap

Enumerator that defines Crc Write data bitwise functionality.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A

S32K1 CRC Driver

### Tresos Configuration Plug-in

Property	Value
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.29 Parameter CrcWriteByteSwap

Write Bit Swap

Enumerator that defines Crc Write data bytewise functionality.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# ${\bf 4.30} \quad {\bf Parameter} \; {\bf CrcReadBitSwap}$

Read Bit Swap

Enumerator that defines Crc Read data bitwise functionality.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

### 4.31 Parameter CrcReadByteSwap

Read Byte Swap

Enumerator that defines Crc Read data bytewise functionality.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
symbolicNameValue	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

### 4.32 Parameter CrcInversionEnable

Inverse Enable (XOR)

The result shall be complement(inversion) of the actual checksum.

Property	Value
type	ECUC-BOOLEAN-PARAM-DEF
origin	NXP
${\it symbolicNameValue}$	False
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
defaultValue	false

# 4.33 Container CrcEcucPartitionRefArray

Crc Ecuc Partition Ref Array.

Included subcontainers:

#### Tresos Configuration Plug-in

#### • None

Property	Value
type	ECUC-PARAM-CONF-CONTAINER-DEF
lowerMultiplicity	0
upperMultiplicity	Infinite
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE

# 4.34 Reference CrcEcucPartitionRef

Maps the Crc driver to zero or multiple ECUC partitions to make the driver API available in the according partition.

Property	Value
type	ECUC-REFERENCE-DEF
origin	NXP
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PRE-COMPILE
${\it requires Symbolic Name Value}$	False
destination	/AUTOSAR/EcucDefs/EcuC/EcucPartitionCollection/EcucPartition

# 4.35 Container CommonPublishedInformation

Common Published Information

Common container, aggregated by all modules. It contains published information about vendor and versions.

Included subcontainers:

#### • None

Property	Value
type	ECUC-PARAM-CONF-CONTAINER-DEF
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A

## ${\bf 4.36}\quad {\bf Parameter}\ {\bf ArRelease Major Version}$

AUTOSAR Release Major Version

Major version number of AUTOSAR specification on which the appropriate implementation is based on.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	4
max	4
min	4

### 4.37 Parameter ArReleaseMinorVersion

AUTOSAR Release Minor Version

Minor version number of AUTOSAR specification on which the appropriate implementation is based on.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	4
max	4
min	4

### 4.38 Parameter ArReleaseRevisionVersion

AUTOSAR Release Revision Version

Revision version number of AUTOSAR specification on which the appropriate implementation is based on.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	0
max	0
min	0

### 4.39 Parameter ModuleId

 ${\bf Module\ ID}$ 

Module ID of this module from Module List.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	201
max	201
min	201

### 4.40 Parameter SwMajorVersion

Software Major Version

Major version number of the vendor specific implementation of the module. The numbering is vendor specific.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	1
max	1
min	1

### 4.41 Parameter SwMinorVersion

Software Minor Version

Minor version number of the vendor specific implementation of the module. The numbering is vendor specific.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	0
max	0
min	0

### 4.42 Parameter SwPatchVersion

Software Patch Version

Patch level version number of the vendor specific implementation of the module. The numbering is vendor specific.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	1
max	1
min	1

### 4.43 Parameter VendorApiInfix

Vendor Api Infix

In driver modules which can be instantiated several times on a single ECU, BSW00347 requires that the name of APIs is extended by the VendorId and a vendor specific name.

This parameter is used to specify the vendor specific name. In total, the implementation specific name is generated as follows:

<ModuleName> >VendorId> <VendorApiInfix>.

E.g. assuming that the VendorId of the implementor is 123 and the implementer chose a VendorApiInfix of "v11r456" a api name Can\_Write defined in the SWS will translate to Can\_123\_v11r456Write.

This parameter is mandatory for all modules with upper multiplicity > 1. It shall not be used for modules with upper multiplicity =1.

Property	Value
type	ECUC-STRING-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	0
upperMultiplicity	1
postBuildVariantMultiplicity	false
multiplicityConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
postBuildVariantValue	falsS32K1 CRC Driver
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	

### 4.44 Parameter VendorId

Vendor Id

Vendor ID of the dedicated implementation of this module according to the AUTOSAR vendor list.

Property	Value
type	ECUC-INTEGER-PARAM-DEF
origin	NXP
symbolicNameValue	false
lowerMultiplicity	1
upperMultiplicity	1
postBuildVariantMultiplicity	N/A
multiplicityConfigClasses	N/A
postBuildVariantValue	false
valueConfigClasses	VARIANT-PRE-COMPILE: PUBLISHED-INFORMATION
defaultValue	43
max	43
min	43

This chapter describes the Tresos configuration plug-in for the driver Driver. The most of the parameters are described below.

# **Chapter 5**

### **Module Index**

# 5.1 Software Specification

Here is a list of all modules:

CRC HLD Driver	45
CRC IPL Driver	53
CRC IPW Driver	50

## **Chapter 6**

### **Module Documentation**

### 6.1 CRC HLD Driver

#### 6.1.1 Detailed Description

### **Data Structures**

- struct Crc\_PartitionType CRC Partition Configuration Type. More...
- struct Crc\_InitType

Initialization data for the CRC driver. More...

#### Macros

- #define CDD\_CRC\_MODULE\_ID
  - Parameters that shall be published within the Crc driver header file and also in the module's description file.
- #define CRC\_INSTANCE\_ID

ID of CRC Instance.

- #define CRC\_CALCULATECRC8\_ID
  - $API\ service\ ID\ for\ Crc\_CalculateCRC8\ function.$
- #define CRC\_CALCULATECRC16\_ID
  - API service ID for Crc\_CalculateCRC16 function.
- #define CRC\_CALCULATECRC32\_ID
  - $API\ service\ ID\ for\ Crc\_CalculateCRC32\ function.$
- #define CRC\_GETVERSIONINFO\_ID
  - API service ID for Crc\_GetVersionInfo function.
- #define CRC\_CALCULATECRC8H2F\_ID
  - $API\ service\ ID\ for\ Crc\_CalculateCRC8H2F\ function.$
- #define CRC\_CALCULATECRC32P4\_ID
  - API service ID for Crc\_CalculateCRC32P4 function.
- #define CRC\_CALCULATECRC64\_ID

API service ID for Crc\_CalculateCRC64 function.

• #define CRC CALCULATECRC16ARC ID

API service ID for Crc\_CalculateCRC16ARC function.

• #define CRC\_INIT\_ID

API service ID for Crc\_Init function.

• #define CRC\_SETCHANNELCONFIG\_ID

API service ID for Crc\_SetChannelConfig function.

• #define CRC\_SETCHANNELCALCULATE\_ID

 $API\ service\ ID\ for\ Crc\_SetChannelCalculate\ function.$ 

• #define CRC\_GETCHANNELRESULT\_ID

API service ID for Crc\_GetChannelResult function.

• #define CRC\_E\_INVALID\_CHANNEL

API service is called with wrong channel identifier.

• #define CRC E INVALID POINTER

API service is called with NULL pointer parameter.

• #define CRC\_E\_PARAM\_CONFIG

The CRC module is not properly configured.

• #define CRC E INIT FAILED

The CRC module is not properly initialized.

• #define CRC\_TYPES\_VENDOR\_ID

Parameters that shall be published within the Crc driver header file and also in the module's description file.

#### Types Reference

• typedef Crc\_Ip\_LogicChannelConfigType Crc\_ChannelConfigType

This type contains the CRC Channel Configuration.

#### Function Reference

• void Crc\_Init (const Crc\_InitType \*ConfigPtr)

This service will store the Crc driver installation configuration based on user configuration.

• void Crc SetChannelConfig (const uint32 Channel, const Crc ChannelConfigType \*pxChannelConfig)

This function initializes the CRC Channel configuration by logicChannel's info.

• uint64 Crc\_SetChannelCalculate (const uint32 Channel, const uint8 \*pCrcData, const uint32 CrcLength, const uint64 CrcStartValue, const boolean IsFirstCall)

This function shall start algorithm calculate CRC.

• uint64 Crc GetChannelResult (const uint32 Channel)

 $This\ function\ get\ result\ CRC\ after\ finish\ calculated.$ 

#### 6.1.2 Data Structure Documentation

#### 6.1.2.1 struct Crc\_PartitionType

CRC Partition Configuration Type.

The Channel is identified by the following Partition

Definition at line 112 of file Crc\_Types.h.

#### 6.1.2.2 struct Crc\_InitType

Initialization data for the CRC driver.

A pointer to such a structure is provided to the CRC initialization routines for configuration.

Definition at line 124 of file Crc\_Types.h.

#### 6.1.3 Macro Definition Documentation

#### 6.1.3.1 CDD\_CRC\_MODULE\_ID

#define CDD\_CRC\_MODULE\_ID

Parameters that shall be published within the Crc driver header file and also in the module's description file.

Definition at line 62 of file CDD Crc.h.

#### 6.1.3.2 CRC\_INSTANCE\_ID

#define CRC\_INSTANCE\_ID

ID of CRC Instance.

Parameters used when raising an error/exception

Definition at line 132 of file CDD\_Crc.h.

#### 6.1.3.3 CRC\_CALCULATECRC8\_ID

#define CRC\_CALCULATECRC8\_ID

API service ID for Crc CalculateCRC8 function.

NXP Semiconductors 47

#### 6.1.3.4 THE SERVICE ID OF THE CALLER STANDARD FUNCTION

Parameters used when raising an error/exception Definition at line 143 of file CDD\_Crc.h.

#### 6.1.3.5 CRC\_CALCULATECRC16\_ID

#define CRC\_CALCULATECRC16\_ID API service ID for Crc\_CalculateCRC16 function. Parameters used when raising an error/exception Definition at line 149 of file CDD\_Crc.h.

#### 6.1.3.6 CRC\_CALCULATECRC32\_ID

#define CRC\_CALCULATECRC32\_ID API service ID for Crc\_CalculateCRC32 function. Parameters used when raising an error/exception Definition at line 155 of file CDD Crc.h.

#### 6.1.3.7 CRC\_GETVERSIONINFO\_ID

#define CRC\_GETVERSIONINFO\_ID API service ID for Crc\_GetVersionInfo function. Parameters used when raising an error/exception Definition at line 161 of file CDD\_Crc.h.

#### 6.1.3.8 CRC\_CALCULATECRC8H2F\_ID

#define CRC\_CALCULATECRC8H2F\_ID API service ID for Crc\_CalculateCRC8H2F function. Parameters used when raising an error/exception Definition at line 167 of file CDD Crc.h.

### 6.1.3.9 CRC\_CALCULATECRC32P4\_ID

#define CRC\_CALCULATECRC32P4\_ID API service ID for Crc\_CalculateCRC32P4 function. Parameters used when raising an error/exception Definition at line 173 of file CDD\_Crc.h.

#### 6.1.3.10 CRC\_CALCULATECRC64\_ID

#define CRC\_CALCULATECRC64\_ID API service ID for Crc\_CalculateCRC64 function. Parameters used when raising an error/exception Definition at line 179 of file CDD\_Crc.h.

#### 6.1.3.11 CRC\_CALCULATECRC16ARC\_ID

#define CRC\_CALCULATECRC16ARC\_ID API service ID for Crc\_CalculateCRC16ARC function. Parameters used when raising an error/exception Definition at line 185 of file CDD Crc.h.

#### 6.1.3.12 CRC INIT ID

#define CRC\_INIT\_ID API service ID for Crc\_Init function.

#### 6.1.3.13 THE SERVICE ID OF THE CALLER CDD FUNCTION

Parameters used when raising an error/exception Definition at line 196 of file CDD\_Crc.h.

#### 6.1.3.14 CRC\_SETCHANNELCONFIG\_ID

#define CRC\_SETCHANNELCONFIG\_ID API service ID for Crc\_SetChannelConfig function. Parameters used when raising an error/exception Definition at line 202 of file CDD\_Crc.h.

#### 6.1.3.15 CRC\_SETCHANNELCALCULATE\_ID

#define CRC\_SETCHANNELCALCULATE\_ID API service ID for Crc\_SetChannelCalculate function. Parameters used when raising an error/exception Definition at line 208 of file CDD Crc.h.

#### 6.1.3.16 CRC\_GETCHANNELRESULT\_ID

#define CRC\_GETCHANNELRESULT\_ID API service ID for Crc\_GetChannelResult function. Parameters used when raising an error/exception Definition at line 214 of file CDD Crc.h.

#### 6.1.3.17 CRC E INVALID CHANNEL

#define CRC\_E\_INVALID\_CHANNEL API service is called with wrong channel identifier.

#### 6.1.3.18 THE ERROR ID TO BE REPORTED

Parameter is used when raising a Det error Definition at line 225 of file CDD\_Crc.h.

NXP Semiconductors 49

#### 6.1.3.19 CRC\_E\_INVALID\_POINTER

#define CRC\_E\_INVALID\_POINTER

API service is called with NULL pointer parameter.

Parameter is used when raising a Det error

Definition at line 232 of file CDD\_Crc.h.

#### 6.1.3.20 CRC\_E\_PARAM\_CONFIG

#define CRC\_E\_PARAM\_CONFIG

The CRC module is not properly configured. Parameter is used when raising a Det error Definition at line 239 of file CDD Crc.h.

#### 6.1.3.21 CRC\_E\_INIT\_FAILED

#define CRC\_E\_INIT\_FAILED

The CRC module is not properly initialized. Parameter is used when raising a Det error Definition at line 246 of file CDD Crc.h.

#### 6.1.3.22 CRC\_TYPES\_VENDOR\_ID

#define CRC\_TYPES\_VENDOR\_ID

Parameters that shall be published within the Crc driver header file and also in the module's description file. Definition at line 61 of file Crc\_Types.h.

#### 6.1.4 Types Reference

#### 6.1.4.1 Crc\_ChannelConfigType

typedef Crc\_Ip\_LogicChannelConfigType Crc\_ChannelConfigType

This type contains the CRC Channel Configuration.

The Channel is identified by the following structure configure parameter for each CRC Channel Definition at line 261 of file CDD\_Crc.h.

#### 6.1.5 Function Reference

#### 6.1.5.1 Crc\_Init()

```
void Crc_Init (
```

```
const Crc_InitType * ConfigPtr )
```

This service will store the Crc driver installation configuration based on user configuration.

This service is a non-reentrant function that shall store user configuration. The initialization is applied for the enabled IPs, configured statically.

#### Parameters

in	pxCrcInit	- Pointer to the Crc specific configuration structure that contains static configuration.	
----	-----------	---	--

#### Returns

void

### $6.1.5.2 \quad Crc\_SetChannelConfig()$

This function initializes the CRC Channel configuration by logicChannel's info.

This service is a reentrant function that shall initialize parameters list for the CRC Channel. The list is composed of an array of Crc Channel parameters settings.

#### Parameters

in	Channel	- Logic Channel Tag defined by the user.
in	pxChannelConfig	- Pointer to the Logic Channel Config

#### Returns

void

#### 6.1.5.3 Crc\_SetChannelCalculate()

This function shall start algorithm calculate CRC.

This service is a reentrant function that shall start the Channel to calculate the CRC using the configured algorithm.

#### Parameters

in	Channel	- Logic Channel Tag defined by the user.
in	*pCrcData	- Pointer to the Crc Data Input.
in	CrcLength	- Length of crcDataPtr block to be calculated in bytes
in	CrcStartValue	- Start value (seed Value) when the algorithm starts.
in	IsFirstCall	- TRUE: ignore CrcStartValue the initialization value is known by the chosen algorithm.
		• FALSE: initialization CrcStartValue is interpreted to be the return value of the previous function

#### Returns

- 32 bit result of CRC calculation

### 6.1.5.4 Crc\_GetChannelResult()

```
uint64 Crc_GetChannelResult (
            const uint32 Channel )
```

This function get result CRC after finish calculated.

This service is a function that shall get result CRC calculated by CRC Channel Allocated

#### Parameters

Channel - Logic Channel Tag defined by the user.
--

#### Returns

Result of CRC calculation.

### 6.2 CRC IPL Driver

#### 6.2.1 Detailed Description

#### **Data Structures**

• struct Crc Ip CrcProtocolInfoType

This type contains the CRC Protocol Information. More...

• struct Crc\_Ip\_LogicChannelStateType

Internal State for the Logic CHannel. More...

• struct Crc Ip LogicChannelConfigType

This type contains the CRC Ip Channel Configuration. More...

• struct Crc\_Ip\_LogicChannelType

This type contains the Crc Ip Channel Type. More...

• struct Crc\_Ip\_InitType

This type contains the Crc Ip Initialization. More...

#### Macros

• #define CRC IP DEVASSERT VENDOR ID

Parameters that shall be published within the standard types header file and also in the module's description file.

#### Enum Reference

• enum Crc\_Ip\_CrcWidthType

This type contains the CRC Width Type.

• enum Crc\_Ip\_CalculationType

 ${\it This type \ contains \ the \ CRC \ Ip \ Channel \ Configuration.}$ 

• enum Crc\_Ip\_ProtocolType

This type contains the CRC Ip Channel Protocol.

#### Function Reference

void Crc\_Ip\_Init (const Crc\_Ip\_InitType \*const pxCrcIpInit)

This function initializes the CRC Driver in IP Layer.

• void Crc\_Ip\_SetChannelConfig (const uint32 LogicChannel, const Crc\_Ip\_LogicChannelConfigType \*px← LocLogicChannelConfig)

 $This \ function \ initializes \ the \ CRC \ Channel \ configuration \ by \ Logic Channel's \ info.$ 

• uint64 Crc\_Ip\_SetChannelCalculate (const uint32 LogicChannel, const uint8 \*DataPtr, const uint32 Length, const uint64 StartValue, const boolean IsFirstCall)

This function shall start algorithm calculate CRC in IP layer.

• uint64 Crc\_Ip\_GetChannelResult (const uint32 LogicChannel)

This function get result CRC after finish calculated in IP Layer.

• uint64 Crc\_Ip\_LookupTablesCalculate (const Crc\_Ip\_LogicChannelStateType \*pxLogicChannelState, const uint8 \*DataPtr, const uint32 Length, const uint64 InitialSeedStartValue)

This function shall start algorithm Lookup Table calculation CRC.

• uint64 Crc\_Ip\_SwCalculate (const Crc\_Ip\_LogicChannelStateType \*pxLogicChannelState, const uint8 \*DataPtr, const uint32 Length, const uint64 InitialSeedStartValue)

This function shall start algorithm Software calculation CRC.

#### 6.2.2 Data Structure Documentation

#### 6.2.2.1 struct Crc\_Ip\_CrcProtocolInfoType

This type contains the CRC Protocol Information. Definition at line 104 of file Crc\_Ip\_State.h.

#### 6.2.2.2 struct Crc\_Ip\_LogicChannelStateType

Internal State for the Logic CHannel.

It shall contain the last loaded configuration by the user for the specified Logic Channel Definition at line 121 of file Crc\_Ip\_State.h.

#### Data Fields

Type	Name	Description
Crc_Ip_ProtocolType	Protocol	Protocol Type: CRC-CCITT, CRC-32, CRC-8, CRC-8-H2F
const Crc_Ip_CrcProtocolInfoType *	CrcProtocolInfo	Crc Protocol Information
uint64	CrcResult	Stores the last CRC Result.

#### 6.2.2.3 struct Crc\_Ip\_LogicChannelConfigType

This type contains the CRC Ip Channel Configuration.

The Channel is identified by the following structure Configure parammeter for each CRC Channel Definition at line 159 of file Crc\_Ip\_Types.h.

#### Data Fields

Type	Name	Description
Crc_Ip_ProtocolType	Protocol	CRC protocol type.
uint64	PolynomValue	Polynomial input, MSB it first. Example polynomial: $0x1021U = 1\_0000\_0010\_0001 = x^12 + x^5 + 1$
boolean	WriteBitSwap	Swap when writing CRC input data.
boolean	WriteByteSwap	Swap when writing CRC input data.
boolean	ReadBitSwap	Swap result bits after algorithm finished calculating $\operatorname{CRC}$ .
boolean	ReadByteSwap	Swap result bytes after algorithm finished calculating CRC .
boolean	InverseEnable	The inversion operation is a complement (or negation) of the content.

#### 6.2.2.4 struct Crc\_Ip\_LogicChannelType

This type contains the Crc Ip Channel Type.

The Crc Ip Initialization contains all the information required to initialize the CRC Driver Internal driver structure. Definition at line 175 of file Crc\_Ip\_Types.h.

#### Data Fields

Type	Name	Description
Crc_Ip_CalculationType	CalculationType	Software, Table, Hardware Calculation
uint8	HwInst	CRC hardware instance.

#### Data Fields

Type	Name	Description
uint8	HwChannel	CRC hardware channel.
Crc_Ip_LogicChannelConfigType *	LogicChannelConfig	Pointer to LogicChannelConfig.

#### 6.2.2.5 struct Crc\_Ip\_InitType

This type contains the Crc Ip Initialization.

The Crc Ip Initialization contains all the information required to initialize the CRC Driver Internal driver structure. Definition at line 192 of file Crc\_Ip\_Types.h.

#### Data Fields

Туре	Name	Description
const Crc_Ip_LogicChannelType *const *	LogicChannelConfigList	Pointer to list LogicChannelConfig

#### 6.2.3 Macro Definition Documentation

#### 6.2.3.1 CRC\_IP\_DEVASSERT\_VENDOR\_ID

#define CRC\_IP\_DEVASSERT\_VENDOR\_ID

Parameters that shall be published within the standard types header file and also in the module's description file. Definition at line 62 of file Crc\_Ip\_Devassert.h.

#### 6.2.4 Enum Reference

#### 6.2.4.1 Crc\_Ip\_CrcWidthType

enum Crc\_Ip\_CrcWidthType

This type contains the CRC Width Type.

Select Width for each Channel to calculate CRC.

Definition at line 88 of file  $Crc\_Ip\_State.h.$ 

#### ${\bf 6.2.4.2 \quad Crc\_Ip\_CalculationType}$

enum Crc\_Ip\_CalculationType

This type contains the CRC Ip Channel Configuration.

The Channel is identified by the following structure Configure parammeter for each CRC Channel Internal driver enumeration.

#### Enumerator

CRC_IP_RUNTIME_CALCULATION	Slower execution (software calculate), but small code size (no ROM table)
CRC_IP_TABLE_CALCULATION	Fast execution (software calculate), but larger code size (ROM table)
CRC_IP_HARDWARE_CALCULATION	Fast execution, less CPU time

Definition at line 122 of file Crc Ip Types.h.

#### 6.2.4.3 Crc\_Ip\_ProtocolType

```
enum Crc_Ip_ProtocolType
```

This type contains the CRC Ip Channel Protocol.

The Protocol shall be selected from the available list. Software CRC supports all protocols. Table based CRC support the following protocols: Hardware CRC supports implementation specific protocols.

#### Enumerator

CRC_PROTOCOL_8BIT_CUSTOM	Generate 8-bit CUSTOM CRC with user defined algorithm
CRC_PROTOCOL_16BIT_CUSTOM	Generate 16-bit CUSTOM CRC with user defined algorithm
CRC_PROTOCOL_32BIT_CUSTOM	Generate 32-bit CUSTOM CRC with user defined algorithm
CRC_PROTOCOL_64BIT_CUSTOM	Generate 64-bit CUSTOM CRC with user defined algorithm
CRC_PROTOCOL_8BIT_H2F	Generate 8-bit CRC: H2F, AUTOSAR 4.0 CRC-8 0x2F
CRC_PROTOCOL_8BIT_SAE_J1850	Generate 8-bit CRC: VDA CAN, SAE-J1850 CRC-8.
CRC_PROTOCOL_16BIT_ARC	Generate 16-bit CRC ARC code with Polynomial: 0x8005
CRC_PROTOCOL_16BIT_CCITT_FALSE	Generate 16-bit CRC: x.25. CCITT16 CRC-16 0x1021
CRC_PROTOCOL_32BIT_E2E_P4	Generate 32-bit CRC: E2E Profile 4 CRC-32 P4 0xF4ACFB13
CRC_PROTOCOL_32BIT_ETHERNET	Generate 32-bit CRC: Ethernet (IEEE 802,3) CCITT32
	CRC-32 0x04C11DB7
CRC_PROTOCOL_64BIT_ECMA	Generate 64-bit CRC: ECMA
CRC_PROTOCOL_INVALID	Invalid protocol

Definition at line 135 of file Crc Ip Types.h.

#### 6.2.5 Function Reference

#### 6.2.5.1 Crc\_Ip\_Init()

This function initializes the CRC Driver in IP Layer.

This service is a non-reentrant function that shall initialize the Crc driver. The user must make sure that the clock is enabled.

#### Parameters

```
in paxLogicChannelConfig - Pointer to the configuration structure.
```

#### Returns

void

#### 6.2.5.2 Crc\_Ip\_SetChannelConfig()

This function initializes the CRC Channel configuration by LogicChannel's info.

This service is a non-reentrant function that shall initializes parameters list for the CRC Channel. The list is composed of an array of Crc Channel parameters settings.

#### Parameters

in	Logic Channel	- Specifies the Logic Channel Tag defined by the user
in	pxLocLogic Channel Config	- Specifies the Logic Channel Config defined by the user

#### Returns

void

#### 6.2.5.3 Crc\_Ip\_SetChannelCalculate()

This function shall start algorithm calculate CRC in IP layer.

This service is a non-reentrant function that shall start algorithm calculate in CRC Channel with LogicChannelConfig info

#### Parameters

in	Logic Channel	- Specifies the Logic Channel Tag defined by the user.
in	*DataPtr	- Pointer to start address of data block to be calculated
in	Length	- Length of DataPtr block to be calculated in bytes. *
in	Start Value	- Start value (seed Value) when the algorithm starts.
in	IsFirstCall	- TRUE: ignore StartValue the initialization value is known by the chosen algorithm.
		• FALSE: initialization StartValue is interpreted to be the return value of the previous function

#### Returns

Result of CRC calculated.

#### 6.2.5.4 Crc\_Ip\_GetChannelResult()

This function get result CRC after finish calculated in IP Layer.

This service is a function that shall get result from CRC Channel after finish calculated

#### Parameters

in	Logic Channel	- Specifies the Logic Channel Tag defined by the user
----	---------------	---

#### Returns

result of CRC calculated.

#### 6.2.5.5 Crc\_Ip\_LookupTablesCalculate()

This function shall start algorithm Lookup Table calculation CRC.

This service is a reentrant function that shall calculate the CRC Lookup Table Channel.

#### Parameters

in	pxLogic Channel State	- Pointer Logic Channel Config contains parameter for Calculate CRC
in	*DataPtr	- Pointer to start address of data block to be calculated.
in	Length	- Length of data block to be calculated in bytes.
in	Initial Seed Start Value	- Start value (seed Value) when the algorithm starts.

#### Returns

32 bit result of CRC calculation

#### 6.2.5.6 Crc\_Ip\_SwCalculate()

This function shall start algorithm Software calculation CRC.

This service is a reentrant function that shall calculate the CRC Software Channel.

#### Parameters

in	pxLogic Channel State	- Pointer Logic Channel Config contains parameter for Calculate CRC
in	*DataPtr	- Pointer to start address of data block to be calculated.
in	Length	- Length of data block to be calculated in bytes.
in	Initial Seed Start Value	- Start value (seed Value) when the algorithm starts.

#### Returns

32 bit result of CRC calculation

### 6.3 CRC IPW Driver

### 6.3.1 Detailed Description

#### Macros

• #define CRC IPW VENDOR ID

Parameters that shall be published within the Crc driver header file and also in the module's description file.

• #define Crc\_Ipw\_Init(pxCrcIpInit)

This function initializes the CRC Driver in IPW Layer.

• #define Crc\_Ipw\_SetChannelConfig(Channel, pxChannelConfig)

This function initializes the CRC Channel configuration by logicChannel's info.

• #define Crc\_Ipw\_SetChannelCalculate(Channel, pCrcData, CrcLength, CrcStartValue, IsFirstCall)

This function shall start algorithm calculate CRC in IPW layer.

• #define Crc\_Ipw\_GetChannelResult(Channel)

This function get result CRC after finish calculated in IPW Layer.

#### 6.3.2 Macro Definition Documentation

#### 6.3.2.1 CRC\_IPW\_VENDOR\_ID

```
#define CRC_IPW_VENDOR_ID
```

Parameters that shall be published within the Crc driver header file and also in the module's description file. Definition at line 62 of file Crc Ipw.h.

#### 6.3.2.2 Crc\_Ipw\_Init

This function initializes the CRC Driver in IPW Layer.

This service is a non-reentrant function that shall initialize the Crc driver. The user must make sure that the clock is enabled.

#### Parameters

in | pxCrcIpInit | - Pointer to the Crc specific configuration structure that contains static configuration.

Returns

void

Definition at line 132 of file Crc Ipw.h.

#### ${\bf 6.3.2.3 \quad Crc\_Ipw\_SetChannelConfig}$

This function initializes the CRC Channel configuration by logicChannel's info.

This service is a reentrant function that shall initialize parameters list for the CRC Channel. The list is composed of an array of Crc Channel parameters settings.

NXP Semiconductors 59

#### Parameters

in	Channel	- Logic Channel Tag defined by the user.
in	pxChannelConfig	- Specifies the Logic Channel Config defined by the user

#### Returns

void

Definition at line 148 of file Crc\_Ipw.h.

#### ${\bf 6.3.2.4 \quad Crc\_Ipw\_SetChannelCalculate}$

This function shall start algorithm calculate CRC in IPW layer.

This service is a reentrant function that shall start the Channel to calculate the CRC using the configured algorithm.

#### Parameters

in	Channel	- Logic Channel Tag defined by the user
in	*pCrcData	- Pointer to the Crc Data Input.
in	CrcLength	- Length of pCrcData block to be calculated in bytes.
in	CrcStartValue	- Start value (seed Value) when the algorithm starts.
in	IsFirstCall	- TRUE: ignore CrcStartValue the initialization value is known by the chosen algorithm.
		• FALSE: initialization CrcStartValue is interpreted to be the return value of the previous function

#### Returns

Result of CRC calculated.

Definition at line 167 of file Crc\_Ipw.h.

#### ${\bf 6.3.2.5 \quad Crc\_Ipw\_GetChannelResult}$

This function get result CRC after finish calculated in IPW Layer.

This service is a function that shall get result CRC Channel parameters list

#### Parameters

in Channel	- Logic Channel Tag defined by the user.

61

Returns

void

Definition at line 181 of file Crc\_Ipw.h.

How to Reach Us:

Home Page:

nxp.com

Web Support:

nxp.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: nxp.com/SalesTermsandConditions.

NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, COOLFLUX, EMBRACE, GREENCHIP, HITAG, I2C BUS, ICODE, JCOP, LIFE VIBES, MIFARE, MIFARE CLASSIC, MIFARE DESFire, MIFARE PLUS, MIFARE FLEX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TRENCHMOS, UCODE, Freescale, the Freescale logo, AltiVec, C-5, CodeTEST, CodeWarrior, ColdFire, ColdFire+, C-Ware, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorlQ, QorlQ Qonverge, Ready Play, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, SMARTMOS, Tower, TurboLink, and UMEMS are trademarks of NXP B.V. All other product or service names are the property of their respective owners. ARM, AMBA, ARM Powered, Artisan, Cortex, Jazelle, Keil, SecurCore, Thumb, TrustZone, and Vision are registered trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. ARM7, ARM9, ARM11, big.LITTLE, CoreLink, CoreSight, DesignStart, Mali, mbed, NEON, POP, Sensinode, Socrates, ULINK and Versatile are trademarks of ARM Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org.

© 2022 NXP B.V.

