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| --- | --- |
| **Project** | Unit\_On\_Target\_eCS 2 |
| **Module** | McuIf |
| **Test Object** | mcuif\_CheckCrcOnFtmRegistersAutotest |

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| --- | --- | --- |
| **Metrics** | | |
| **Cyclomatic Complexity (CC)** | |  | | --- | |  | |  |
| **Test Case to Complexity Ratio (TC/C)** | |  | | --- | |  | |  |

**Statistics**

|  |  |  |
| --- | --- | --- |
| **Total Testcases** | 1 |  |

**Module Properties**

|  |  |
| --- | --- |
| **Project Root Directory** | S:\Tools\Unit\_On\_Target\_eCS 2 |
| **Configuration File** | $(PROJECTROOT)\tessy\config\configuration.xml |
| **Target Environment** | IAR ARM V5 iSYSTEM winIDEA (Default) |
| **Kind of Test** | Unit Test |
| **Linker Options** |  |
| **Source Root Directory** | S:\Components\Application\Autoliv |

| **Attributes** |  |
| --- | --- |
| **Name** | **Value** |
| CPU | Cortex-M4F |
| Endianess | little |
| InitObjDir | $(PROJECTROOT)\tessy\config\obj |
| InitSrcDir | $(PROJECTROOT)\tessy\config\src |
| Project File | $(PROJECTROOT)\tessy\config\eCS\_winIDEA.xjrf |
| Target Initialization File |  |
| Use Board Files | true |
|  |  |

| **Interface** | | |
| --- | --- | --- |
| **Element** | **Passing** | **Target Passing** |
| **External Functions** |  |  |
| uint32 Crc\_CalculateCRC32(const uint8 \*,uint32,uint32,boolean) |  |  |
| **Local Functions** |  |  |
| uint8 mcuif\_FtmSwTestRegCrc() |  |  |
| **Global Variables** |  |  |
| unsigned char mcuif\_CheckCrcOnFtmRegistersAutotest::u8NoOfParsedCrcRegisters#1 | IN |  |
| unsigned char mcuif\_CheckCrcOnFtmRegistersAutotest::u8PcrCrcStatusCounter#1 | IRRELEVANT |  |
| unsigned char mcuif\_CheckCrcOnFtmRegistersAutotest::u8PreviousCrcTestResult#1 | IRRELEVANT |  |
| unsigned char mcuif\_CheckCrcOnFtmRegistersAutotest::u8TestFailedThisCycle#1 | IRRELEVANT |  |
| unsigned char ku8FtmConfigRegisterMasks[17] | IRRELEVANT |  |
| unsigned int mcuif\_ku32FlexTimerRegistersArray[4] | IRRELEVANT |  |
| unsigned int mcuif\_u32FtmCrcInitDataOutput[4][17] | IRRELEVANT |  |
| unsigned int mcuif\_u32FtmCrcRuntimeOutput[17] | IRRELEVANT |  |
| unsigned int mcuif\_u32TempCrcValue | IRRELEVANT |  |
| unsigned char mcuif\_u8FtmCrcCalcQueuePort | IRRELEVANT |  |
| **Parameter** |  |  |
| unsigned char \* pu8TestResult | IN | INOUT |
|  |  |  |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Usercode**   |  | | --- | | **Stub Functions** | | **Crc\_CalculateCRC32**  $stub uint32 Crc\_CalculateCRC32(const uint8 \*,uint32,uint32,boolean) { $case 1.0 { return 0; } } | |  | |

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| |  |  | | --- | --- | | **Test Case 1** | **-** |    |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | |  |  | | **Test Step 1.1 (Repeat Count = 1)** |  |  | **Name** | **Input Value** | | --- | --- | | mcuif\_CheckCrcOnFtmRegistersAutotest::u8NoOfParsedCrcRegisters#1 | 15 | | pu8TestResult | target\_pu8TestResult | | target\_pu8TestResult | 0 |  | **Name** | **Expected Value** | | --- | --- | | target\_pu8TestResult | 1 | | |

|  |  |  |  |  |  |  |  |  |  |  |  |  |
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| | **Source File(s)** |  | | --- | --- | | **File** | |  |  |  |  | | --- | --- | --- | --- | | $(SOURCEROOT)\McuIf\Implementation\src\McuIf.c | Revision: 1.12 |  |  | | | **Compiler Options** | -DATOMICS\_USER\_MULTICORE\_CASE=0 -DATOMICS\_USE\_GENERIC\_IMPL=1 -DCANSM\_NO\_SYMBOLS\_WITHOUT\_PREFIX -DCOMM\_DONT\_PROVIDE\_LEGACY\_SYMBOLIC\_NAMES -DCOMPILERCFG\_EXTENSION\_MCAL\_FILE -DDISABLE\_MCAL\_INTERMODULE\_ASR\_CHECK -DECUM\_PROVIDE\_LEGACY\_SYMBOLIC\_NAMES -DKU8\_GLOBAL\_NUMBER\_OF\_SOLENOIDS=6 -DNM\_DONT\_PROVIDE\_LEGACY\_SYMBOLIC\_NAMES -DOSIARARM -DOS\_ARCH=OS\_CORTEXM -DOS\_CPU=OS\_S32K14X -DOS\_DEVELOPMENT\_SANITY\_CHECKS=0 -DOS\_INITCORE=0 -DOS\_KERNEL\_TYPE=OS\_SYSTEM\_CALL -DOS\_MEMMAP=0 -DOS\_N\_CORES=1 -DOS\_RELEASE\_SUFFIX=OS\_AS403 -DOS\_SCHEDULING\_ALGORITHM=OS\_CLZ\_QUEUE -DOS\_TOOL=OS\_iar -DOS\_USE\_TRACE=1 -DTS\_ARCH\_DERIVATE=TS\_S32K14X -DTS\_ARCH\_FAMILY=TS\_CORTEXM -D\_ICCARM\_\_ -I$(SOURCEROOT)\EFX\Implementation\inc -I$(SOURCEROOT)\EFX\Implementation\cfg -I$(SOURCEROOT)\FSM\Implementation\inc -I$(SOURCEROOT)\FSM\Implementation\cfg -I$(SOURCEROOT)\MIC\Implementation\inc -I$(SOURCEROOT)\MIC\Implementation\cfg -I$(SOURCEROOT)\NVP\Implementation\inc -I$(SOURCEROOT)\NVP\Implementation\cfg -I$(SOURCEROOT)\RCM\Implementation\inc -I$(SOURCEROOT)\RCM\Implementation\cfg -I$(SOURCEROOT)\SAD\Implementation\inc -I$(SOURCEROOT)\SAD\Implementation\cfg -I$(SOURCEROOT)\SBC\Implementation\inc -I$(SOURCEROOT)\SBC\Implementation\cfg -I$(SOURCEROOT)\STM\Implementation\inc -I$(SOURCEROOT)\STM\Implementation\cfg -I$(SOURCEROOT)\TL\_Lib\Implementation\inc -I$(SOURCEROOT)\TL\_Lib\Implementation\cfg -I$(SOURCEROOT)\AdcIf\Implementation\inc -I$(SOURCEROOT)\AdcIf\Implementation\cfg -I$(SOURCEROOT)\IoHwAb\Implementation\inc -I$(SOURCEROOT)\IoHwAb\Implementation\cfg -I$(SOURCEROOT)\BswMIf\Implementation\inc -I$(SOURCEROOT)\BswMIf\Implementation\cfg -I$(SOURCEROOT)\CompilerIf\Implementation\inc -I$(SOURCEROOT)\CompilerIf\Implementation\cfg -I$(SOURCEROOT)\DcmIf\Implementation\inc -I$(SOURCEROOT)\DcmIf\Implementation\cfg -I$(SOURCEROOT)\DemIf\Implementation\inc -I$(SOURCEROOT)\DemIf\Implementation\cfg -I$(SOURCEROOT)\DioIf\Implementation\inc -I$(SOURCEROOT)\DioIf\Implementation\cfg -I$(SOURCEROOT)\EcuMIf\Implementation\inc -I$(SOURCEROOT)\EcuMIf\Implementation\cfg -I$(SOURCEROOT)\McuIf\Implementation\inc -I$(SOURCEROOT)\McuIf\Implementation\cfg -I$(SOURCEROOT)\LPM\Implementation\inc -I$(SOURCEROOT)\LPM\Implementation\cfg -I$(SOURCEROOT)\NmIf\Implementation\inc -I$(SOURCEROOT)\NmIf\Implementation\cfg -I$(SOURCEROOT)\NvmIf\Implementation\inc -I$(SOURCEROOT)\NvmIf\Implementation\cfg -I$(SOURCEROOT)\OsIf\Implementation\inc -I$(SOURCEROOT)\OsIf\Implementation\cfg -I$(SOURCEROOT)\PlatformsIf\Implementation\inc -I$(SOURCEROOT)\PlatformsIf\Implementation\cfg -I$(SOURCEROOT)\PwmIf\Implementation\inc -I$(SOURCEROOT)\PwmIf\Implementation\cfg -I$(SOURCEROOT)\RteIf\Implementation\inc -I$(SOURCEROOT)\RteIf\Implementation\cfg -I$(SOURCEROOT)\SpiIf\Implementation\inc -I$(SOURCEROOT)\SpiIf\Implementation\cfg -I$(SOURCEROOT)\Startup\Implementation\inc -I$(SOURCEROOT)\Startup\Implementation\cfg -I$(SOURCEROOT)\..\Supplier\Tresos\_Configuration\_8.5.1\Workspace\Application\Tresos\_BSW\_generated\include -I$(SOURCEROOT)\..\Supplier\Tresos\_Configuration\_8.5.1\Workspace\Application\Tresos\_BSW\_generated -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Os\_TS\_T40D2M6I0R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Dem\_TS\_TxDxM5I18R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Dcm\_TS\_TxDxM4I14R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Com\_TS\_TxDxM6I3R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/ComM\_TS\_TxDxM5I17R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Crc\_TS\_TxDxM6I11R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Base\_TS\_TxDxM5I0R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/PduR\_TS\_TxDxM5I3R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/CanSM\_TS\_TxDxM3I6R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/CanIf\_TS\_TxDxM6I9R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/CanTp\_TS\_TxDxM6I8R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Atomics\_TS\_TxDxM1I0R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/BswM\_TS\_TxDxM1I14R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/IpduM\_TS\_TxDxM3I3R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Rte\_TS\_TxDxM6I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Compiler\_TS\_TxDxM1I0R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Compiler\_TS\_TxDxM1I0R0/include/CORTEXM -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Compiler\_TS\_TxDxM1I0R0/include/CORTEXM/iar -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Platforms\_TS\_T40D2M1I0R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Platforms\_TS\_T40D2M1I0R0/include/CORTEXM -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/MemIf\_TS\_TxDxM5I11R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/EcuM\_TS\_TxDxM5I14R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/NvM\_TS\_TxDxM6I17R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Fee\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Adc\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Can\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Dio\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Fls\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Mcl\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Mcu\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Port\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Pwm\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Resource\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Spi\_TS\_T40D2M10I2R0/include -Is:/Tools/EB\_BSW\_8.5.1/Workspace/plugins/Gpt\_TS\_T40D2M10I2R0/include -I$(SOURCEROOT)\ACC\Implementation\cfg -I$(SOURCEROOT)\ACC\Implementation\inc -I$(SOURCEROOT)\ATM\Implementation\inc -I$(SOURCEROOT)\CIL\Implementation\inc -I$(SOURCEROOT)\DIA\Implementation\inc -IC:\Projects\eCS\Phase\_01\View\_Development\_R1\_2\_1\Metrics\Static\_Analysis\Stubs -I$(SOURCEROOT)\..\Supplier\SCST\src\h -IC:\Program Files (x86)\IAR Systems\Embedded Workbench 8.3 EWARM FS 8.40.3\arm\inc\c | |  |  | |