|  |  |
| --- | --- |
| **Project** | DAI\_MMA |
| **Module** | MMG\_CyclesModes |
| **Test Object** | mmg\_UpdateModeStatus\_InhibitionOfCyclesByInternalFailure\_TensioningCycles |
| **Module UUID** | |  | | --- | | ed3f0367-43a5-49bc-b49b-c0271f8b190b | |
| **Test Object UUID** | |  | | --- | | 38f3f611-6744-44f1-9d10-9bb9648bb92a | |

|  |  |  |
| --- | --- | --- |
| **Instrumentation: Test object only** | | |
| **Statement (C0) Coverage** | 100 % |  |
| **Branch (C1) Coverage** | **83.33 %** |  |

**Statistics**

|  |  |  |
| --- | --- | --- |
| **Total Testcases** | 2 |  |
| **Successful** | 2 |  |
| **Failed** | 0 |  |
| **Not Executed** | 0 |  |

**Module Properties**

|  |  |
| --- | --- |
| **Project Root Directory** | C:\Projects\DAI\_MMA\Phase\_02\View\_Development\Tools\Tessy\Workspace |
| **Configuration File** | $(PROJECTROOT)\tessy\config\configuration.xml |
| **Target Environment** | GNU GCC Eclipse CDT (Default) |
| **Kind of Test** | Unit Test |
| **Linker Options** |  |
| **Source Root Directory** | C:\Projects\DAI\_MMA\Phase\_02\View\_Development\Components\Application\Autoliv |
| | **Source File(s)** |  | | --- | --- | | **File** | |  |  |  |  | | --- | --- | --- | --- | | $(SOURCEROOT)\MMG\Implementation\src\MMG\_CyclesModes.c | Revision: 1.1.9.16 |  |  | | | **Compiler Options** | -I$(SOURCEROOT)\MMG\Implementation\inc -I$(SOURCEROOT)\TL\_Lib\Implementation\inc -I$(SOURCEROOT)\..\Supplier\DaVinci\_generated -I$(SOURCEROOT)\..\Supplier\\_Common -I$(SOURCEROOT)\..\Supplier\Dem -I$(SOURCEROOT)\..\Supplier\Os -I$(SOURCEROOT)\..\Supplier\Dio -I$(SOURCEROOT)\..\Supplier\Det -I$(SOURCEROOT)\NVP\Implementation\inc | |  |  | | |

| **Notes** | |
| --- | --- |
| **Type** | **Text** |
| |  | | --- | | [PROBLEM] Test Object 'mmg\_UpdateModeStatus\_InhibitionOfCyclesByInternalFailure\_TensioningCycles' | | u8ModeStatusIsOk ia initialized with TRUE value just before the IF loop. ELSE statement unreachable. |
|  |  |

| **Interface** | | |
| --- | --- | --- |
| **Element** | **Passing** | **Target Passing** |
| **External Functions** |  |  |
| void ERH\_runGetAecStatus(u8AecIdentifierType,u8AecStatusType \*) |  |  |
| **External Variables** |  |  |
| unsigned long MMG\_u32ModesStatus | INOUT |  |
| unsigned long MMG\_u32ModesStatusComplement | INOUT |  |
|  |  |  |

|  |
| --- |
| **Fault Injections** |
| | **Fault Injection Test Cases** | | --- | | |  | | --- | | Test case 2: AAUT\_MMG\_0055 | | |
| |  |  |  | | --- | --- | --- | | |  | | --- | | **Branch Path: IF/ELSE** | | | | **Code (inserted before decision)** | u8ModeStatusIsOk == 0; | | **Applicable for** | |  | | --- | | Fault injection test case 2 | | |  |  | |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Usercode**   |  | | --- | | **Stub Functions** | | **ERH\_runGetAecStatus**  $stub void ERH\_runGetAecStatus(u8AecIdentifierType,u8AecStatusType \*) {  $case 1.1 {  \* pu8AecStatus = 2;  }  $case 1.2 {  \* pu8AecStatus = ERH\_KU8\_AEC\_QUALIFIED\_STATUS\_MASK;  }  $case 2.1 {  \* pu8AecStatus = 2;  }  $case 2.2 {  \* pu8AecStatus = ERH\_KU8\_AEC\_QUALIFIED\_STATUS\_MASK;  }  } | |  | |

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| |  |  | | --- | --- | | **Test Case 1: AAUT\_MMG\_0055** | Result: OK |  |  |  | | --- | --- | | **Specification** | COVERS: DSG\_MMG\_0039 | | **Description** | Check internal and voltage status | | **UUID** | 6bc6fe86-32ee-49c6-878b-ea9821436558 |      |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | |  |  | | **Test Step 1.1 (Repeat Count = 1): Internal and voltage status OK** |  | | |  |  | | --- | --- | | **Specification** | COVERS: DSG\_MMG\_0039 | | |      | **Name** | **Input Value** | | --- | --- | | MMG\_u32ModesStatus | 0 | | MMG\_u32ModesStatusComplement | 0 |      | **Name** | **Actual Value** | **Expected Value** | **Result** | | --- | --- | --- | --- | | MMG\_u32ModesStatus | 0 | 0 |  | | MMG\_u32ModesStatusComplement | 2097152 | 2097152 |  |  |  |  | | --- | --- | |  |  | | **Test Step 1.2 (Repeat Count = 1): Internal and voltage status NOK** |  |      | **Name** | **Input Value** | | --- | --- | | MMG\_u32ModesStatus | 0 | | MMG\_u32ModesStatusComplement | 0 |      | **Name** | **Actual Value** | **Expected Value** | **Result** | | --- | --- | --- | --- | | MMG\_u32ModesStatus | 2097152 | 2097152 |  | | MMG\_u32ModesStatusComplement | 0 | 0 |  | |  |  |  | | --- | --- | | **Test Case 2: AAUT\_MMG\_0055** | Result: OK |  |  |  | | --- | --- | | **Specification** | COVERS: DSG\_MMG\_0039 | | **Description** | Check internal and voltage status | | **UUID** | cca17d77-ff1a-4d20-9077-6f1c5ac02822 |      |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | |  |  | | **Test Step 2.1 (Repeat Count = 1): Internal and voltage status OK** |  | | |  |  | | --- | --- | | **Specification** | COVERS: DSG\_MMG\_0039 | | |      | **Name** | **Input Value** | | --- | --- | | MMG\_u32ModesStatus | 0 | | MMG\_u32ModesStatusComplement | 0 |      | **Name** | **Actual Value** | **Expected Value** | **Result** | | --- | --- | --- | --- | | MMG\_u32ModesStatus | 0 | 0 |  | | MMG\_u32ModesStatusComplement | 2097152 | 2097152 |  |  |  |  | | --- | --- | |  |  | | **Test Step 2.2 (Repeat Count = 1): Internal and voltage status NOK** |  |      | **Name** | **Input Value** | | --- | --- | | MMG\_u32ModesStatus | 0 | | MMG\_u32ModesStatusComplement | 0 |      | **Name** | **Actual Value** | **Expected Value** | **Result** | | --- | --- | --- | --- | | MMG\_u32ModesStatus | 2097152 | 2097152 |  | | MMG\_u32ModesStatusComplement | 0 | 0 |  | | |