**Autoliv Confidential**

|  |
| --- |
| **Distribution** |
| see [E3920297 - Project Configuration Item Management (PCIM)](https://plm.autoliv.int/linkto/latestreleased/alvProjectManagement/E3920297/*) |

**Software INTEGRATION Summary Report**

**Project:** **223748 VW AUI AU 416 EL SB ECU**

**SW Version: P25**

**HW Version :** C1

[1. Introduction 3](#_Toc63257757)

[1.1. Definitions and Abbreviations 3](#_Toc63257758)

[1.2. Strategy 3](#_Toc63257759)

[1.3. Test scope 4](#_Toc63257760)

[1.4. Product Identification 4](#_Toc63257761)

[1.5. Test environment 5](#_Toc63257762)

[1.6. Document References 5](#_Toc63257763)

[1.6.1. Input Documents (Customer Specifications, Autoliv Specifications) 5](#_Toc63257764)

[1.6.2. Output Documents (Test Specifications) 5](#_Toc63257765)

[2. Test Results 6](#_Toc63257766)

[2.1. Information of integration performed 6](#_Toc63257767)

[2.1.1. Tests Performed – ATM module 6](#_Toc63257768)

[2.1.2. Tests Performed – BFD module 8](#_Toc63257769)

[2.1.3. Tests Performed – BFE module 9](#_Toc63257770)

[2.1.4. Tests Performed – BFS module 10](#_Toc63257771)

[2.1.5. Tests Performed – BMM module 10](#_Toc63257772)

[2.1.6. Tests Performed – BPA module 11](#_Toc63257773)

[2.1.7. Tests Performed – BSW module 11](#_Toc63257774)

[2.1.8. Tests Performed – CIL module 11](#_Toc63257775)

[2.1.9. Tests Performed – DIA module 13](#_Toc63257776)

[2.1.10. Tests Performed – ECUM module 15](#_Toc63257777)

[2.1.11. Tests Performed – EOL module 15](#_Toc63257778)

[2.1.12. Tests Performed – ERH module 16](#_Toc63257779)

[2.1.13. Tests Performed – HWA module 17](#_Toc63257780)

[2.1.14. Tests Performed – MIC module 17](#_Toc63257781)

[2.1.15. Tests Performed – MMG module 17](#_Toc63257782)

[2.1.16. Tests Performed – OS module 18](#_Toc63257783)

[2.1.17. Tests Performed – PAL module 19](#_Toc63257784)

[2.1.18. Tests Performed – PMP module 21](#_Toc63257785)

[2.1.19. Tests Performed – PRO module 21](#_Toc63257786)

[2.1.20. Tests Performed – RCM module 21](#_Toc63257787)

[2.1.21. Tests Performed – RTE module 22](#_Toc63257788)

[2.1.22. Tests Performed – SCH module 22](#_Toc63257789)

[2.1.23. Tests Performed – SCM module 23](#_Toc63257790)

[2.1.24. Tests Performed – SFR module 23](#_Toc63257791)

[2.2. Global Summary 24](#_Toc63257792)

[3. Issues Verified 24](#_Toc63257793)

[3.1. Software Release Issue(s) Noted 24](#_Toc63257794)

[3.2. Other Issue(s) Noted 25](#_Toc63257795)

[3.3. Issue(s) closed 25](#_Toc63257796)

**EVOLUTION OF THE DOCUMENT**

| Version | Date | Author | Motive and nature of the modifications |
| --- | --- | --- | --- |
| 1.1 | 09-12-2019 | Aymeric Diankouika | Creation of the document for R1.1 SW release |
| 1.2 | 10-12-2019 | Aymeric Diankouika | Add history in document |
| 1.3 | 28-01-2020 | Aymeric Diankouika | Update list of tests in DIA |
| 1.4 | 12-08-2020 | Andrei Anca | Update document for project 223748 VW AUI AU 416 EL SB ECU |
| 1.5 | 17-12-2020 | Andrei Anca | Update document for project 223748 VW AUI AU 416 EL SB ECU |
| 1.6 | 03-02-2021 | Andrei Anca | Update document for project 223748 VW AUI AU 416 EL SB ECU |
| 1.7 | 03-08-2021 | Andrei Anca | Update document for project 223748 VW AUI AU 416 EL SB ECU |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

# 1. Introduction

This document provides managers, the Lead Software Verification Engineer, Design Leader, the software group and all affiliated technical support staff with the integration summary information for **223748 VW AUI AU 416 EL SB ECU** P25 SW release.

## Definitions and Abbreviations

SCH Scheduler

SW Software

SBE Seat Belt Electronic

SWPF Software Product Follow-Up

SwArchM Software Architecture Matrix

ATM Auto Test Manager

BPA Belt Parking Algorithm

DIA Diagnostic

HWA Haptic Warning

MMG Mode Management

OS Operating System

PAL Power Abstraction Layer

PMP Physical Measure Provider

RTE Real Time Environment

SFR Standard Function Recovery

## Strategy

Integration tests are be performed having the SW Architectural requirements as inputs. Tests will mainly check the behaviour of the software, both dynamic and static aspects of a function and metrics of a module.

Priority #1 modules tested:

- ATM  
 - AudiTr6\_SWarchitecture  
 - BFD  
 - BFE  
 - CIL  
 - EOL  
 - ERH  
 - MMG  
 - NmIf  
 - RTE  
 - OS

## Test scope

The purpose of the software integration tests is to verify, through testing, the software architecture requirements.

Test scope is defined in SDP (E3920154).

**CONCLUSION**

P30 SW Integration tests:

* Integration tests phase 1 (Platform to project integration) finished at 80% regarding the scope defined in SDP
* Integration tests phase 2 and 3 (minor and major project changes) finished at 60% regarding the scope defined in SDP

Results can be retrieved from SwArchM matrix.

## Product Identification

**Project Name:**  223748 VW AUI AU 416 EL SB ECU

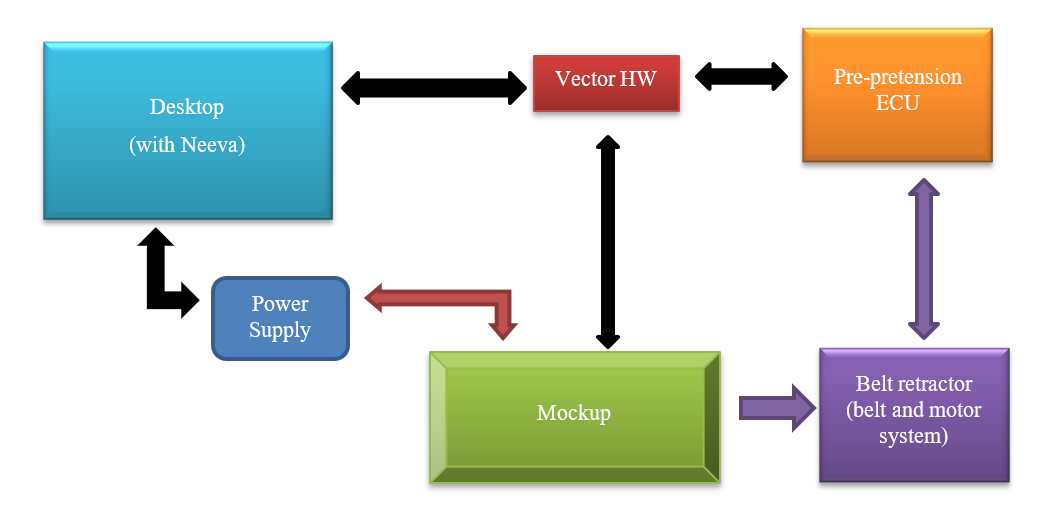
**Software Version:** P2

**Hardware Version:** C1

## Test environment

PP4G Test Environment will have the following equipment’s:

* Mock-Up / Proto
* Belt Retractor
* PC with Neeva
* CAN Case XL



## Document References

### Input Documents (Customer Specifications, Autoliv Specifications)

|  |  |  |
| --- | --- | --- |
| **Ref.** | **Document** | **Version** |
| 1 | E3920154 Audi Tranche 6 Software Development Plan.docx | E3920154 |

Notes:

### Output Documents (Test Specifications)

|  |  |  |
| --- | --- | --- |
| **Ref.** | **Document** | **Version** |
| Tests | e:/MKSProjects/SEB/PP/AUDI\_TR6/  Phase\_01/View\_Development/Tests/Tests\_Integration/Automatic\_Tests/Test\_Scenarios/ | 1.10 |
| Results | e:/MKSProjects/SEB/PP/AUDI\_TR6/  Phase\_01/View\_Development/Tests/Tests\_Integration/Automatic\_Tests/Test\_Reports/ | 1.10 |

# Test Results

## Information of integration performed

### Tests Performed – ATM

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_atm\_startseqcyclic\_ecuknockout\_timeout** | **SBE** | **OK** |  |
| **arch\_test\_atm\_run\_checkecuknockouttimeout** | **SBE** | **OK** |  |

***Number of tests: 2***  
***Number of positives tests: 2***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – AudiTr6\_SWarchitecture

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_ecu\_startup\_sequence** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_startup\_validation** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_after\_run** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_off** | **SBE** | **NOTRUN** |  |
| **arch\_test\_ecu\_run** | **SBE** | **OK** |  |
| **arch\_test\_transition\_from\_ecu\_run\_to\_ecu\_sleep** | **SBE** | **OK** |  |
| **arch\_test\_transition\_to\_ecu\_process\_mode** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_process\_mode** | **SBE** | **OK** |  |
| **arch\_test\_transition\_from\_ecu\_process\_mode\_to\_ecu\_run** | **SBE** | **OK** |  |
| **arch\_test\_transition\_from\_ecu\_after\_run\_to\_ecu\_run** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_postrun\_ecu\_sleep\_transition** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_postrun\_ecu\_stop\_transition** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_ecu\_stop\_state** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_stop\_ecu\_run\_transition** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_stop\_ecu\_postrun\_transition** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_stop\_ecu\_sleep\_transition** | **SBE** | **OK** |  |
| **arch\_test\_ecu\_sleep\_state** | **SBE** | **OK** |  |

***Number of tests: 17***  
***Number of positives tests: 16***  
***Number of negatives tests: 0***  
***Number of not run tests: 1***

### Tests Performed – BFD

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_bfd\_tesnioningcyclefromcanordiagrequest** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_abortcycles** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_bsrcyclefromcanordiagrequest** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_cil\_rungetactivationrequest** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_cil\_rungetreleaseallowed** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_crashsignalinhibitscycles** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_dia\_rungetdiagrequestcycle** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_eolcounterinhibit** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_eolcounterinhibitngu** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_factorymodeinhibitecycles** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_handlevehicledynamics** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_incorrecttensioning** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_incorrecttensioningexecution** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_inhibitecycles** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_initfunction** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_inpositionbeltdisplacement** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_inpositionhallsensorerror** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_inpositionmonitoringwindow** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_inpositionreference** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_outofposition** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_mainfunction\_periodicity\_10ms** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_mmg\_runcheckmodestatus** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_motorconnection** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_noinhibionsnoabortions** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_noinhibionsnoabortionsbsr** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_releasenoinhibionsnoabortions** | **SBE** | **OK** |  |
| **arch\_test\_bfd\_rungetpositionfromt0** | **SBE** | **OK** |  |

***Number of tests: 27***  
***Number of positives tests: 27***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – BFE

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_bfe\_runexecutestandardsteps** | **SBE** | **OK** |  |
| **arch\_test\_bfe\_velocitycontrolalgo** | **SBE** | **OK** |  |

***Number of tests: 2***  
***Number of positives tests: 2***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – CIL

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_cil\_statusframe\_rgs\_anf01** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_straffung\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_geteventstatus** | **SBE** | **OK** |  |
| **arch\_test\_cil\_receivesignal** | **SBE** | **OK** |  |
| **arch\_test\_cil\_haptic\_warning\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_crashabschaltung\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_bpdc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_hard\_release\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_01gatherinfo\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_01gatherinfo\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_statusframe\_rgs\_vl\_01** | **SBE** | **OK** |  |
| **arch\_test\_cil\_beltspeed\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_beltspeed\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_bobbindisplacement\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_bobbindisplacement\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_keinestraffungunterspg\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_nicht\_programmiert\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_uebertemperatur\_defekt\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_uebertemperatur\_defekt\_hwselfprotection\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_spiehwerror\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_spiehwerror\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_adcoutoforder\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_adcoutoforder\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_extwdgoutoforder\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_extwdgoutoforder\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_mosfethighsc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_mosfethighsc\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_mosfetlowsc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_mosfetlowsc\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_mosfetoc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_mosfetoc\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_hallsensor\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_hallsensor\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_temperaturesensorfailure\_vl** | **SBE** | **NOK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_temperaturesensorfailure\_vr** | **SBE** | **NOK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_enableoutoforder\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_enableoutoforder\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_highsideswitch\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_highsideswitch\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_adccalibration\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_batlogimplausible\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_batlogimplausible\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_bauteilfehler\_inhibit** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_max\_gurtlosezyklen\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_max\_gurtlosezyklen\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_temp\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_temp\_vr** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_unterspannung\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_max\_straffzyklenfehler\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_max\_haptikzyklenfehler\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_crashabschaltung\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_ueberspannung\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_motorfehler\_motorinsc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_motorfehler\_motorcurrent\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_motorfehler\_motordisconnected\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_motorfehler\_motorblocked\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_motorfehler\_motororder\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_eeprom\_ram\_rom\_fehler\_flsecc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_strom\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_noaction\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_bsr\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_incorrecttensioningexec10** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_incorrecttensioningexecution** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_incorrecttensioningrequest\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_status\_init** | **SBE** | **OK** |  |
| **arch\_test\_cil\_applitocan\_calledfunctions** | **SBE** | **OK** |  |
| **arch\_test\_cil\_applitocan\_readmotorcurrentina** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_eeprom\_ram\_rom\_fehler\_nvmecc\_vl** | **SBE** | **OK** |  |
| **arch\_test\_cil\_rgs\_anf\_01\_timeout** | **SBE** | **OK** |  |
| **arch\_test\_cil\_ecuknockouttimeout** | **SBE** | **OK** |  |
| **arch\_test\_cil\_ecuknockouttimeout\_aec\_notqualified** | **SBE** | **OK** |  |
| **arch\_test\_cil\_reset\_ecuknockout\_counter\_nvem\_12** | **SBE** | **OK** |  |
| **arch\_test\_cil\_shutdownlevel\_state** | **SBE** | **OK** |  |
| **arch\_test\_cil\_shutdownlevel\_0** | **SBE** | **OK** |  |
| **arch\_test\_cil\_shutdownlevel\_state\_at\_wakeup** | **SBE** | **OK** |  |
| **arch\_test\_cil\_timeout\_nvem\_12** | **SBE** | **OK** |  |
| **arch\_test\_cil\_reset\_ecuknockout\_counter\_kn\_airbag\_01** | **SBE** | **OK** |  |
| **arch\_test\_cil\_reset\_ecuknockout\_counter\_gpa\_01** | **SBE** | **OK** |  |
| **arch\_test\_cil\_reset\_ecuknockout\_counter\_oru\_01** | **SBE** | **OK** |  |
| **arch\_test\_cil\_reset\_ecuknockout\_counter\_clampcontrol\_01** | **SBE** | **OK** |  |
| **arch\_test\_cil\_reset\_ecuknockout\_counter\_radiofunc\_01** | **SBE** | **OK** |  |

***Number of tests: 81***  
***Number of positives tests: 79***  
***Number of negatives tests: 2***  
***Number of not run tests: 0***

### Tests Performed – EOL

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_eol\_runmainfunction\_precrash\_counter\_holding\_step** | **SBE** | **OK** |  |
| **arch\_test\_eol\_runmainfunction\_precrash\_counter** | **SBE** | **OK** |  |

***Number of tests: 2***  
***Number of positives tests: 2***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – ERH

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_erh\_ecuknockouttimeout** | **SBE** | **OK** |  |

***Number of tests: 1***  
***Number of positives tests: 1***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – MMG

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_mmg\_check\_ecuknockout\_input** | **SBE** | **OK** |  |

***Number of tests: 1***  
***Number of positives tests: 1***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – NmIf

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_nmif\_nminterfaces** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_repeatmessage** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_bussleep** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_after\_run\_abortion\_knockout\_error** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_tafter\_run\_currentconsumtion** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_bpa\_after\_run\_currentconsumtion** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_transition\_bpa\_stop\_mode\_bpa\_after\_run** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_shutdown\_level** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_sw\_ecu\_sleep\_state\_send\_frame\_wakeup\_range\_voltage** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_sw\_ecu\_sleep\_state\_no\_functional\_frame\_reception** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_shutdown\_level\_1** | **SBE** | **OK** |  |
| **arch\_test\_nmif\_bpa\_stop\_mode\_to\_low** | **SBE** | **OK** |  |

***Number of tests: 12***  
***Number of positives tests: 12***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – RTE

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_rte\_alv\_beltfunctionsexec\_function\_call\_order** | **SBE** | **OK** |  |

***Number of tests: 1***  
***Number of positives tests: 1***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – OS

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK/NOTRUN*** | ***Comments/ Issue tracking number*** |
| **arch\_test\_os\_checkostask\_alv\_autotests\_called\_rte\_defaultscheduletable** | **SBE** | **OK** |  |

***Number of tests: 1***  
***Number of positives tests: 1***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

## Global Summary

***Total number of tests: 147***

***Total number of positives tests: 144***

***Total number of negatives tests: 2***

***Total number of not run tests: 1***

# Issues Verified

## Software Release Issue(s) Noted

| Tracking Number | Description | Note |
| --- | --- | --- |
| **807538** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] Periodicity Mismatch for Run Autotests | | |  |
| **807568** | |  |  | | --- | --- | | |  | | --- | | AUDI\_TR6][P25][INT] Inputs mismatch for BFE\_runExecuteStandardSteps | | |  |
| **807569** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] The Motor stage internal status Not provided | | |  |
| **807570** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] Motor stage internal status Startup | | |  |
| **807571** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] BFE Get Power order not called | | |  |
| **807567** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] BFE\_runScheduleSteps  NVP Paramaters | | |  |
| **807554** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] Enable/DisableHBSMonitoring | | |  |
| **807613** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT]  Function BMM\_runGetPositionFromMinPos\_deg is never called | | |  |
| **807584** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] BMM\_runGetSpeed\_deg\_s is Not Reachable | | |  |
| **807582** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] Status of the detection of the pyro device firing | | |  |
| **807598** | |  |  | | --- | --- | | |  | | --- | | AUDI\_TR6][P25][INT] RAM ECC Event staus is not read in CIL\_runAppliToCAN | | |  |
| **807566** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] DIA\_WriteDataByIdentifier\_SessionChangeInit not reached | | |  |
| **807585** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] Generation of clock signal from a MCU externa | | |  |
| **807586** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] DIA\_runRidF780\_EnableDisableECLKStop shall stop the generation of clock signal | | |  |
| **807587** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] EnableDisableECLKRes shall provide urrent status of the external clock | | |  |
| **807588** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] DIA\_WriteDataByIdentifier\_Init not reached | | |  |
| **807589** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] The function Dcm\_Confirmation do not exist | | |  |
| **807590** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] CallBackDCMRequestServices\_CBReqServInd\_1\_StopProtocol was not developed | | |  |
| **807609** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] ECC Not Implemented | | |  |
| **807610** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] SCH Periodicity is not called corectly | | |  |
| **807592** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] DIA\_runClearDiagRequestCycle shall be called to acknowledge the releasing cycle | | |  |
| **807591** | |  |  | | --- | --- | | |  | | --- | | [AUDI\_TR6][P25][INT] The car cranking status signal from CIL shall be an input. | | |  |

## Other Issue(s) Noted

| Tracking Number | Description | Note |
| --- | --- | --- |
| **803992** | [AUDI\_TR6][P20][INT] Function BMM\_runGetPositionFromMinPos\_deg is never called | Issue Rejected |

## Issue(s) closed

| Tracking Number | Description | Note |
| --- | --- | --- |
| **803790** | [AUDI\_TR6][P20][INT] Functions wrong periodic call | Issue Closed |
| **803836** | [AUDI\_TR6][P20][INT] System Context is not saved when transitioning to Bootloader | Issue Closed |
| **803991** | [AUDI\_TR6][P20][INT] Startup functions wrong periodic call | Issue Closed |