**Autoliv Confidential**

|  |
| --- |
| **Distribution** |
| see [E3920297 - Project Configuration Item Management (PCIM)](https://plm.autoliv.int/linkto/latestreleased/alvProjectManagement/E3920297/*) |

**Software QUALIFICATION Summary Report**

**Project:** **223748 VW AUI AU 416 EL SB ECU**

**SW Version: P25**

**HW Version : B1**

[1. Introduction 3](#_Toc63332127)

[1.1. Definitions and Abbreviations 3](#_Toc63332128)

[1.2. Strategy 4](#_Toc63332129)

[1.3. Test scope 4](#_Toc63332130)

[1.4. Product Identification 4](#_Toc63332131)

[1.5. Test environment 5](#_Toc63332132)

[1.6. Document References 5](#_Toc63332133)

[1.6.1. Input Documents (Customer Specifications, Autoliv Specifications) 5](#_Toc63332134)

[1.6.2. Output Documents (Test Specifications) 5](#_Toc63332135)

[2. Test Results 6](#_Toc63332136)

[2.1. Information of performed tests for white box requirements 6](#_Toc63332137)

[2.1.1. Tests Performed – TF\_B 6](#_Toc63332138)

[2.1.2. Tests Performed – TF\_E 7](#_Toc63332139)

[2.1.3. Tests Performed – TF\_G 11](#_Toc63332140)

[2.1.4. Tests Performed – TF\_H 12](#_Toc63332141)

[2.1.5. Tests Performed – TF\_I 14](#_Toc63332142)

[2.1.6. Tests Performed – TF\_J 15](#_Toc63332143)

[2.1.7. Tests Performed – TF\_R 15](#_Toc63332144)

[2.2. Information of performed tests for Reviews requirements 17](#_Toc63332145)

[2.2.1. Tests Performed – TF\_B 17](#_Toc63332146)

[2.2.2. Tests Performed – TF\_E 17](#_Toc63332147)

[2.2.3. Tests Performed – TF\_G 18](#_Toc63332148)

[2.2.4. Tests Performed – TF\_H 18](#_Toc63332149)

[2.2.5. Tests Performed – TF\_I 20](#_Toc63332150)

[2.2.6. Tests Performed – TF\_J 21](#_Toc63332151)

[2.2.7. Tests Performed – TF\_N 21](#_Toc63332152)

[2.2.8. Tests Performed – TF\_R 22](#_Toc63332153)

[3. Issues Verified 29](#_Toc63332154)

[3.1. Software Release Issue(s) Verifed 29](#_Toc63332155)

[3.2. Other Issue(s) Noted 29](#_Toc63332156)

[3.3. Issue(s) closed 29](#_Toc63332157)

**EVOLUTION OF THE DOCUMENT**

| Version | Date | Author | Motive and nature of the modifications |
| --- | --- | --- | --- |
| 1.1 | 09-01-2020 | Aymeric Diankouika | Creation of the document for R1.1 SW release |
| 1.2 | 09-01-2020 | Aymeric Diankouika | Update TF-H tests list |
| 1.3 | 28-01-2020 | Aymeric Diankouika | Update TF-H tests list |
| 1.4 | 29-01-2020 | Aymeric Diankouika | Change a TF-H test name |
| 1.5 | 14-08-2020 | Dan Dustinta | Update summary report for AUDI\_TR6 |
| 1.6 | 03-02-2021 | Andreea Negrea | Update summary report for P25 |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

# 1. Introduction

This document provides managers, the Lead Software Verification Engineer, Design Leader, the software group and all affiliated technical support staff with the integration summary information for **223748 VW AUI AU 416 EL SB ECU** P20 SW release.

## Definitions and Abbreviations

SCH Scheduler

SW Software

SBE Seat Belt Electronic

SWPF Software Product Follow-Up

SwArchM Software Architecture Matrix

ATM Auto Test Manager

BPA Belt Parking Algorithm

DIA Diagnostic

HWA Haptic Warning

MMG Mode Management

OS Operating System

PAL Power Abstraction Layer

PMP Physical Measure Provider

RTE Real Time Environment

SFR Standard Function Recovery

## Strategy

Software White-box tests are performed having the Software requirements as inputs. Tests will mainly check the behaviour of the software, inputs values.

Priority #1 requirement tested:

- TF\_B  
 - TF\_E  
 - TF\_G  
 - TF\_H  
 - TF\_I  
 - TF\_J  
 - TF\_R

## Test scope

The purpose of the software qualification tests is to verify, through testing, the software requirements.

Test scope is defined in Software Qualification Plan ([E4069699](https://plm.autoliv.int/linkto/latestreleased/ElectronicsDesignManagement/E4069699/*)).

**CONCLUSION**

P025 SW White-box Qualification Tests:

* White box qualification tests phase finished at 74% regarding the scope defined in the Software Qualification Plan
* 6 PTC issue were created

Results can be retrieved from SRM ([E4275524](http://plm.autoliv.int/linkto/latest/ElectronicsDesignManagement/E4275524/*)).

## Product Identification

**Project Name:**  223748 VW AUI AU 416 EL SB ECU

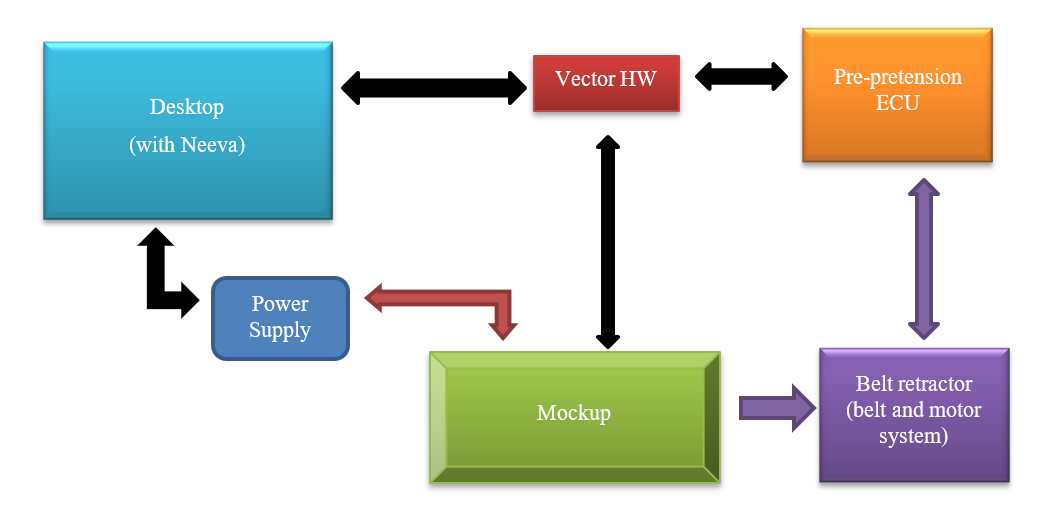
**Software Version:** P25

**Hardware Version:** B1

## Test environment

AUDI\_TR6 Test Environment will have the following equipment:

* Mock-Up / Proto
* Belt Retractor
* PC with Neeva
* CAN Case XL



## Document References

### Input Documents (Customer Specifications, Autoliv Specifications)

|  |  |  |
| --- | --- | --- |
| **Ref.** | **Document** | **Version** |
| 1 | **E4069699** - Increment SW Verification Plan | Rev006 |

Notes:

### Output Documents (Test Specifications)

|  |  |  |
| --- | --- | --- |
| **Ref.** | **Document** | **Version** |
| Tests | e:/MKSProjects/SBE/PP/AUDI\_TR6/Phase\_01/View\_Development/  Tests/Tests\_Qualification /Automatic\_Tests/Test\_Scenarios/ | VW\_SBE\_PP4G\_TR6\_P025\_P25\_01\_DEV |
| Results | e:/MKSProjects/SBE/PP/AUDI\_TR6/Phase\_01/View\_Development/  Tests/Tests\_Qualification /Automatic\_Tests/Test\_Reports/ | VW\_SBE\_PP4G\_TR6\_P025\_P25\_01\_DEV |

# Test Results

## Information of performed tests for white box requirements

### Tests Performed – TF\_B

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_B\_CAN\_FRAME\_ACKNOWLEDGEMENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2695\_RGS\_VL\_01\_SPI\_ERROR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2696\_RGS\_VL\_01\_ADC\_OUT\_OF\_ORDER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2697\_RGS\_VL\_01\_EXT\_WDG\_OUT\_OF\_ORDER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2704\_RGS\_VL\_01\_ADC\_CALIBRATION\_FAILED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2663\_RGS\_VL\_KEINESTRAFFUNGUEBERSPG** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_RGS\_VL\_TEMP** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_RGS\_VL\_TEMP\_SIGNAL\_MANAGED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2664\_RGS\_VL\_TEMP\_TEMPERATURE\_SENSOR\_FAILURE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_RHS\_VL\_EEPROM\_RAM\_ROM\_FEHLER\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_RGS\_VR\_TEMP** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_B\_2758\_RGS\_VR\_TEMP\_TEMPERATURE\_SENSOR\_FAILURE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_2827\_NM\_RGS\_VL** | **SBE** | **NOTRUN** |  |
| **QUALIF\_TEST\_TF\_B\_2827\_SIGNAL\_INIT** | **SBE** | **OK** |  |

***Number of tests: 14***  
***Number of positives tests: 12***  
***Number of negatives tests: 1***  
***Number of not run tests: 1***

### Tests Performed – TF\_E

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_E\_45\_DIAGSESSIONCONTROL\_ECU\_IGNORED\_VALUE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7167\_DIAGSESSIONCONTROL\_POSRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6013\_DIAGSESSIONCONTROL\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6016\_DIAGECURESET\_ECU\_IGNORED\_VALUE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6019\_DIAGECURESET\_ECU\_POSRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6025\_DIAGECURESET\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6553\_IGNORED\_VALUES** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_140\_POSITIVE\_RESPONSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7151\_READDATABYIDENTIFIER\_DIDF18B\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7150\_READDATABYIDENTIFIER\_DIDF18C\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7160\_READDATABYIDENTIFIER\_DIDFD00\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7149\_READDATABYIDENTIFIER\_DIDFD01\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7148\_READDATABYIDENTIFIER\_DIDFD03\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7147\_READDATABYIDENTIFIER\_DID4024\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_757\_RESET\_CAUSE\_OS\_EXCEPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_759\_SW\_INTERNAL\_WDG\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_760\_INTERNAL\_WATCHDOG\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_762\_PLL\_FAILURE\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_763\_RAM\_ECC\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_764\_ECC\_PFLASH\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_765\_INVALID\_INTERRUPT\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_766\_RESET\_CAUSE\_ECC\_UNIDENTIFIED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_767\_BTLD\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_768\_STACK\_OVERFLOW\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7660\_RAM\_DATA\_CORRUPTED\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_769\_RESET\_CAUSE\_E2P\_ECC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7659\_EXTERNAL\_WATCHDOG\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_770\_UNKNOWN\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7146\_READDATABYIDENTIFIER\_DIDFD08\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_809\_RESET\_CAUSE\_RESET\_CAUSE\_UNKNOWN** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_798\_RESET\_CAUSE\_CRYSTAL\_FAILURE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_800\_RESET\_CAUSE\_PLL\_FAILURE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_802\_RESET\_CAUSE\_ECC\_RAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_803\_RESET\_CAUSE\_ECC\_PFLASH** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_804\_RESET\_CAUSE\_OS\_EXCEPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_805\_RESET\_CAUSE\_INVALID\_INTERRUPT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_806\_RESET\_CAUSE\_ECC\_UNIDENTIFIED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7661\_RESET\_CAUSE\_ECC\_E2P** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_807\_RESET\_CAUSE\_STACK\_OVERFLOW** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_808\_RESET\_CAUSE\_EXTERNAL\_WDG** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7145\_READDATABYIDENTIFIER\_DIDFD09\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD10\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD11\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD12\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD13\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD14\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD15\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD16\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD17\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD18\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD19\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD20\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD21\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD22\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD23\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7144\_READDATABYIDENTIFIER\_DIDFD24\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7143\_READDATABYIDENTIFIER\_DIDFD2A\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7143\_READDATABYIDENTIFIER\_DIDFD2B\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7142\_READDATABYIDENTIFIER\_DIDFD2F\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_935\_936\_937\_938\_READ\_CALIBRATION\_ECU\_PARAMETERS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7141\_READDATABYIDENTIFIER\_DIDFD31\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_969\_READMOTORTEMPERATUREADAPTATIONPARAMETERS\_DIDFD34** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_971\_READMOTORTEMPERATUREADAPTATIONPARAMETERS\_DIDFD34** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7140\_READDATABYIDENTIFIER\_DIDFD34\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7139\_READDATABYIDENTIFIER\_DIDFD36\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7138\_READDATABYIDENTIFIER\_DIDFD37\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7136\_READDATABYIDENTIFIER\_DIDFD39\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7135\_READDATABYIDENTIFIER\_DIDFD3D\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7134\_READDATABYIDENTIFIER\_DIDFD3F\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7132\_READDATABYIDENTIFIER\_DIDFD41\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7131\_READDATABYIDENTIFIER\_DIDFD61\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7129\_READDATABYIDENTIFIER\_DIDFD83\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7128\_READDATABYIDENTIFIER\_DIDFE00\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7127\_READDATABYIDENTIFIER\_DIDFEFF\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7126\_READDATABYIDENTIFIER\_DIDFDFF\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6538\_READMEMORYBYADDRESS\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_6053\_SECURITYACCESS\_IGNORED\_VALUES** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7652\_SA\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7125\_WRITEDATABYIDENTIFIER\_DIDF18B\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7124\_WRITEDATABYIDENTIFIER\_DIDF18C\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7123\_WRITEDATABYIDENTIFIER\_DIDFDXX\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7121\_WRITEDATABYIDENTIFIER\_DIDFD2F\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7117\_WRITEDATABYIDENTIFIER\_DIDFD83\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7116\_WRITEDATABYIDENTIFIER\_DIDFEFF\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7114\_ECU\_LOCKUNLOCK\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7711\_F791\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7112\_F793\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6949\_DID\_F793\_PROCESSMODE\_ENTER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6951\_DID\_F793\_PROCESSMODE\_ENTER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6952\_DID\_F793\_PROCESSMODE\_EXIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6954\_DID\_F793\_PROCESSMODE\_EXIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7053\_DID\_F703\_START\_CYCLENUMBER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7053\_DID\_F703\_STOP\_CYCLENUMBER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6998\_DID\_F782\_NRC72** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7110\_F782\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_7109\_F780\_NEGRESP\_NO\_OTHER\_VALUE\_SEND\_BY\_ECU** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_6821\_DID\_F780\_ECLK\_NO\_HW** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_9072\_RIDFF00\_ERASE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_CONTROL\_DTC\_SETTING\_NRC31** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8110\_GURTSTRAFFER\_LINKS\_TEMPERATUR\_0X23E4** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8111\_GURTSTRAFFER\_LINKS\_TEMPERATUR\_0X23E4** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8112\_GURTSTRAFFER\_LINKS\_TEMPERATUR\_0X23E4** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8113\_GURTSTRAFFER\_LINKS\_TEMPERATUR\_0X23E4** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8135\_GURTSTRAFFER\_LINKS\_STROMAUFNAHME\_0X23E5** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8156\_GURTSTRAFFER\_LINKS\_VERSORGUNGSSPANNUNG\_0X23E6** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8157\_GURTSTRAFFER\_LINKS\_VERSORGUNGSSPANNUNG\_0X23E6** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8158\_0X23E6\_DATABYTE1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8180\_GURTSTRAFFER\_LINKS\_KLEMME\_30\_0X23E7** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8182\_0X23E7\_DATABYTE1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8751\_GURTSTRAFFER\_LINKS\_FEHLERSTATUS\_0X23F0** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8760\_GURTSTRAFFER\_LINKS\_FEHLERSTATUS\_0X23F0** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8768\_GURTSTRAFFER\_LINKS\_FEHLERSTATUS\_0X23F0** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8766\_GURTSTRAFFER\_LINKS\_FEHLERSTATUS\_0X23F0** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8765\_GURTSTRAFFER\_LINKS\_FEHLERSTATUS\_0X23F0** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8764\_GURTSTRAFFER\_LINKS\_FEHLERSTATUS\_0X23F0** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8331\_GURTSTRAFFER\_RECHTS\_TEMPERATUR\_0X23EC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8332\_GURTSTRAFFER\_RECHTS\_TEMPERATUR\_0X23EC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8333\_GURTSTRAFFER\_RECHTS\_TEMPERATUR\_0X23EC** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_8334\_GURTSTRAFFER\_RECHTS\_TEMPERATUR\_0X23EC** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_8357\_8408\_NEGATIVE\_CURRENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8359\_GURTSTRAFFER\_RECHTS\_STROMAUFNAHME\_0X23ED** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_8382\_GURTSTRAFFER\_RECHTS\_VERSORGUNGSSPANNUNG\_0X23EE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8383\_GURTSTRAFFER\_RECHTS\_VERSORGUNGSSPANNUNG\_0X23EE** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_8384\_0X23EE\_DATABYTE1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8407\_GURTSTRAFFER\_RECHTS\_KLEMME\_30\_0X23EF** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_E\_8409\_0X23EF\_DATABYTE1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8787\_GURTSTRAFFER\_RECHTS\_FEHLERSTATUS\_0X23F1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8796\_GURTSTRAFFER\_RECHTS\_FEHLERSTATUS\_0X23F1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8798\_GURTSTRAFFER\_RECHTS\_FEHLERSTATUS\_0X23F1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8801\_GURTSTRAFFER\_RECHTS\_FEHLERSTATUS\_0X23F1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8802\_GURTSTRAFFER\_RECHTS\_FEHLERSTATUS\_0X23F1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8803\_GURTSTRAFFER\_RECHTS\_FEHLERSTATUS\_0X23F1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_8584\_VW\_HARDWARE\_VERSION\_NUMBER\_0XF1A3** | **SBE** | **OK** |  |

***Number of tests: 133***  
***Number of positives tests: 126***  
***Number of negatives tests: 7***  
***Number of not run tests: 0***

### Tests Performed – TF\_G

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_G\_526\_TEMP\_SENSOR\_FAIL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_G\_560\_BFE\_RUNEXECUTESTANDARDSTEPS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_G\_783\_FROMDIAGNOSTICTOIDLE\_MODE** | **SBE** | **OK** |  |

***Number of tests: 3***  
***Number of positives tests: 3***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_H

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_H\_24\_25\_AUTOTESTS\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_27\_AUTOTEST\_STATUS** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_H\_34\_INHIBITED\_AUTOTESTS\_STATUS** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_H\_1799\_CONTEXT\_INHIBIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_85\_TEST\_INHIBIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_95\_SHUTDOWN\_INHIBIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_96\_SHUTDOWN\_INHIBIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_106\_STABLE\_BATTERY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_108\_UNSTABLE\_BATTERY** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_H\_143\_RESET\_EVALUATED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_145\_RUNRAMMEMORYCORRUPTION\_AUTOTEST\_READRESETCAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_149\_RUNRAMMEMORYCORRUPTION\_AUTOTEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2120\_RUNRAMMEMORYCORRUPTION\_AUTOTEST\_ECCRECORD** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_179\_RUNEEPROMMEMORYCORRUPTION\_AUTOTEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2121\_RUNEEPROMMEMORYCORRUPTION\_AUTOTEST\_ECCRECORD** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_240\_RUNPFLASHMEMORYCORRUPTION\_AUTOTEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_ADC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2127\_CHECKPOWERSUPPLYOV\_RUNNING** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2127\_CHECKPOWERSUPPLYOV\_STARTUP** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_H\_OVERVOLTAGE\_KL30\_LEVEL\_SAMPLED\_FILTERED\_VALUE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_325\_OVERVOLTAGE\_CURRENT\_QUALIFICATIONDESKILL\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_OVERVOLTAGE\_QUALIFICATION\_DESKILLING\_THRESHOLDS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_OVER\_VOLTAGE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_341\_OVERVOLTAGE\_KL30\_LEVEL\_MEASUREMENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2128\_CHECKPOWERSUPPLYUV\_RUNNING** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2128\_CHECKPOWERSUPPLYUV\_STARTUP** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_H\_UNDERVOLTAGE\_KL30\_LEVEL\_SAMPLED\_FILTERED\_VALUE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_364\_UNDERVOLTAGE\_CURRENT\_QUALIFICATIONDESKILL\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_367\_VOLTAGE\_BELOW\_THRESHOLD\_UNDERVOLTAGE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_370\_NORMAL\_RANGE\_AFTER\_UNDERVOLTAGE\_QUALIFIED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_UNDER\_VOLTAGE\_OUTSIDE\_TENSIONNING** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_381\_KL30\_SHALL\_MEASURE\_KL30** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_396\_AUTOTEST\_UNDERVOLTAGE\_PERIODICITY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_401\_AUTOTEST\_MEASUREBATTERYVOLTAGE\_PERIODICITY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_403\_INPUT\_AECBATTERYUV\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_406\_409\_THRESHOLD\_BELOW\_AND\_ABOVE\_IS\_UNDERVOLTAGE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_419\_KL30\_MEASUREMENT\_PERIODICITY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2129\_AUTOTEST\_LOGICALOV\_PERIODICITY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_LOGICAL\_OVER\_VOLTAGE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2158\_AUTOTEST\_LOGICALUV\_PERIODICITY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_LOGICAL\_UNDER\_VOLTAGE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_659\_AUTOTEST\_HWSELFPROTECTION\_PERIODICITY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_663\_HB\_N\_I\_REL\_CURRENTSENSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_664\_HB\_N\_I\_REL\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_666\_667\_HB\_N\_I\_REL\_CURRENTSENSE\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_680\_681\_SIGNALS\_MEASURED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_MOSFET\_SHORT\_CIRCUIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_MOSFET\_OPEN\_CIRCUIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_873\_CHECKMOTORCONNECTION\_RUNNING** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_877\_878\_880\_881\_INPUT\_CHECKMOTORCONNECTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_884\_INPUT\_NVP\_PARAMETER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_MOTOR\_CONNECTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_900\_901\_902\_903\_PAL\_AUTOTEST\_CHECKMOTORCONNECTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_1051\_MOTORTEMPERATURE\_AUTOTEST\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_1055\_1056\_1070\_INPUT\_MOTORTEMPERATURE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_1059\_1062\_INPUT\_NVP\_PARAMETERS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_HIGH\_SIDE\_SWITCH\_REGULATION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2162\_HS\_SW\_NULL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_2150\_AUTOTEST\_EXT\_WDG\_PERIODICITY100MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_HANDLE\_RESET\_CAUSES** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TO\_CHECK\_ADC\_CALIBRATION** | **SBE** | **OK** |  |

***Number of tests: 61***  
***Number of positives tests: 56***  
***Number of negatives tests: 5***  
***Number of not run tests: 0***

### Tests Performed – TF\_I

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_I\_EVENT\_MONITORING\_STATE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ADC\_OUT\_OF\_ORDER\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_MOSFET\_HIGH\_SC\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_MOSFET\_LOW\_SC\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_MOSFET\_OC\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_HALL\_SENSOR\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ENABLE\_OUT\_OF\_ORDER\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_HW\_SELF\_PROTECTION\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_MOTOR\_ORDER\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_HIGH\_SIDE\_SWITCH\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_WARM\_RESET\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_TEMPERATURE\_SENSOR\_FAILURE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_EXT\_WDG\_OUT\_OF\_ORDER\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_EXT\_MOTOR\_BLOCKED\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_EXT\_MOTOR\_IN\_SC\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_EXT\_MOTOR\_CURRENT\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_MOTOR\_DISCONNECTED\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_SW\_SELF\_PROTECTION\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_NVM\_NOT\_PRG\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_NVM\_INTEGRITY\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_RAM\_INTEGRITY\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_PFLASH\_INTEGRITY\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ERR\_FLS\_ECC\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_NVM\_E\_WRONG\_BLOCK\_ID** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_NVM\_QUEUE\_OVERFLOW** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_BAT\_PWR\_OVER\_VOLTAGE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_BAT\_PWR\_UNDER\_VOLTAGE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_BAT\_PWR\_UNDER\_VOLTAGE\_DURING\_TENS\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_BAT\_LOG\_OVER\_VOLTAGE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_BAT\_LOG\_UNDER\_VOLTAGE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_BAT\_LOG\_IMPLAUSIBLE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ERR\_CAN\_BUS\_OFF\_BSW\_LEVEL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ERR\_CAN\_BUS\_OFF\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_HIGH\_FORCE\_END\_OF\_LIFE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_LOW\_FORCE\_END\_OF\_LIFE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_COMFORT\_FORCE\_END\_OF\_LIFE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_HAPTIC\_FORCE\_END\_OF\_LIFE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ADC\_CALIBRATION\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ADC\_CALIBRATION** | **SBE** | **OK** |  |

***Number of tests: 39***  
***Number of positives tests: 39***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_J

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_J\_197\_WRITE\_SYSTEM\_CONTEXT\_BLOCK\_0\_INDEPENDENTLY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_197\_WRITE\_SYSTEM\_CONTEXT\_BLOCK\_1\_INDEPENDENTLY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_197\_WRITE\_SYSTEM\_CONTEXT\_BLOCK\_2\_INDEPENDENTLY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_89\_SYSTEM\_CONTEXT\_SAVE\_TIME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_206\_CHECK\_SYSTEM\_CONTEXT\_STORAGE\_PROGRAMMING\_SESSION** | **SBE** | **NOK** |  |
| **QUALIF\_TEST\_TF\_J\_208\_SYSTEM\_CONTEXT\_DETECT\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_212\_SYSTEM\_CONTEXT\_DEFAULT\_RECOVERY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_SYSTEM\_CONTEXT\_DETECT\_WARM\_RESET** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_214\_SYSTEM\_CONTEXT\_DETECT\_WARM\_RESET\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_216\_SYSTEM\_CONTEXT\_STORAGE\_TIME** | **SBE** | **NOK** |  |

***Number of tests: 10***  
***Number of positives tests: 8***  
***Number of negatives tests: 2***  
***Number of not run tests: 0***

### Tests Performed – TF\_R

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_R\_2399\_ACCELERATION\_STATUS\_RELEASE\_ALLOWED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_99\_MOTORACTIVATION\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2508\_CALL\_BPDC\_EVERY10MS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2500\_ACIVATE\_BPDO\_CONDITIONS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2501\_ACIVATE\_BPDC\_CONDITIONS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_149\_MOTOR\_ACTIVATION\_INHIBITION\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_MOTOR\_ACTIVATION\_INHIBITION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_177\_MOTOR\_ACTIVATION\_TENSIONING\_DIRECTION\_INHIBITION\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_MOTOR\_IN\_TENSIONING\_DIRECTION\_INHIBITION\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_310\_PRE\_PRE\_TENSIONING** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_437\_PRE\_PRE\_TENSIONING** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_551\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_805\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_987\_BPA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_BELT\_PARKING\_FUNCTION\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1258\_BPA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1293\_BPA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_BELT\_PARKING\_REQUEST\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1564\_BPDC\_REQUEST\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1598\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_HAPTIC\_WARNING\_FUNCTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1704\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1755\_PRO\_INHIBABORT\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1813\_SFR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_RELEASING\_FUNCTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1844\_PATINA\_REQUEST\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1876\_SFR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_1972\_BFS\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_SELECT\_BELT\_FUNCTION\_EXECUTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2110\_CYCLESCOUNTER\_PERIODICITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2628\_POSITIONRETAINED\_AFTER\_BSR** | **SBE** | **NOTRUN** |  |

***Number of tests: 31***  
***Number of positives tests: 30***  
***Number of negatives tests: 0***  
***Number of not run tests: 1***

## Global Summary for white box tests

***Total number of tests: 291***

***Total number of positives tests: 274***

***Total number of negatives tests: 15***

***Total number of not run tests: 2***

## Information of performed tests for Reviews requirements

### Tests Performed – TF\_B

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_B\_66\_73\_1439** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_78** | **SBE** | **NOK** | **800820** |
| **QUALIF\_TEST\_TF\_B\_1649** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_1653** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_1657** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_1663** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_CAN\_BUS\_OFF** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_CAN\_BUS\_OFF\_ERRORS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_CAN\_BUS\_OFF\_REGISTERS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_FRAME\_ACKNOWLEDGE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_BAUDRATE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_MAX\_BAUDRATE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_MEASUREMENT\_FRAME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_MEASUREMENT\_FRAME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_CAN\_DETECT\_FRAME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_CAN\_DETECT\_FAIL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_B\_MEMORYCONS** | **SBE** | **NOK** | **807629** |

***Number of tests: 17***  
***Number of positives tests: 15***  
***Number of negatives tests: 2***  
***Number of not run tests: 0***

### Tests Performed – TF\_D

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_D\_00001** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_D\_00002** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_D\_00003** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_D\_00004** | **SBE** | **OK** |  |

***Number of tests: 4***  
***Number of positives tests: 4***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_E

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_E\_DID\_23E5** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_E\_139\_149\_150** | **SBE** | **OK** |  |

***Number of tests: 2***  
***Number of positives tests: 2***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_G

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_G\_911** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_G\_915** | **SBE** | **OK** |  |

***Number of tests: 2***  
***Number of positives tests: 2***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_H

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_H\_15\_16** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_GENERIC\_MODULE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_BUS\_OFF\_INTERRUPT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_GETTING\_RESULTS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_STARTUP\_AUTOTESTS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_STARTUP\_AUTOTESTS\_FUNCTIONALITY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_SETUP** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_CRITICAL\_AUTOTESTS\_MANAGEMENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_AUTOTESTS\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_AUTOTESTS\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_AUTOTESTS\_EXEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_STARTUP\_AUTOTESTS\_EXEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_STARTUP\_AUTOTESTS\_EXEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_SC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_RAM\_NOT\_CRITICAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_NVM\_NOT\_CRITICAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_NVM\_NOTPORG\_NOT\_CRITICAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_NVM\_NOTPORG\_INPUTS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_NVM\_NOTPORG\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_NVM\_NOTPORG\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_PFLASH\_CRITICAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_PFLASH\_RESET\_CAUSE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_CRITICAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_UV\_DURING\_TENS\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HES\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HES\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HES\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOGIC\_OV\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOGIC\_OV\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOGIC\_UV\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOGIC\_UV\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOGIC\_UV\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_IMPL\_BATTERY\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_IMPL\_BATTERY\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_IMPL\_BATTERY\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HALF\_BRIDGE\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HALF\_BRIDGE\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HALF\_BRIDGE\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HALF\_BRIDGE\_SELF\_PROT\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_SC\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_SC\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_SC\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_OCAT\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_OCAT\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_BLOCKED\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_BLOCKED\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_BLOCKED\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_CURRENT\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_CURRENT\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_CURRENT\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_TEMP\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_PWM\_ORDER\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_PWM\_ORDER\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_PWM\_ORDER\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_IN\_SC\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_IN\_SC\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_IN\_SC\_COMPUTED\_DATA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOTOR\_IN\_SC\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGH\_SIDE\_SWITCH\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGH\_SIDE\_SWITCH\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGH\_SIDE\_SWITCH\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_EXT\_WDG** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_RESET\_CAUSE\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_RESET\_CAUSE\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TEMP\_SENS\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TEMP\_SENS\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_TEMP\_SENS\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGHF\_EOL\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOWF\_EOL\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HWA\_EOL\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_BUSS\_OFF\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_ONE\_SHOT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HALF\_BRIDGE\_ONE\_SHOT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_MOSFET\_SC\_ONE\_SHOT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_RAM\_INTEGRITY\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_EEPROM\_INTEGRITY\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_PFLASH\_INTEGRITY\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGHF\_EOL\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGHF\_EOL\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HIGHF\_EOL\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOWF\_EOL\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOWF\_EOL\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_LOWF\_EOL\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_CONFORT\_EOL\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_CONFORT\_EOL\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_CONFORT\_EOL\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HAPTIC\_EOL\_TYPE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HAPTIC\_EOL\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_HAPTIC\_EOL\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_CONFORT\_EOL\_TEST** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_H\_ADC\_CALIB\_NOT\_CRITICAL** | **SBE** | **OK** |  |

***Number of tests: 94***  
***Number of positives tests: 94***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_I

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_I\_DEM\_EVENT\_ID** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_AECS\_IDENTIFIER** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_LINKED\_AUTOTESTS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_AEC\_DEFINITIONS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ERR\_ADC\_OUT\_OF\_ORDER\_QALIF** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_I\_ERR\_TEMP\_SENSOR\_FAIL\_QALIF** | **SBE** | **OK** |  |

***Number of tests: 6***  
***Number of positives tests: 6***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_J

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_J\_CONTEXT\_TIME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_DEFICIENCY** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_BATTERY\_LOSS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_BATTERY\_LOSS\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_EXECUTED\_CYCLE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_EXECUTED\_CYCLE\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_ECPL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_ECPL\_SIZE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_ECPL\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_CHECKSUM\_SIZE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_CHECKSUM\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_BLOCK\_SIZE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_BLOCKS\_NAME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_BLOCKS\_ADDRESSES** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_J\_BLOCKS\_VALUES** | **SBE** | **OK** |  |

***Number of tests: 15***  
***Number of positives tests: 15***  
***Number of negatives tests: 0***  
***Number of not run tests: 0***

### Tests Performed – TF\_N

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_N\_SIG\_BELT\_DIRECTION** | **SBE** | **NOK** | **804004** |
| **QUALIF\_TEST\_TF\_N\_BOBBIN\_PERIMETER** | **SBE** | **NOK** | **804004** |
| **QUALIF\_TEST\_TF\_N\_NO\_OF\_POLES** | **SBE** | **NOK** | **804004** |
| **QUALIF\_TEST\_TF\_N\_BELT\_SPEED\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_ECPL\_CONFIG** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_BELT\_POSITION\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_BELT\_BLOCKED\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_BELT\_SPEED\_THRESHOLD** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_PYRO\_DATA\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_PYRO\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_HES\_INHIB\_DESIGN\_CONSTR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_N\_HES\_GENERATE\_PULSES** | **SBE** | **OK** |  |

***Number of tests: 12***  
***Number of positives tests: 9***  
***Number of negatives tests: 3***  
***Number of not run tests: 0***

### Tests Performed – TF\_R

|  |  |  |  |
| --- | --- | --- | --- |
| ***Test Identification*** | ***Tester*** | ***OK / NOK*** | ***Comments/ Issue tracking number*** |
| **QUALIF\_TEST\_TF\_R\_25** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_38** | **SBE** | **NOK** | **800848** |
| **QUALIF\_TEST\_TF\_R\_44** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_45** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_53** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_70** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_76** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_90** | **SBE** | **NOK** | **800850** |
| **QUALIF\_TEST\_TF\_R\_91** | **SBE** | **NOK** | **800851** |
| **QUALIF\_TEST\_TF\_R\_92** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_93** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2398\_ACC\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2419\_CYCLE\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2420\_BSR\_CYCLE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2476\_HAPTIC\_REPETITION\_TIME** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2478\_HAPTIC\_REPETITION\_COUNTER\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2479\_HARD\_RELEASE\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2485\_BPA\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2488\_DOOR\_OPEN\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2499\_BUCKLE\_OPEN\_STATUS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2527\_DOOR\_OPEN\_REQ** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2528\_DOOR\_CLOSE\_REQ** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2494\_NO\_CRANK** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2495\_VEH\_SPEED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2496\_VEH\_SPEED\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2497\_NO\_CRANK** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2498\_NO\_CRANK** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2529\_DOOR\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2530\_DOOR\_EVENT\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2531\_BUCKLE\_EVENT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2532\_BUCKLE\_EVENT\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_2533\_COMPUTED\_BPA\_EVENTS** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_EOLIFE\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_ENABLE\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_EXECUTED\_CYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_SPRING\_STIFF\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CAR\_CRASH\_STIFF\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CRANK\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ECU\_MODE\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LAST\_CYCLE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_NEW\_CYCLE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_NEW\_CYCLE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_INHIB\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_INHIB\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_ABORT\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_ABORT\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_INPUT\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_IGNORE\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPA\_REQ\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPA\_REQ\_ABORT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPA\_BPDA\_REQ** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_MOTOR\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_MOTOR\_DIRECTION\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_MOTOR\_BLOCKED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_OV\_AEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_HES\_AEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_END\_OF\_LIFE\_AEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_ENABLE\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_VEH\_SPEED\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_SPRING\_STIFF\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_CAR\_CRASH\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CRANK\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_FACTORY\_MODE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_SPEED\_THRESHOLD\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_INHIB\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_INHIB\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_ABORT\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_ABORT\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_CRITICAL\_AT\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_CRITICAL\_AT\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDC\_INHIB\_STATUS\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_INPUT\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPA\_REQ\_INHIB** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPA\_REQ\_ABORT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPA\_BPDC\_REQ** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_MOT\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_MOT\_DIR\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_OV\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_ENDOFLIFE\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_HES\_AEC\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_ENABLE\_PARAM** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_ENABLE\_SIG\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_BUCKLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_CAR\_CRASH\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_CRANK\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_FACT\_MODE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_EXT\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_EXT\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_INHIB\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_INHIB\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_ABORT\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_ABORT\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_REQ\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_INPUT\_HWA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_IGNORE\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_NEW\_CYCLE END\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CURRENT\_CYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LAST\_CYCLE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_1\_REQ\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HWA\_2\_REQ\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_MOTOR\_ACTIV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_MOTOR\_DIR\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_OV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_DIAGONCAN\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_IGNORE\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_INHIB\_STATUS\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CURRENT\_EXECUTED\_CYCLE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_CYCLE1\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRO\_CYCLE2\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_MOTOR\_ACTIV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_AUTOTESTNAME\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R REL\_HWPROTECTION\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_TIME\_COUNTER\_INHIB INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_INHIB\_STATUS\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ANTIPATINA\_DIAGONCAN\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_IGNORE\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ANTIPATINA\_INHIB\_STATUS\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CURRENT\_EXECUTED\_CYCLE** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ANTIPATINA\_NEWSTARTEDCYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ANTIPATINA\_CYCLE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ANTIPATINA\_CYCLE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MOT\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_ACC\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_INHIB\_STATUS\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_CYCLE\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_HALL AEC\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_NEWSTARTEDCYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_ENDEDCYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_ANTIPATINAREQUEST\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_FOLLOWINGCYCLE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_PENDINGHARDRELEASE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_PENDINGSMOOTHRELEASE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_PENDINGBACKUPRELEASE\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_HARDRELEASE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_SMOOTHRELEASE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_REL\_BACKUPRELEASE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE2** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE3** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE4** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE5** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE6** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE7** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_CYCLE8** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_BPDO** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_BPDC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_HWA1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_HWA2** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_PRO1** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_PRO2** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_HARD\_REL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_SMOOTH\_REL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_BACKUP\_REL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_EXEC\_ANTIPATINA** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_CURRENT EXEC** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BELT\_FUNC\_CYCLE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CYCLE\_DEFAULT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_NEW\_CYCLE\_START\_CYCLE\_COUNTER\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_NEW\_ALIVE\_PARAM\_CYCLE\_COUNTER\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CYCLE\_COUNTER\_HIGH\_FORCE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CYCLE\_COUNTER\_LOW\_FORCE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CYCLE\_COUNTER\_COMFORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CYCLE\_COUNTER\_HAPTIC\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ADC\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HIGH\_SIDE\_SWITCH\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HIGH\_MOSFET\_HIGH\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HIGH\_MOSFET\_LOW\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HIGH\_MOTOR\_CONNECTION\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HIGH\_MOTOR\_SHORT\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_COMMAND\_CONST\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LOGIC\_UVT\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MOTOR\_CURRENT\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MOTOR\_POWER\_ORDER\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_HW\_SELF\_PROT\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_SW\_THERMAL\_PROT\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_UNDERVOLTAGE\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_UV\_DURING\_TENS\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LOG\_IMPLAUS\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BUSS\_OFF\_AEC\_STATUS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ENDOFLIFE\_COUNTERS\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_MOTOR\_ACTIV\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_MOTOR\_DIR\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_PS\_OV\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_EOL AEC\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_PRE\_PARAM\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_ABORT\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_EXT\_INHIB\_CAR\_CRASH\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_EXT\_INHIB\_FACTORY\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_INT LOW\_FORCE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_EXT\_LOW\_FORCE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_ABORT\_INT LOW\_FORCE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_ABORT\_EXT\_LOW\_FORCE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_REQ\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_REQ\_INPUT\_IGNORE\_CORRUPTED** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LOW FORCE\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LOW FORCE\_INT INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LOW FORCE\_ABORT\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_LOW FORCE\_INT\_ABORT\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CURRENT\_EXECUTED\_CYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CURRENT\_EXECUTED\_CYCLE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_PARAM\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR MOTOR\_ACTIV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR MOTOR\_DIRECTION\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_OV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_HES\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_EOL\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_CAR\_CRASH\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_FACTORY\_MODE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_EXT\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_EXT\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_INHIB\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_INHIB\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_I\_ABORT\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_MANAGE\_E\_ABORT\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_INHIB\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_ABORT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DIAGONCAN\_INPUT\_BSR** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_DIAGONCAN\_IGNORE\_CORRUPTION** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_INT\_INHIB\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR\_CURRENT CYCLE\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BSR CYCLE\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_MOTOR\_ACTIV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_MOTOR\_DIR\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_BLOCK\_BELT\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_OV\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BPDO\_HES\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_PRE\_TRIGG\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_ACCEL\_STATUS\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_CAN\_TRIGG\_OFF\_STATUS\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BUCKLE\_SWITCH\_INT INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_BUCKLE\_SWITCH\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_SEATBELT\_EVENT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_SEATBELT\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DOOR\_SWITCH\_SIG\_INPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_RPREV\_DOOR\_INTERNAL** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_RPREV\_DOOR\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DOOR\_EVENT\_OUTPUT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_DOOR\_INIT** | **SBE** | **OK** |  |
| **QUALIF\_TEST\_TF\_R\_COUNT\_EXEC\_CYCLES\_OUTPUT** | **SBE** | **OK** |  |

***Number of tests: 267***  
***Number of positives tests: 264***  
***Number of negatives tests: 3***  
***Number of not run tests: 0***

## Global Summary for Reviews requirements tests

***Total number of tests: 419***

***Total number of positives tests: 411***

***Total number of negatives tests: 8***

***Total number of not run tests: 0***

# Issues Verified

## Software Release Issue(s) Verifed

| Tracking Number | Description | Status |
| --- | --- | --- |
| - | |  |  | | --- | --- | | |  | | --- | | - | | | - |

## Other Issue(s) Noted

| Tracking Number | Description | Status |
| --- | --- | --- |
| - | |  |  | | --- | --- | | |  | | --- | | - | | | - |

## Issue(s) closed

| Tracking Number | Description | Note |
| --- | --- | --- |
| - | |  |  | | --- | --- | | |  | | --- | | - | | | - |