**Autoliv Confidential**

|  |  |
| --- | --- |
| DISTRIBUTION : | |
| Firm | To |
| AEE-C | Design Leader: *Pavel Borchescu* |
| AEE-C | Project Team Leader: *Daniel Andris* |
| AEE-C | Software line Manager: *Mihai Ianos* |
|  |  |

**Software Development UNIT Tests**

**Summary Report**

**Project: AUDI TR6**

**SW Version: P030**

[1. Introduction 3](#_Toc48156344)

[1.1. Definitions and Abbreviations 3](#_Toc48156345)

[1.2. Test scope 3](#_Toc48156346)

[1.3. Product Identification 4](#_Toc48156347)

[1.4. Document References 4](#_Toc48156348)

[1.4.1. Input Documents (Customer Specifications, Autoliv Specifications) 4](#_Toc48156349)

[1.4.2. Output Documents (Test Docs) 6](#_Toc48156350)

[2. Test Results 8](#_Toc48156351)

[2.1. Information of verification performed 8](#_Toc48156352)

# 1. Introduction

## Definitions and Abbreviations

SW Software

HW Hardware

PP Pre-Pretensionner

## Test scope

All modules included in the P030 scope, as defined in the unit test strategy for each release (see Software Development Plan).

## Product Identification

**Project Name:** AUDI\_TR6

**Software Version:** P030

## Document References

### Input Documents (Customer Specifications, Autoliv Specifications)

|  |  |  |
| --- | --- | --- |
| **Ref.** | **Document** | **Path** |
| 1 | AdcIf Design Documentation | [AdcIf/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/AdcIf/Design/project.pj) |
| 2 | ATM Design Documentation | [ATM/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/ATM/Design/project.pj) |
| 3 | BFD Design Documentation | [BFD/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BFD/Design/project.pj) |
| 4 | BFE Design Documentation | [BFE/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BFE/Design/project.pj) |
| 5 | BFS Design Documentation | [BFS/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BFS/Design/project.pj) |
| 6 | BMM Design Documentation | [BMM/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BMM/Design/project.pj) |
| 7 | BPA Design Documentation | [BPA/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BPA/Design/project.pj) |
| 8 | BswMIf Design Documentation | [BswMIf/Design/project.pj](http://alva-mks01.alv.autoliv.int:7001/si/viewrevision?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/BswMIf/Design/project.pj&selection=BswMIf%20-%20Detailed%20Design%20Document.docx&revision=:member) |
| 9 | CIL Design Documentation | [CIL/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/CIL/Design/project.pj) |
| 10 | DcmIf Design Documentation | [DcmIf/Design/project.pj](http://alva-mks01.alv.autoliv.int:7001/si/viewrevision?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/DcmIf/Design/project.pj&selection=DcmIf%20-%20Detailed%20Design%20Document.docx&revision=:member) |
| 11 | DemIf Design Documentation | [DemIf/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/DemIf/Design/project.pj) |
| 12 | DIA Design Documentation | [DIA/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/DIA/Design/project.pj) |
| 13 | EOL Design Documentation | [EOL/Design/project.pj](http://alva-mks01.alv.autoliv.int:7001/si/viewrevision?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/EOL/Design/project.pj&selection=EOL-Detailed%20Design%20Document.docx&revision=:member) |
| 14 | ERH Design Documentation | [ERH/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/ERH/Design/project.pj) |
| 15 | HWA Design Documentation | [HWA/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/HWA/Design/project.pj) |
| 16 | MIC Design Documentation | [MIC/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/MIC/Design/project.pj) |
| 17 | MMG Design Documentation | [MMG/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/MMG/Design/project.pj) |
| 18 | NmIf Design Documentation | NmIf/Design/project.pj |
| 19 | NvMIf Design Documentation | [NvMIf/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/NvMIf/Design/project.pj) |
| 20 | PAL Design Documentation | [PAL/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/PAL/Design/project.pj) |
| 21 | PMP Design Documentation | [PMP/Design/project.pj](http://alva-mks01.alv.autoliv.int:7001/si/viewrevision?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/PMP/Design/project.pj&selection=PMP%20-%20Detailed%20Design%20Document.docx&revision=:member) |
| 22 | PortIf Design Documentation | [PortIf/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/PortIf/Design/project.pj) |
| 23 | PRO Design Documentation | [PRO/Design/project.pj](http://alva-mks01.alv.autoliv.int:7001/si/viewrevision?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/PRO/Design/project.pj&selection=PRO%20-%20Detailed%20Design%20Document.docx&revision=:member) |
| 24 | RCM Design Documentation | [RCM/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/RCM/Design/project.pj) |
| 25 | SCM Design Documentation | [SCM/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/SCM/Design/project.pj) |
| 26 | SFR Design Documentation | [SFR/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/SFR/Design/project.pj) |
| 27 | SpiIf Design Documentation | [SpiIf/Design/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/SpiIf/Design/project.pj) |

### Output Documents (Test Docs)

|  |  |  |
| --- | --- | --- |
| **Ref.** | **Document** | **Path** |
| 1 | AdcIf\_UnitTestReport.xslx | [AdcIf/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/AdcIf/Tests/project.pj) |
| 2 | ATM\_UnitTestReport.xslx | [ATM/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/ATM/Tests/project.pj) |
| 3 | BFD\_UnitTestReport.xslx | [BFD/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BFD/Tests/project.pj) |
| 4 | BFE MBD Tests | [BFE/Design/MBD/MBD\_Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BFE/Design/MBD/MBD_Tests/project.pj) |
| 5 | BFS MBD Tests | [BFS/Design/MBD/MBD\_Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BFS/Design/MBD/MBD_Tests/project.pj) |
| 6 | BMM\_UnitTestReport.xslx | [BMM/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BMM/Tests/project.pj) |
| 7 | BPA MBD Tests | [BPA/Design/MBD/MBD\_Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/BPA/Design/MBD/MBD_Tests/project.pj) |
| 8 | BswMIf\_UnitTestReport.xslx | [BswMIf/Tests/project.pj&revision=:member](http://alva-mks01.alv.autoliv.int:7001/si/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/BswMIf/Tests/project.pj&revision=:member) |
| 9 | CIL\_UnitTestReport.xslx | [CIL/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/CIL/Tests/project.pj) |
| 10 | DcmIf\_UnitTestReport.xslx | [DcmIf/Tests/project.pj&revision=:member](http://alva-mks01.alv.autoliv.int:7001/si/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/DcmIf/Tests/project.pj&revision=:member) |
| 11 | DemIf\_UnitTestReport.xslx | [DemIf/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/DemIf/Tests/project.pj) |
| 12 | DIA\_UnitTestReport.xslx | [DIA/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/DIA/Tests/project.pj) |
| 13 | EOL Design Documentation | [EOL/Tests/project.pj](http://alva-mks01.alv.autoliv.int:7001/si/viewrevision?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/EOL/Tests/project.pj&selection=EOL_UnitTestReport.xlsx&revision=:member) |
| 14 | ERH\_UnitTestReport.xslx | [ERH/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/ERH/Tests/project.pj) |
| 15 | HWA MBD Tests | [HWA/Design/MBD/MBD\_Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/HWA/Design/MBD/MBD_Tests/project.pj) |
| 16 | MIC\_UnitTestReport.xslx | [MIC/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/MIC/Tests/project.pj) |
| 17 | MMG\_UnitTestReport.xslx | [MMG/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/MMG/Tests/project.pj) |
| 18 | NvMIf\_UnitTestReport.xslx | [NvMIf/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/NvMIf/Tests/project.pj) |
| 19 | NmIf\_UnitTestReport.xslx | [NmIf/Tests/unittest/project.pj&revision=:member](http://alva-mks01.alv.autoliv.int:7001/si/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/NmIf/Tests/unittest/project.pj&revision=:member) |
| 20 | PAL\_UnitTestReport.xslx | [PAL/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/PAL/Tests/project.pj) |
| 21 | PMP\_UnitTestReport.xslx | [PMP/Tests/unittest/project.pj&revision=:member](http://alva-mks01.alv.autoliv.int:7001/si/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/PMP/Tests/unittest/project.pj&revision=:member) |
| 22 | PortIf\_UnitTestReport.xslx | [PortIf/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/PortIf/Tests/project.pj) |
| 23 | PRO MBD Tests | [PRO/Design/MBD/project.pj&revision=:member](http://alva-mks01.alv.autoliv.int:7001/si/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Development/Components/Application/PRO/Design/MBD/project.pj&revision=:member) |
| 24 | RCM\_UnitTestReport.xslx | [RCM/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/RCM/Tests/project.pj) |
| 25 | SCM\_UnitTestReport.xslx | [SCM/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/SCM/Tests/project.pj) |
| 26 | SFR Design Documentation | [SFR/Design/MBD/MBD\_Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/SFR/Design/MBD/MBD_Tests/project.pj) |
| 27 | SpiIf\_UnitTestReport.xslx | [SpiIf/Tests/project.pj](http://alva-mks01:7001/siro/viewproject?projectName=e:/MKSProjects/SBE/PP/AUDI_TR6/Phase_01/View_Release/Components/Application/SpiIf/Tests/project.pj) |

# Test Results

## Information of verification performed

### P030 Tests

|  |  |  |
| --- | --- | --- |
| **TC** | **Result** | **Requirements** |
| **ATM\_Cfg** |  |  |
| **ATM\_cbk\_RGS\_ANF01\_Timeout\_TC** | PASSED | DSG\_ATM\_0003 |
| **RCM\_cbk\_RunResetCause\_Autotest\_TC** | PASSED | DSG\_ATM\_0003 |
| **RCM\_cbk\_ExtWatchdogTest\_Autotest\_TC** | PASSED | DSG\_ATM\_0003 |
| **AAUT\_ATM\_Cfg\_CyclicTests** | PASSED | DSG\_ATM\_0003 |
| **AAUT\_ATM\_Cfg\_StartupTests** | PASSED | DSG\_ATM\_0002 |
| **ATM** |  |  |
| **atm\_SetDemEvent\_TC** | PASSED | DSG\_ATM\_0009 |
| **BFD\_exec** |  |  |
| **AAUT\_BFD\_EXEC\_0001** | OK | DSG\_BFD\_0001; DSG\_BFD\_0002 |
| **AAUT\_BFD\_EXEC\_0002** | OK | DSG\_BFD\_0003 |
| **AAUT\_BFD\_EXEC\_0003** | OK | DSG\_BFD\_0004 |
| **AAUT\_BFD\_EXEC\_0004** | OK | DSG\_BFD\_0005; DSG\_BFD\_0006 |
| **AAUT\_BFD\_EXEC\_0005** | OK | DSG\_BFD\_0007 |
| **BFD\_inhib** |  |  |
| **AAUT\_BFD\_INHIB\_0001** | OK | DSG\_BFD\_0009; DSG\_BFD\_0010; DSG\_BFD\_0011; DSG\_BFD\_0008 |
| **AAUT\_BFD\_INHIB\_0002** | OK | DSG\_BFD\_0012; DSG\_BFD\_0014; DSG\_BFD\_0013 |
| **BFD\_mgr** |  |  |
| **AAUT\_BFD\_MGR\_0001** | OK | DSG\_BFD\_0015 |
| **AAUT\_BFD\_MGR\_0002** | OK | DSG\_BFD\_0019; DSG\_BFD\_0020; DSG\_BFD\_0017; DSG\_BFD\_0018; DSG\_BFD\_0021; DSG\_BFD\_0025; DSG\_BFD\_0022; DSG\_BFD\_0023; DSG\_BFD\_0024; DSG\_BFD\_0028; DSG\_BFD\_0029; |
| **AAUT\_BFD\_MGR\_0003** | OK | DSG\_BFD\_0016 |
| **AAUT\_BFD\_MGR\_0004** | OK | DSG\_BFD\_0028 |
| **AAUT\_BFD\_MGR\_0005** | OK | DSG\_BFD\_0024 |
| **AAUT\_BFD\_MGR\_0006** | OK | DSG\_BFD\_0028; DSG\_BFD\_0031; |
| **AAUT\_BFD\_MGR\_0007** | OK | DSG\_BFD\_0028; DSG\_BFD\_0030; |
| **AAUT\_BFD\_MGR\_0008** | OK | DSG\_BFD\_0032; |
| **AAUT\_BFD\_MGR\_0009** | OK | DSG\_BFD\_0003; DSG\_BFD\_0022 |
| **AAUT\_BFD\_MGR\_0010** | OK | DSG\_BFD\_0033; |
| **BMM\_Autotests** |  |  |
| **AAUT\_BMM\_0001** | PASSED | DSG\_BMM\_00023; DSG\_BMM\_00024; DSG\_BMM\_00025; DSG\_BMM\_00026; DSG\_BMM\_00027; DSG\_BMM\_00028 |
| **AAUT\_BMM\_0002** | PASSED | DSG\_BMM\_00023; DSG\_BMM\_00024; DSG\_BMM\_00025; DSG\_BMM\_00026; DSG\_BMM\_00027; DSG\_BMM\_00028 |
| **AAUT\_BMM\_0003** | PASSED | DSG\_BMM\_00023; DSG\_BMM\_00024; DSG\_BMM\_00025; DSG\_BMM\_00026; DSG\_BMM\_00027; DSG\_BMM\_00028 |
| **AAUT\_BMM\_0004** | PASSED | DSG\_BMM\_00023; DSG\_BMM\_00024; DSG\_BMM\_00025; DSG\_BMM\_00026; DSG\_BMM\_00027; DSG\_BMM\_00028 |
| **BMM** |  |  |
| **AAUT\_BMM\_0005** | PASSED | DSG\_BMM\_00004; DSG\_BMM\_00005; DSG\_BMM\_00006; DSG\_BMM\_00007; DSG\_BMM\_00008 |
| **AAUT\_BMM\_0006** | PASSED | DSG\_BMM\_00004; DSG\_BMM\_00005; DSG\_BMM\_00006; DSG\_BMM\_00007; DSG\_BMM\_00008 |
| **AAUT\_BMM\_0007** | PASSED | DSG\_BMM\_00004; DSG\_BMM\_00005; DSG\_BMM\_00006; DSG\_BMM\_00007; DSG\_BMM\_00008 |
| **AAUT\_BMM\_0008** | PASSED | DSG\_BMM\_00009; DSG\_BMM\_00010 |
| **CIL\_Init** |  |  |
| **AAUT\_CIL\_0001** | PASSED | ARCH\_SW\_CIL\_0600; |
| **AAUT\_CIL\_0002** | PASSED | ARCH\_SW\_CIL\_0200;ARCH\_SW\_CIL\_0100 |
| **CIL\_ManageMF** |  |  |
| **AAUT\_CIL\_0007** | PASSED | DSG\_CIL\_0024 DSG\_CIL\_0025 DSG\_CIL\_0026 DSG\_CIL\_0027 DSG\_CIL\_0028 DSG\_CIL\_0029 DSG\_CIL\_0030 DSG\_CIL\_0031 DSG\_CIL\_0032 DSG\_CIL\_0033 DSG\_CIL\_0034 DSG\_CIL\_0035 DSG\_CIL\_0036 DSG\_CIL\_0037 DSG\_CIL\_0038 DSG\_CIL\_0039 DSG\_CIL\_0040 DSG\_CIL\_0041 |
| **CIL\_CANToAppli** |  |  |
| **AAUT\_CIL\_0004** | PASSED | DSG\_CIL\_0001; DSG\_CIL\_0002; DSG\_CIL\_0003; DSG\_CIL\_0004; DSG\_CIL\_0005; DSG\_CIL\_0006 |
| **AAUT\_CIL\_0005** | PASSED | DSG\_CIL\_0005 |
| **AAUT\_CIL\_0006** | OK | DSG\_CIL\_0001; DSG\_CIL\_0002; DSG\_CIL\_0003; DSG\_CIL\_0006; DSG\_CIL\_0044; DSG\_CIL\_0042; DSG\_CIL\_0043; DSG\_CIL\_0055; DSG\_CIL\_0054; |
| **AAUT\_CIL\_0016** | OK | DSG\_CIL\_0023 |
| **AAUT\_CIL\_0018** | PASSED | DSG\_CIL\_0071, |
| **AAUT\_CIL\_0019** | PASSED | DSG\_CIL\_0005, |
| **AAUT\_CIL\_0020** | PASSED | DSG\_CIL\_0068, |
| **AAUT\_CIL\_0021** | PASSED | DSG\_CIL\_0069, |
| **AAUT\_CIL\_0022** | PASSED | DSG\_CIL\_0072, |
| **AAUT\_CIL\_0023** | PASSED | DSG\_CIL\_0067, |
| **AAUT\_CIL\_0024** | PASSED | DSG\_CIL\_0070, |
| **AAUT\_CIL\_0025** | PASSED | DSG\_CIL\_0073, |
| **AAUT\_CIL\_0026** | PASSED | DSG\_CIL\_0074, |
| **AAUT\_CIL\_0027** | PASSED | DSG\_CIL\_0075, |
| **AAUT\_CIL\_0028** | PASSED | DSG\_CIL\_0066, |
| **AAUT\_CIL\_0029** | PASSED | DSG\_CIL\_0001, |
| **AAUT\_CIL\_0030** | PASSED | DSG\_CIL\_0044, |
| **CIL\_AppliToCAN** |  |  |
| **AAUT\_CIL\_0008** | PASSED | DSG\_CIL\_0001; DSG\_CIL\_0005; DSG\_CIL\_0007; DSG\_CIL\_0008; DSG\_CIL\_0009; DSG\_CIL\_0010; DSG\_CIL\_0011; DSG\_CIL\_0012; DSG\_CIL\_0013; DSG\_CIL\_0014; DSG\_CIL\_0015; DSG\_CIL\_0016; DSG\_CIL\_0017; DSG\_CIL\_0018; DSG\_CIL\_0019; DSG\_CIL\_0020; DSG\_CIL\_0021; DSG\_CIL\_0022; DSG\_CIL\_0023; DSG\_CIL\_0006 ; DSG\_CIL\_0046; DSG\_CIL\_0047; DSG\_CIL\_0048; DSG\_CIL\_0053; DSG\_CIL\_0042; DSG\_CIL\_0062; |
| **AAUT\_CIL\_0010** | PASSED | DSG\_CIL\_0065 |
| **AAUT\_CIL\_0011** | PASSED | DSG\_CIL\_0065 |
| **AAUT\_CIL\_0012** | PASSED | DSG\_CIL\_0065 |
| **AAUT\_CIL\_0013** | PASSED | DSG\_CIL\_0065 |
| **AAUT\_CIL\_0009** | PASSED | DSG\_CIL\_0005 |
| **AAUT\_CIL\_0014** | PASSED | DSG\_CIL\_0023; DSG\_CIL\_0063; DSG\_CIL\_0062; DSG\_CIL\_0065 |
| **AAUT\_CIL\_0015** | PASSED | DSG\_CIL\_0061, DSG\_CIL\_0060, DSG\_CIL\_0059, DSG\_CIL\_0058, DSG\_CIL\_0056 |
| **AAUT\_CIL\_0017** | OK | DSG\_CIL\_0023 DSG\_CIL\_0076 |
| **DemIf** |  |  |
| **AAUT\_Demif\_0001** | PASSED | DSG\_DemIf\_00002, |
| **AAUT\_Demif\_0002** | PASSED | DSG\_DemIf\_00001, |
| **AAUT\_Demif\_0003** | PASSED | DSG\_DemIf\_00003, |
| **AAUT\_Demif\_0004** | PASSED |  |
| **AAUT\_Demif\_0005** | PASSED | DSG\_DemIf\_00004, |
| **AAUT\_Demif\_0006** | PASSED | DSG\_DemIf\_00009, |
| **AAUT\_Demif\_0007** | PASSED | DSG\_DemIf\_00005, |
| **AAUT\_Demif\_0008** | PASSED |  |
| **AAUT\_Demif\_0009** | PASSED | DSG\_DemIf\_00006, |
| **AAUT\_Demif\_0010** | PASSED | DSG\_DemIf\_00007, |
| **AAUT\_Demif\_0011** | PASSED | DSG\_DemIf\_00008, |
| **EOL** |  |  |
| **AAUT\_EOL\_0000** | PASSED | DSG\_EOL\_0001; |
| **AAUT\_EOL\_0001** | PASSED | DSG\_EOL\_0002 |
| **EOL\_autotest** |  |  |
| **AAUT\_EOL\_0002** | PASSED | DSG\_EOL\_0003 |
| **AAUT\_EOL\_0003** | PASSED | DSG\_EOL\_0004 |
| **AAUT\_EOL\_0004** | PASSED | DSG\_EOL\_0005 |
| **AAUT\_EOL\_0005** | PASSED | DSG\_EOL\_0006 |
| **ERH** |  |  |
| **AAUT\_ERH\_001** | OK | DSG\_ERH\_00008; |
| **AAUT\_ERH\_002** | OK | DSG\_ERH\_00009; DSG\_ERH\_00010; DSG\_ERH\_00011 |
| **AAUT\_ERH\_003** | OK | DSG\_ERH\_00005; DSG\_ERH\_00006; DSG\_ERH\_00007; |
| **AAUT\_ERH\_004** | OK | DSG\_ERH\_00001; DSG\_ERH\_00002; DSG\_ERH\_00003; DSG\_ERH\_00004; |
| **AAUT\_ERH\_005** | OK | DSG\_ERH\_00012; DSG\_ERH\_00013; DSG\_ERH\_00014; DSG\_ERH\_00015; DSG\_ERH\_00016; DSG\_ERH\_00017; DSG\_ERH\_00018; |
| **AAUT\_ERH\_006** | OK | DSG\_ERH\_00019; DSG\_ERH\_00020; DSG\_ERH\_00021; DSG\_ERH\_00022; DSG\_ERH\_00023; |
| **AAUT\_ERH\_007** | OK | DSG\_ERH\_00024; DSG\_ERH\_00025; DSG\_ERH\_00026; DSG\_ERH\_00027; DSG\_ERH\_00028; |
| **AAUT\_ERH\_008** | OK | DSG\_ERH\_00029; DSG\_ERH\_00030; DSG\_ERH\_00031; |
| **AAUT\_ERH\_009** | OK | DSG\_ERH\_00005; DSG\_ERH\_00006; DSG\_ERH\_00007; DSG\_ERH\_00029; DSG\_ERH\_00030; DSG\_ERH\_00031; |
| **AAUT\_ERH\_010** | OK | DSG\_ERH\_00017; DSG\_ERH\_00022; DSG\_ERH\_00028; DSG\_ERH\_00032; |
| **AAUT\_ERH\_011** | OK | DSG\_ERH\_00012; DSG\_ERH\_00013; DSG\_ERH\_00015; DSG\_ERH\_00016; DSG\_ERH\_00019; DSG\_ERH\_00001; DSG\_ERH\_00002; DSG\_ERH\_00003; DSG\_ERH\_00004; |
| **AAUT\_ERH\_012** | OK | DSG\_ERH\_00033; DSG\_ERH\_00034; DSG\_ERH\_00001; DSG\_ERH\_00002; DSG\_ERH\_00003; DSG\_ERH\_00004; DSG\_ERH\_00024 DSG\_ERH\_00025; DSG\_ERH\_00026; DSG\_ERH\_00027; DSG\_ERH\_00028; |
| **AAUT\_ERH\_013** | OK | DSG\_ERH\_00001; DSG\_ERH\_00002; DSG\_ERH\_00003; DSG\_ERH\_00004; DSG\_ERH\_00033; DSG\_ERH\_00034; DSG\_ERH\_00029; DSG\_ERH\_00030; DSG\_ERH\_00031; DSG\_ERH\_00008; DSG\_ERH\_00035, DSG\_ERH\_00036; DSG\_ERH\_00037; |
| **AAUT\_ERH\_014** | OK |  |
| **AAUT\_ERH\_015** | OK | DSG\_ERH\_00039; DSG\_ERH\_00038 |
| **AAUT\_ERH\_016** | OK | DSG\_ERH\_00040; DSG\_ERH\_00038 |
| **AAUT\_ERH\_017** | OK | DSG\_ERH\_00041; DSG\_ERH\_00038 |
| **AAUT\_ERH\_018** | OK | DSG\_ERH\_00042; DSG\_ERH\_00038 |
| **AAUT\_ERH\_019** | OK | DSG\_ERH\_00043; DSG\_ERH\_00038 |
| **AAUT\_ERH\_020** | OK | DSG\_ERH\_00044; DSG\_ERH\_00038 |
| **MIC** |  |  |
| **AAUT\_MIC\_0001** | PASSED | DSG\_MIC\_0001 |
| **AAUT\_MIC\_0002** | PASSED | DSG\_MIC\_0001 |
| **AAUT\_MIC\_0003a** | PASSED | DSG\_MIC\_0001 |
| **AAUT\_MIC\_0003b** | PASSED | DSG\_MIC\_0001 |
| **AAUT\_MIC\_0004** | PASSED | DSG\_MIC\_0001 |
| **MIC\_autotests** |  |  |
| **AAUT\_MIC\_autotests\_0001** | PASSED | DSG\_MIC\_0004 |
| **AAUT\_MIC\_autotests\_0002** | PASSED | DSG\_MIC\_0004 |
| **AAUT\_MIC\_autotests\_0003** | PASSED | DSG\_MIC\_0004 |
| **AAUT\_MIC\_autotests\_0004** | PASSED | DSG\_MIC\_0005 |
| **AAUT\_MIC\_autotests\_0005** | PASSED | DSG\_MIC\_0005 |
| **AAUT\_MIC\_autotests\_0006** | PASSED | DSG\_MIC\_0007 |
| **AAUT\_MIC\_autotests\_0007** | PASSED | DSG\_MIC\_0007 |
| **AAUT\_MIC\_autotests\_0008** | PASSED | DSG\_MIC\_0006 |
| **MIC\_LLD** |  |  |
| **AAUT\_MIC\_LLD\_0001** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0002** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0003** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0004** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0005** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0006** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0007** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0008** | PASSED | DSG\_MIC\_0002 |
| **AAUT\_MIC\_LLD\_0009** | PASSED | DSG\_MIC\_0002 |
| **MMG\_CommunicationModes** |  |  |
| **AAUT\_MMG\_0001** | PASSED |  |
| **AAUT\_MMG\_0002** | PASSED |  |
| **AAUT\_MMG\_0003** | PASSED |  |
| **AAUT\_MMG\_0004** | PASSED |  |
| **MMG\_CriticalAutoTestsModes** |  |  |
| **AAUT\_MMG\_0005** | PASSED | DSG\_MMG\_0004 |
| **AAUT\_MMG\_0006** | PASSED | DSG\_MMG\_0004 |
| **AAUT\_MMG\_0007** | PASSED | DSG\_MMG\_0004 |
| **MMG\_CyclesModes** |  |  |
| **AAUT\_MMG\_0008** | PASSED | DSG\_MMG\_0004 |
| **AAUT\_MMG\_0009** | PASSED | DSG\_MMG\_0004 |
| **MMG** |  |  |
| **AAUT\_MMG\_000A** | PASSED | DSG\_MMG\_0001; DSG\_MMG\_0002 |
| **AAUT\_MMG\_000B** | PASSED | DSG\_MMG\_0004 |
| **AAUT\_MMG\_000C** | PASSED | DSG\_MMG\_0004 |
| **AAUT\_MMG\_000D** | PASSED | DSG\_MMG\_0001; DSG\_MMG\_0002 |
| **Nm\_ExtraCallbacks** |  |  |
| **AAUT\_NmIf\_0001** | PASSED | DSG\_NMIF\_00004; |
| **AAUT\_NmIf\_0002** | PASSED | DSG\_NMIF\_00003; |
| **AAUT\_NmIf\_0003** | PASSED |  |
| **AAUT\_NmIf\_0004** | PASSED | DSG\_NMIF\_00002 |
| **AAUT\_NmIf\_0005** | PASSED | DSG\_NMIF\_00001 |
| **AAUT\_NmIf\_0006** | PASSED | DSG\_NMIF\_00001 |
| **AAUT\_NmIf\_0007** | PASSED | DSG\_NMIF\_00005, DSG\_NMIF\_00006, DSG\_NMIF\_00007, DSG\_NMIF\_00008, DSG\_NMIF\_00009, DSG\_NMIF\_00010, DSG\_NMIF\_00011, DSG\_NMIF\_00012, DSG\_NMIF\_00013, DSG\_NMIF\_00014, DSG\_NMIF\_00015, DSG\_NMIF\_00016, DSG\_NMIF\_00017, DSG\_NMIF\_00018, DSG\_NMIF\_00019, DSG\_NMIF\_00020, DSG\_NMIF\_00021, DSG\_NMIF\_00022, DSG\_NMIF\_00023 |
| **AAUT\_NmIf\_0008** | PASSED | DSG\_NMIF\_00016, DSG\_NMIF\_00017, |
| **AAUT\_NmIf\_0009** | PASSED | DSG\_NMIF\_00024 |
| **AAUT\_NmIf\_0010** | PASSED |  |
| **AAUT\_NmIf\_0011** | PASSED |  |
| **NvmIf\_autotest** |  |  |
| **AAUT\_NvMlf\_0001** | PASSED | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **AAUT\_NvMlf\_0002** | PASSED | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **AAUT\_NvMlf\_0003** | PASSED | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **AAUT\_NvMlf\_0004** | OK | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **AAUT\_NvMlf\_0005** | OK | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **AAUT\_NvMlf\_0006** | OK | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **AAUT\_NvMlf\_0007** | OK | DSG\_NcmIf\_00014; DSG\_NcmIf\_00015 |
| **PAL\_Autotests** |  |  |
| **AAUT\_PAL\_0001** | PASSED | DSG\_PAL\_0042; DSG\_PAL\_0043; DSG\_PAL\_0045 |
| **AAUT\_PAL\_0002a** | PASSED | DSG\_PAL\_0044 |
| **AAUT\_PAL\_0002b** | PASSED | DSG\_PAL\_0044 |
| **AAUT\_PAL\_0003a** | PASSED | DSG\_PAL\_0046 |
| **AAUT\_PAL\_0003b** | PASSED | DSG\_PAL\_0046 |
| **AAUT\_PAL\_0004a** | PASSED | DSG\_PAL\_0047 |
| **AAUT\_PAL\_0004b** | PASSED | DSG\_PAL\_0047 |
| **AAUT\_PAL\_0005a** | PASSED | DSG\_PAL\_0048 |
| **AAUT\_PAL\_0005b** | PASSED | DSG\_PAL\_0048 |
| **AAUT\_PAL\_0006** | PASSED | DSG\_PAL\_0042 |
| **AAUT\_PAL\_0007** | PASSED | DSG\_PAL\_0049 |
| **PMP** |  |  |
| **AAUT\_PMP\_0001** | PASSED | DSG\_PMP\_0011, DSG\_PMP\_0012, DSG\_PMP\_0013, DSG\_PMP\_0014, DSG\_PMP\_0015, |
| **AAUT\_PMP\_0002** | PASSED | DSG\_PMP\_0011, DSG\_PMP\_0012, DSG\_PMP\_0013, DSG\_PMP\_0014, DSG\_PMP\_0015, |
| **AAUT\_PMP\_0003** | PASSED | DSG\_PMP\_0011, DSG\_PMP\_0012, DSG\_PMP\_0013, DSG\_PMP\_0014, DSG\_PMP\_0015, |
| **AAUT\_PMP\_0004** | PASSED | DSG\_PMP\_0011, DSG\_PMP\_0012, DSG\_PMP\_0013, DSG\_PMP\_0014, DSG\_PMP\_0015, |
| **AAUT\_PMP\_0005** | PASSED | DSG\_PMP\_0016, |
| **AAUT\_PMP\_0006** | PASSED | DSG\_PMP\_0016, |
| **AAUT\_PMP\_0007** | PASSED | DSG\_PMP\_0016, |
| **AAUT\_PMP\_0008** | PASSED | DSG\_PMP\_0016, |
| **PMP\_autotests** |  |  |
| **AAUT\_PMP\_autotests\_0001** | PASSED | DSG\_PMP\_0077, DSG\_PMP\_0079, |
| **AAUT\_PMP\_autotests\_0002** | PASSED | DSG\_PMP\_0077, DSG\_PMP\_0080, |
| **AAUT\_PMP\_autotests\_0003** | PASSED | DSG\_PMP\_0077, DSG\_PMP\_0078, |
| **AAUT\_PMP\_autotests\_0004** | PASSED | DSG\_PMP\_0081, DSG\_PMP\_0083, |
| **AAUT\_PMP\_autotests\_0005** | PASSED | DSG\_PMP\_0081, DSG\_PMP\_0084, |
| **AAUT\_PMP\_autotests\_0006** | PASSED | DSG\_PMP\_0081, DSG\_PMP\_0082, |
| **PortIf** |  |  |
| **AAUT\_Portlf\_0001** | PASSED | DSG\_PortIf\_0002 |
| **AAUT\_Portlf\_0002** | PASSED | DSG\_PortIf\_0002 |
| **AAUT\_Portlf\_0003** | PASSED | DSG\_PortIf\_0001 |
| **AAUT\_Portlf\_0004** | PASSED | DSG\_PortIf\_0003 |
| **AAUT\_Portlf\_0005** | PASSED | DSG\_PortIf\_0003 |
| **Rcm** |  |  |
| **AAUT\_RCM\_0006** | PASSED |  |
| **AAUT\_RCM\_0007** | PASSED |  |
| **AAUT\_RCM\_0008** | PASSED | DSG\_RCM\_0017 |
| **AAUT\_RCM\_0009** | PASSED | DSG\_RCM\_0018; DSG\_RCM\_0019; DSG\_RCM\_0020; DSG\_RCM\_0021; DSG\_RCM\_0022 |
| **AAUT\_RCM\_000A** | PASSED | DSG\_RCM\_0018; DSG\_RCM\_0019; DSG\_RCM\_0020; DSG\_RCM\_0021; DSG\_RCM\_0022 |
| **AAUT\_RCM\_000B** | PASSED | DSG\_RCM\_0018; DSG\_RCM\_0019; DSG\_RCM\_0020; DSG\_RCM\_0021; DSG\_RCM\_0022 |
| **RCM\_autotests** |  |  |
| **AAUT\_RCM\_0001** | PASSED | DSG\_RCM\_0014; DSG\_RCM\_0015 |
| **AAUT\_RCM\_0002** | PASSED | DSG\_RCM\_0014; DSG\_RCM\_0015 |
| **AAUT\_RCM\_0003** | PASSED | DSG\_RCM\_0014; DSG\_RCM\_0015 |
| **AAUT\_RCM\_0004** | PASSED | DSG\_RCM\_0016 |
| **AAUT\_RCM\_0005** | PASSED | DSG\_RCM\_0016 |
| **SCM\_Cfg** |  |  |
| **AAUT\_SCM\_0001** | PASSED | DSG\_SCM\_0001; DSG\_SCM\_0003; DSG\_SCM\_0004; DSG\_SCM\_0008 |
| **AAUT\_SCM\_0002** | OK | DSG\_SCM\_0001; DSG\_SCM\_0003; DSG\_SCM\_0004; DSG\_SCM\_0009 |
| **AAUT\_SCM\_0003** | PASSED | DSG\_SCM\_0001; DSG\_SCM\_0003; DSG\_SCM\_0004; DSG\_SCM\_0010 |
| **AAUT\_SCM\_0004** | PASSED | DSG\_SCM\_0001; DSG\_SCM\_0003; DSG\_SCM\_0004 |
| **AAUT\_SCM\_0005** | PASSED | DSG\_SCM\_0007 |
| **AAUT\_SCM\_0006** | PASSED | DSG\_SCM\_0007 |
| **AAUT\_SCM\_0007** | PASSED | DSG\_SCM\_0007 |
| **AAUT\_SCM\_0008** | PASSED | DSG\_SCM\_0007 |
| **AAUT\_SCM\_0009** | PASSED | DSG\_SCM\_0007 |
| **AAUT\_SCM\_0010** | PASSED | DSG\_SCM\_0007 |
| **AAUT\_SCM\_0011** | PASSED | DSG\_SCM\_0005 |
| **AAUT\_SCM\_0012** | PASSED | DSG\_SCM\_0005 |
| **AAUT\_SCM\_0013** | PASSED | DSG\_SCM\_0014 |
| **AAUT\_SCM\_0014** | PASSED | DSG\_SCM\_0014 |
| **SpiIf** |  |  |
| **AAUT\_Spilf\_0001** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0002** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0003a** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0003b** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0004a** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0004b** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0005** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0006** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0007a** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0007b** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0008** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0009a** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0009b** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0009c** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_0009d** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_000A** | PASSED | DSG\_SpiIf\_0004 |
| **AAUT\_Spilf\_000B** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000C** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000D** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000E** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000F** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000G** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000H** | PASSED | DSG\_SpiIf\_0005 |
| **AAUT\_Spilf\_000I** | PASSED | DSG\_SpiIf\_0005 |

Notes:

***Number of tests: 229***

***Number of tests passed: 229***

***Number of tests failed: 0***

***Number of tests not run: 0***