# **Anat Miller**

SENIOR SOFTWARE ENGINEER

#### **Details**

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#### Skills

Embedded system devlopment

Python

SaaS

Back-end web devlopement

Docker

Linux

SQL

Windows

Object-Oriented Programming (OOP)

Websocket (Async)

**REST API** 

Docker/Containerization

MultiCore

Mongo DB

Amazon AWS cloud

GCP

Azure

TCP/IP

MySQL

Networking

# **Profile**

Accomplished **Python Senior Software** Engineer with over **15** years of experience. I have a proven track record in the **AI industry**, specializing in building **high-performance**, **scalable** systems and deploying deep learning models. specializing in developing back-end **SaaS** multi-tenant **micro-service** solutions from scratch at high scale . Driven to make a significant impact and always ready to **innovate!**.

## **Professional Experience**

## Senior C++/Python backend software engineer, PTC

MARCH 2022 - PRESENT

- Designed, developed, and successfully implemented complex features in C/C++
  for software CAD 2D/3D visualization tools, enhancing software's capabilities
  and user experience.
- Played a pivotal role in the high-value transition of a desktop on-prem application to a web-based SaaS model, contributing to the company's growth and market adaptation.
- Participating in the end-to-end software development lifecycle, from design to implementation and testing.
- Developing multi-threaded backend servers on Linux and Windows.
- Applied an in-depth understanding of the Linux operating system to optimize software solutions and enhance overall system efficiency
- Developing a scalable backend server in a multi-tenant environment to support many clients simultaneously.
- Designed, coordinated, and implemented various cross-functional and cross-site projects with diverse stakeholders.
- Conducting code reviews and offering mentorship to junior team members.

#### Software Devloper, Intel, Tel Aviv

JANUARY 2018 - MARCH 2022

- Design and implementation of robust automation infrastructure and tools in
   Python and C/C++
- Worked on various Linux platforms, demonstrating adaptability.
- Design and development of optimized operators commonly used in deep learning workloads in C++.
- Experienced in **Docker building** and **deployment**, ensuring a robust and scalable solution.
- Familiar with commonly used neural network Al frameworks such as Pytorch,
   TensoFlow for inference and training.
- Execute, debug, and analyze complex SW/HW/FW tests.

### Software Developer, Intel Labs

JANUARY 2016 - JANUARY 2018

- Design and implementation of a robust **C/C++** and **Python** automation infrastructure and tools.
- Design and development of embedded unitests for Intel RealSense 3D cameras.
- Development on Windows and Linux platforms.
- Experience working with cloud: GCP and AWS cloud environments
- Working with **Docker** and **containers**

UDP Ethernet 802.3 **Routing and Switching** C++ Centos **PSOS** Git Jenkins Jira **Pycharm** Adaptable to dynamic challenge Performance Optimization. Experience with Agile Methodologies Visual Studio Mutli-threaded

## Languages

Hebrew

English

### SW Embedded Networking Engineer, Audio Codes

JANUARY 2013 - JANUARY 2015

- Planned, designed, and developed in C/C++ on Linux.
- Implemented and integrated code into Microsoft Lync, enhancing communication capabilities and ensuring compatibility with the Microsoft ecosystem.

### Dsp/Software/Algorithm Engineer, Radvision

JANUARY 2011 - JANUARY 2013

- Planned, designed, and developed multimedia applications in C/C++ on Linux.
- Worked on multi-core and multi-threaded systems to optimize performance and scalability.
- Implement various video algorithms to enhance the quality and functionality of the media processing capabilities.
- Collaborated in a real-time embedded environment, employing optimizations.

#### Real Time Engineer, Teledata Networks

JANUARY 2008 - JANUARY 2011

- Designed and implemented solutions involving various **network** protocols
- Developed efficient code in C/C++ on both the Linux and Nucleus operating systems
- Designed and coded PHY driver.
- Integrated with diverse hardware devices, including FPGA, SFP, I2C, and FLASH.

#### Real Time Enginner, Axerra Networks

JANUARY 2005 - 2008

- Designed and implemented **network protocols** for **embedded** systems.
- Real-time engineer in the field of embedded network traffic.
- Developed and implemented the **Power Quick Motorola 860 processor** using the **PSOS Real-Time operating system**.

### **Education**

## B.sc Computer Science, Bar Ilan Unvirestity

JANUARY 2004 - JANUARY 2007

Graduate with High honor GPA:87

 Project in Compile Company: VSTO Outlook Plugin in C#;text to speech;Working with SQL server DB.

# Military service

#### Programmer, Glilot

JULY 1997 - MAY 1999

- Leading a team of developers.
- Designed and developed educational apps and interactive games for various education divisions.