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Ahmed Nassar

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About


Currently at NVIDIA working on V&V to ensure safety of self-driving cars (simulation, scenario description languages, ontologies, specification languages, runtime verification).

Obtained a PhD in Electrical and Computer Engineering from UC Irvine, working on complete runtime verification frameworks targeted at software running over distributed multiprocessor systems (including specification languages, architectural supporting mechanisms, and specification mining) with applications to host-based intrusion detection.

Industry experience in digital SoC design and advanced (i.e., assertion-based, coverage-driven, constrained-random) functional verification.

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