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Margaret Bloom

 Member for 8 years, 11 months  Last seen this week

Profile

Activity

Stats

43,893
reputation

1.6m
reached

611
answers

15
questions



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gold badges

● assembly

Nov 29, 2018

● Electorate

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Aug 31, 2017



87
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● Documentation Pio...

Aug 16, 2016

● Pundit

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● cpu

Apr 24, 2019

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Jan 25, 2018

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1,912 score 340 posts 54 posts %

x86 ●

1,220 score 211 posts 34 posts %

x86-64 ●

644 score 78 posts 12 posts %

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328 score 65 posts 10 posts %

cpu ●

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Answers

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362



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Feb 29, 2016

 77	Multi threading with Millicores in Kubernetes	May 17, 2020
 56	How is 1 == [1] in javascript?	May 4, 2016
 56	Why doesn't GCC use partial registers?	Jan 10, 2017
 56	Why is imul used for multiplying unsigned numbers?	Mar 3, 2017
 55	Is incrementing an int effectively atomic in specific cases?	Sep 8, 2016
 49	What are _mm_prefetch() locality hints?	Oct 2, 2017
 48	Does a memory barrier ensure that the cache coherence has been completed?	Mar 12, 2017
 46	Why don't compilers merge redundant std::atomic writes?	Aug 30, 2017
 42	Why in x86-64 the virtual address are 4 bits shorter than physical (48 bits vs. 52 long)?	Oct 1, 2017






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