



Profiles ▼

# Peter Cordes

 Member for 15 years  Last seen this week

  Halifax, Canada

Profile

Activity

## Stats

360,821  
reputation

18.1m  
reached

6,604  
answers

28  
questions



#10 this month

## About

GNU/Linux programmer/performance engineer and command line junkie. He/him.

Primary maintainer of the Stack Overflow [x86 tag wiki](#).

I like efficient code, and knowing how things *really* work.

I mostly use C/C++ (and perl/bash) on Linux, but I mostly look at assembly-language stuff on SO because it's more interesting to me, and there are fewer people posting good asm answers.

(profile pic is from <https://xkcd.com/386/>, and describes me perfectly. Incomplete answers/explanations make me crazy.)

## Badges

[View all badges](#)



**49**

gold badges

● x86-64

Jul 27, 2018

● cpu-architecture

Sep 12, 2018

● intel

Aug 16, 2019



**699**

silver badges

● avx2

Jun 17, 2019

● micro-optimization

Oct 3, 2018

● masm

Aug 14, 2023



**958**

bronze badges

● lock-free

Aug 29, 2018

● avx512

Feb 3, 2019

● stdatomic

Feb 10, 2019

## Top tags

[View all tags](#)

assembly ●

17,124 score 3,397 posts 51 posts %

x86 ●

13,937 score 2,113 posts 32 posts %







c++ ●	9,691 score	1,137 posts	17 posts %
c ●	6,115 score	1,189 posts	18 posts %
x86-64 ●	5,655 score	1,101 posts	17 posts %
cpu-architecture ●	4,996 score	866 posts	13 posts %

## Top posts

View all [questions](#), [answers](#), and [articles](#)





All	Questions	Answers	Articles	Score	Newest
-----	-----------	---------	----------	-------	--------

A	2135	Why does C++ code for testing the Collatz conjecture run faster than hand-written assembly?	Nov 1, 2016
A	430	Deoptimizing a program for the pipeline in Intel Sandybridge-family CPUs	May 21, 2016
A	371	How much of 'What Every Programmer Should Know About Memory' is still valid?	Dec 8, 2017
A	329	What is the best way to set a register to zero in x86 assembly: xor, mov or and?	Nov 12, 2015

	324	Does the C++ standard allow for an uninitialized bool to crash a program?	Jan 10, 2019
	267	Is incrementing an int effectively atomic in specific cases?	Sep 8, 2016
	184	Why does glibc's strlen need to be so complicated to run quickly?	Aug 27, 2019
	176	Bubble sort slower with -O3 than -O2 with GCC	Oct 9, 2021
	158	Fastest way to do horizontal SSE vector sum (or other reduction)	Feb 8, 2016
	154	How to remove "noise" from GCC/clang assembly output?	Jul 24, 2016

## Top Meta posts

 14  103

	77	How are we supposed to post godbolt links now that URL shortening is blocked?
	65	The dawn of Documentation: a solstice update
	63	Discussion: New AI-generated content policy
	62	Are questions that boil down to errors in off-site resources on-topic?



59

[Git question with very nonlinear history: Answers cannibalizing each other into redundancy](#)

## Top network posts

[View all network posts](#)

126

[Why does preset "veryfast" in FFmpeg generate the most compressed file compared to all other presets?](#)



100

[Why does my browser think that https://1.1.1.1 is secure?](#)



64

[Why are leap seconds needed so often?](#)



57

[The start of x86: Intel 8080 vs Intel 8086?](#)



57

[Did any compiler fully use Intel x87 80-bit floating point?](#)

## Communities

[View all](#) [Stack Overflow](#)

360.8k

[Unix & Linux](#)

6.5k

[Super User](#)

6.1k

[Code Golf](#)

5k

[Retrocomputing](#)

3.6k