Bo Zhang

Address: 4106 Yosemite Street, Cedar Park, TX, USA 78613   **Phone**: (+1) 734-546-5863

EDUCATION

University of Michigan, Ann Arbor - MI, USA
Master in **Electrical Engineering** - **GPA: 3.8/4.0**   Sep. 2014 - Dec. 2015
Shanghai Jiao Tong University, - Shanghai, China
Bachelor in **Electrical Engineering** - **GPA: 3.76/4.0** , **Valedictorian**   2010 - 2014

WORK EXPERIENCE

AMD, Austin, TX   Nov 2017 - Present
Sr. CPU Performance Engineer

- Use Excel spreadsheet to project benchmark's score of AMD laptop product

- Run benchmarks on both AMD and competitors' laptop and compare performance

Oracle, Austin, TX   April 2016 - Oct 2017
Hardware Engineer

- Write Java microbenchmarks and use them to evaluate performance on both Oracle servers

- Investigate and solve Java performance issues on Oracle servers

- Tune default Java Virtual Machine flags for Oracle servers

National Instruments, Austin, TX   May 2015 - Aug 2015
Hardware Engineer Intern

- Use Labview as a tool to create applications to measure voltage and current

- Develope plug-in VIs in Labview and tested on real hardware of Digital Input Module

SKILLS

Programming Languages: Java, Scala, C/C++, Matlab, perl, python
Applications: Synopsys VCS, ModelSim, Xilinx, Simulink, Labview