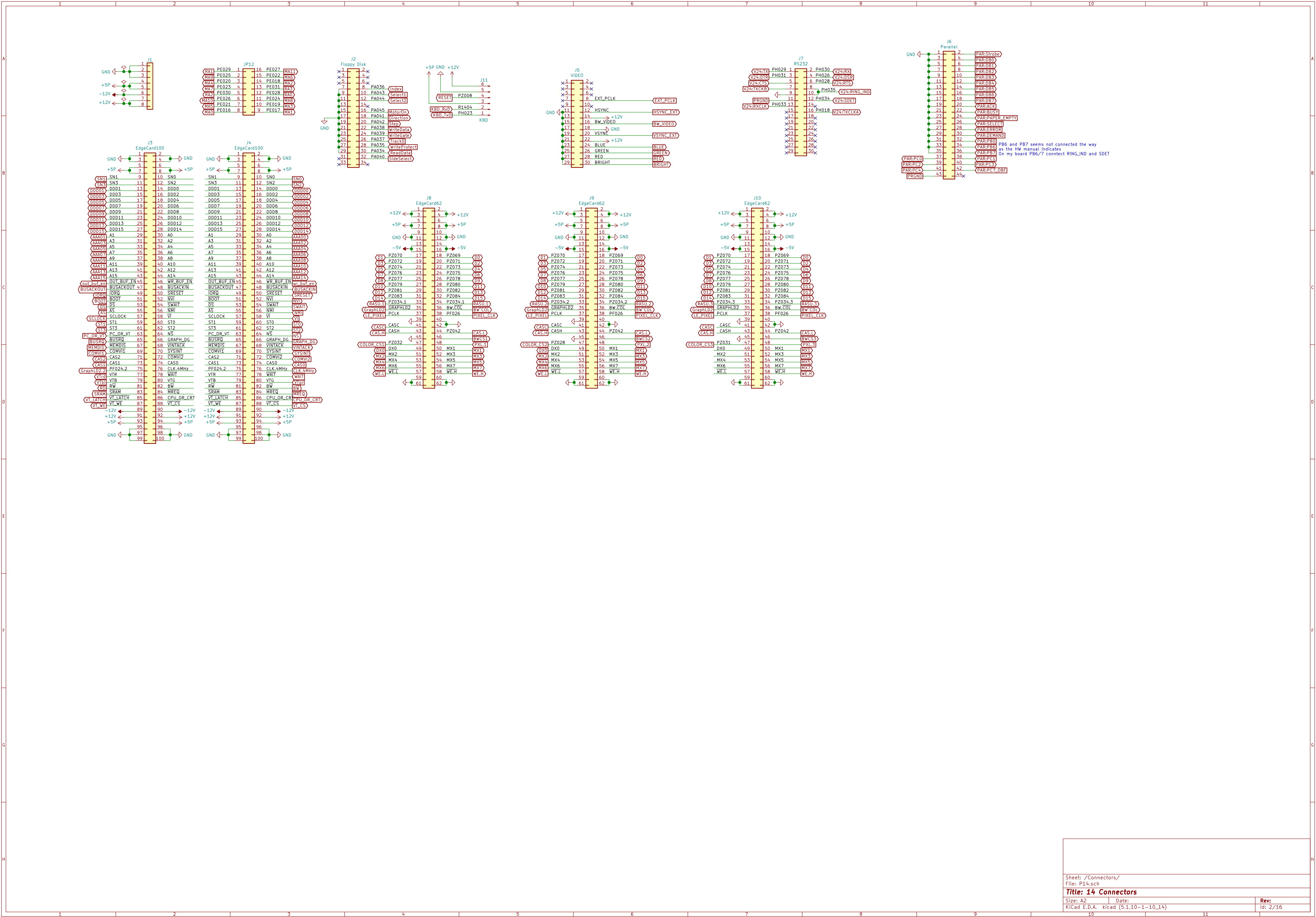
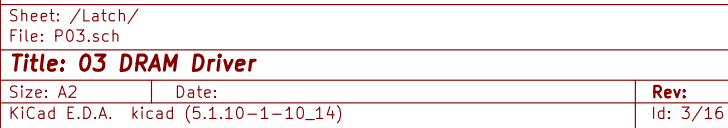
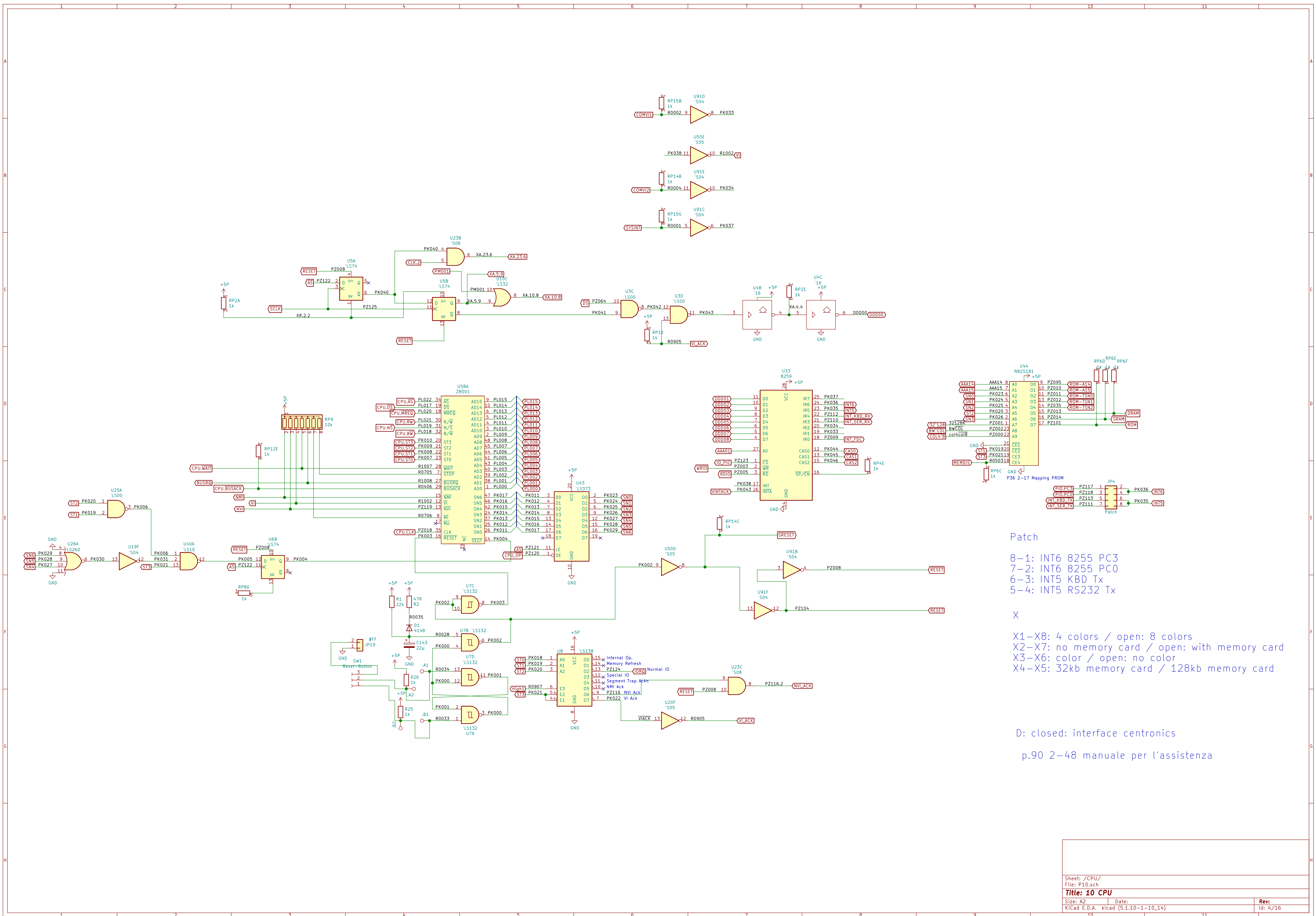


	1	2	3	4	5	6	7	8	9	10	11	
A	Sheet: FDC+Latch File: P01.sch	Sheet: Timings File: P02.sch	Sheet: Latch File: P03.sch	Sheet: DRAM File: P04.sch	Sheet: CRTC File: P05.sch							
B	Sheet: State File: P06.sch	Sheet: ROM File: P07.sch	Sheet: Serial File: P08.sch	Sheet: PIO File: P09.sch	Sheet: CPU File: P10.sch							
C	Sheet: AD_Mux File: P11.sch	Sheet: CLK_IO File: P12.sch	Sheet: Power File: P13.sch	Sheet: Connectors File: P14.sch	Sheet: Spare File: P15.sch							
D												
E												
F												
G												
H												
	1	2	3	4	5	6	7	8	9	10	11	







Patch

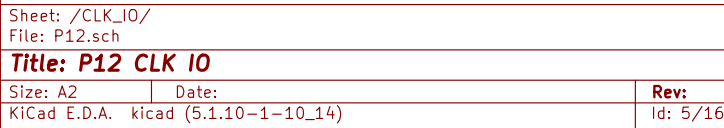
8-1: INT6 8255 PC3  
7-2: INT6 8255 PC0  
6-3: INT5 KBD Tx  
5-4: INT5 RS232 Tx

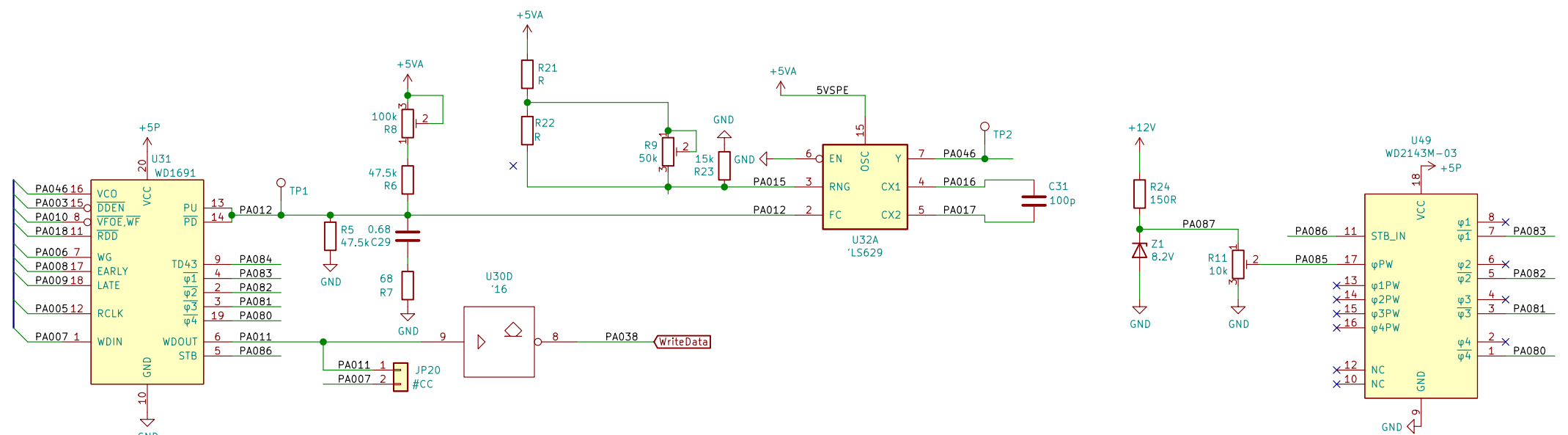
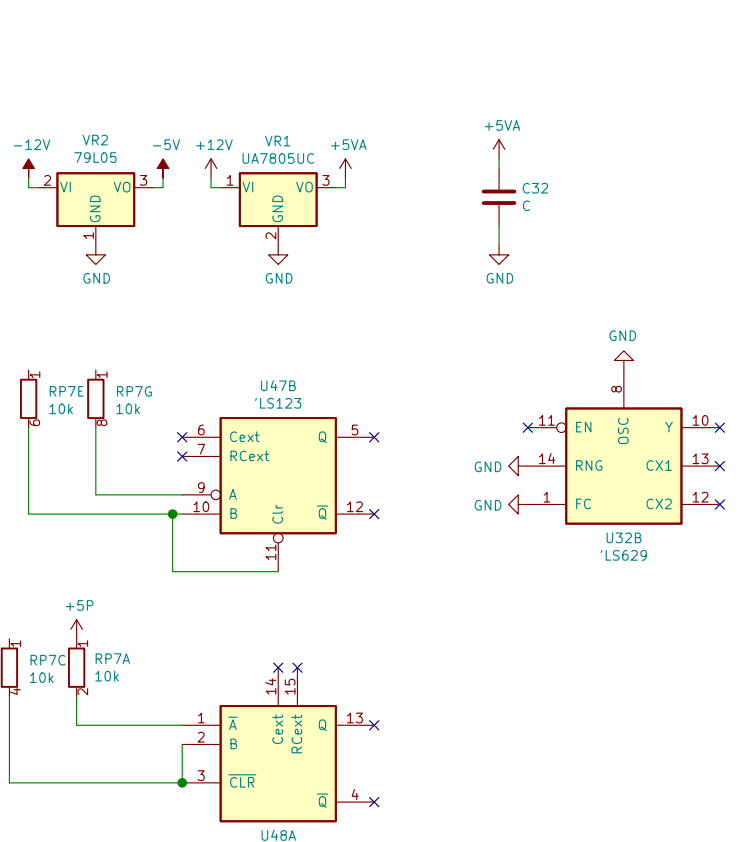
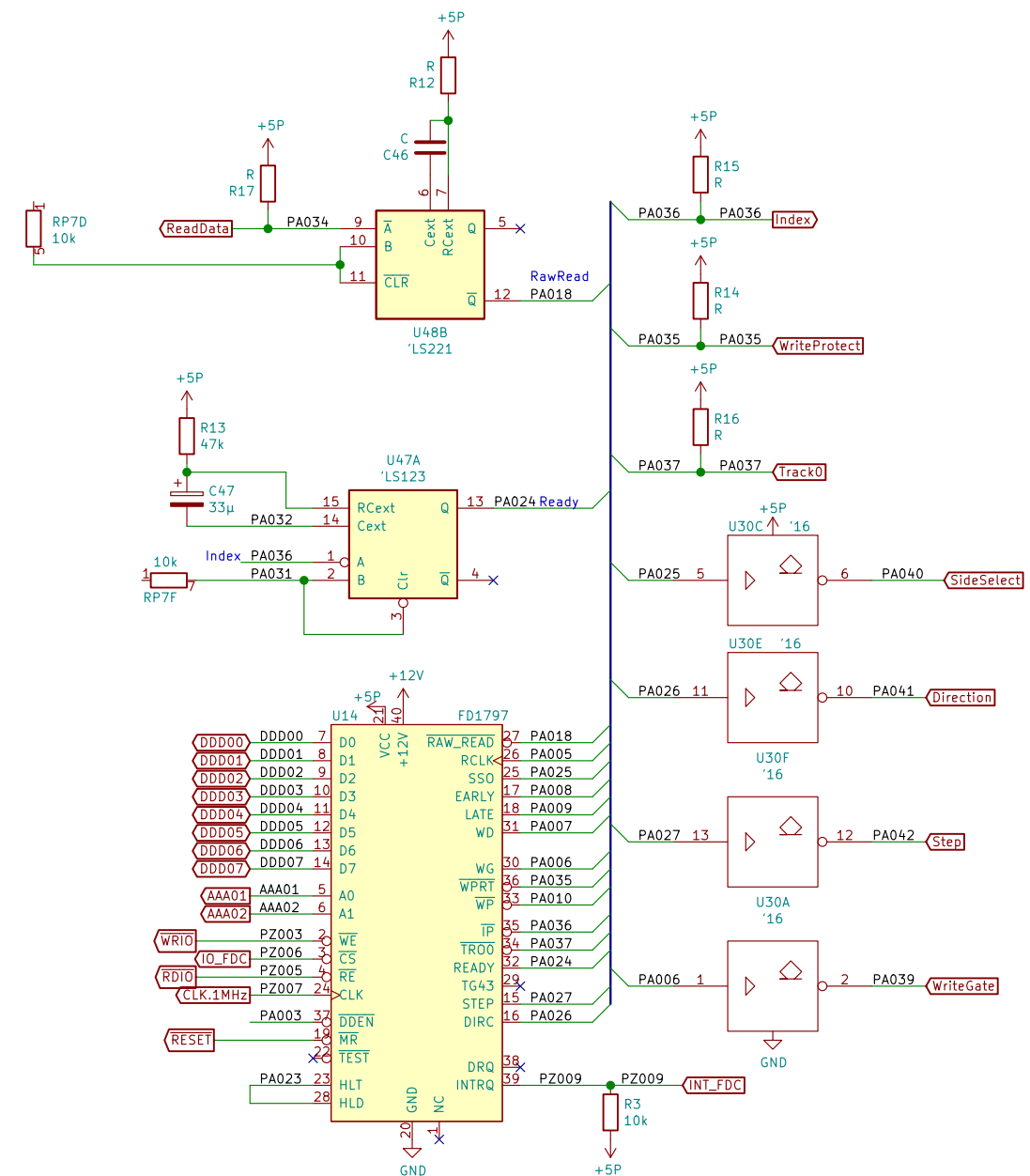
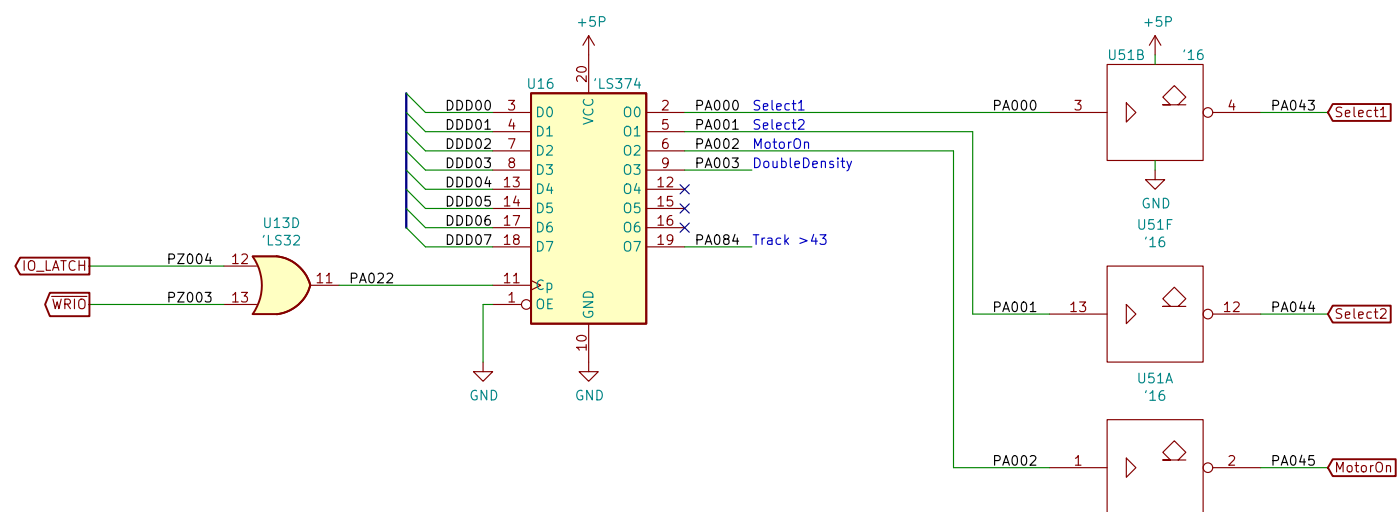
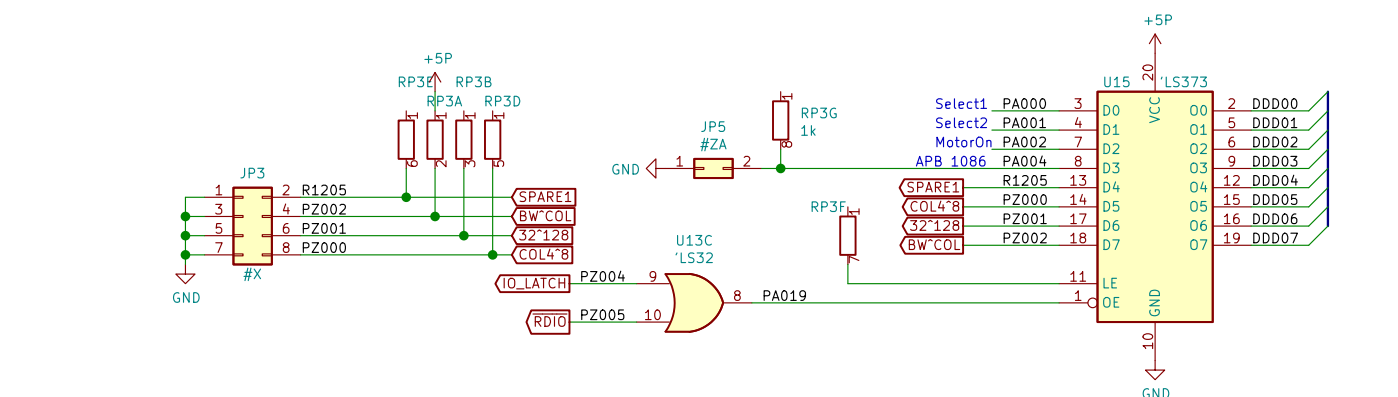
X

X1-X8: 4 colors / open: 8 colors  
X2-X7: no memory card / open: with memory card  
X3-X6: color / open: no color  
X4-X5: 32kb memory card / 128kb memory card

D: closed: interface centronics

p.90 2-48 manuale per l'assistenza





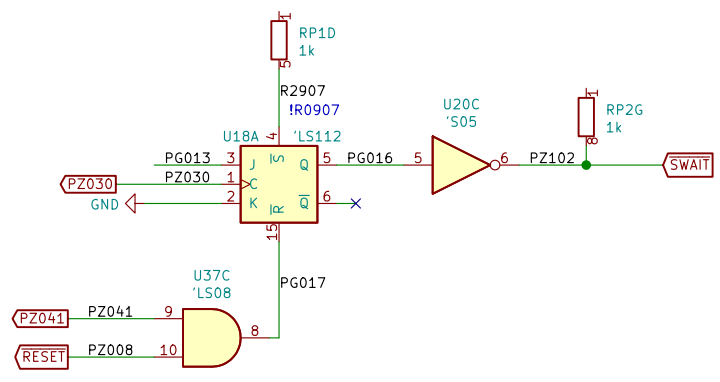


#K.1 - 1:memory slot 128 KB. 2:32KB or none  
#V.1 - 1:memory slot 32KB or none 2:128 KB  
#W.1 - 1:memory slot 128 KB. 2:32KB or none  
#L.1 - 1: single voltage 16 ic 2: three voltage 16k  
#J.1 - 1: 12V for 16k 2: 5V  
#H.1 - 1: -5V for 16k: 5V  
#ZA.1 1 bypass diagnostics 2: every time on start or reset

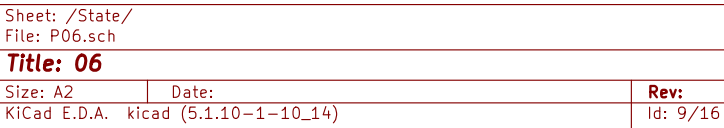
K1: 512K system ram  
K2: 128-224 system ram

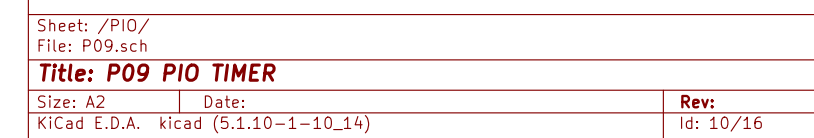
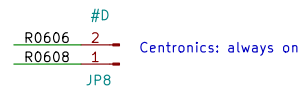
z1: m20 with or without extension board  
z2: not used

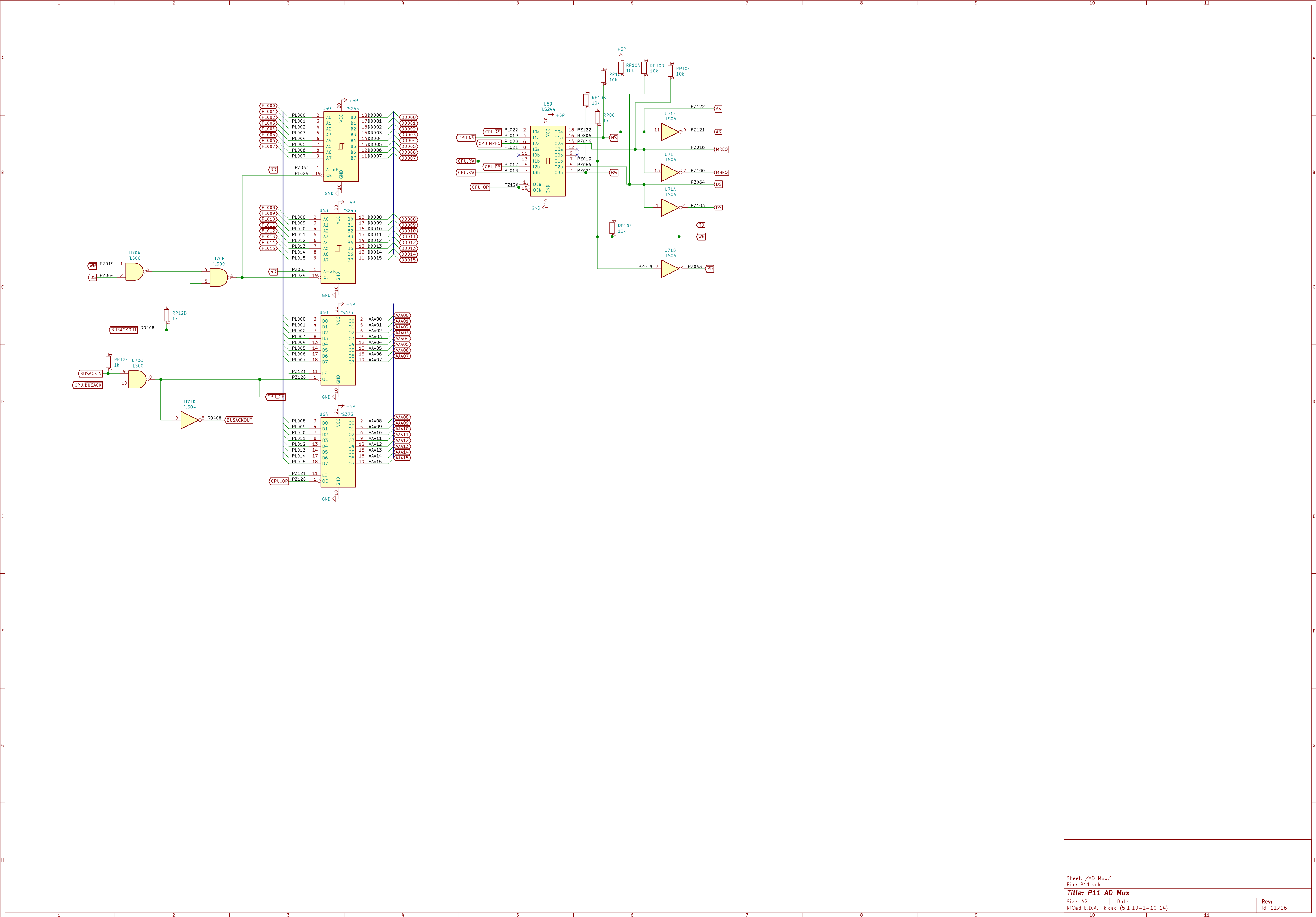
B1: dynamic rom MK3700  
B2: static rom

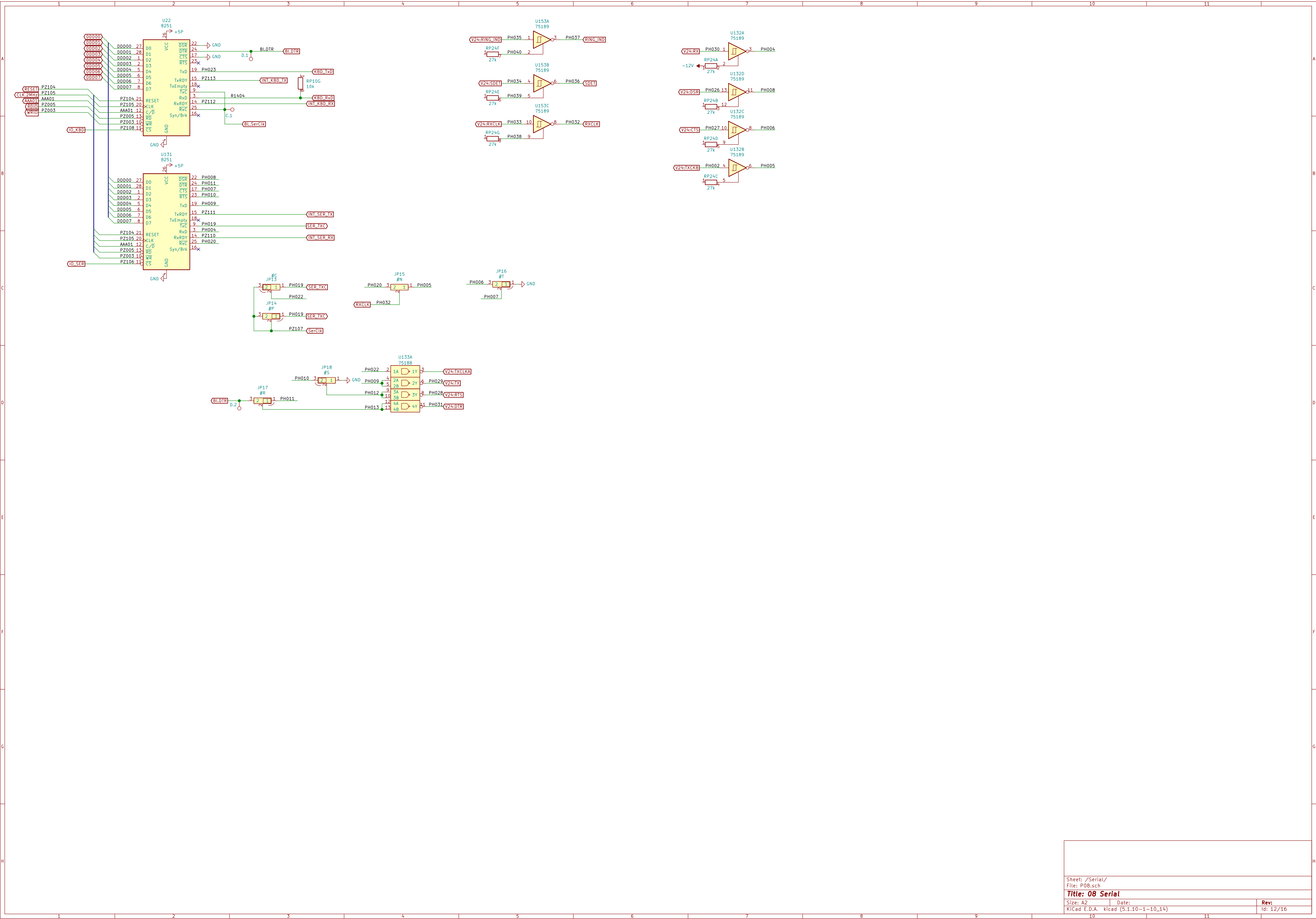














[illegible]



