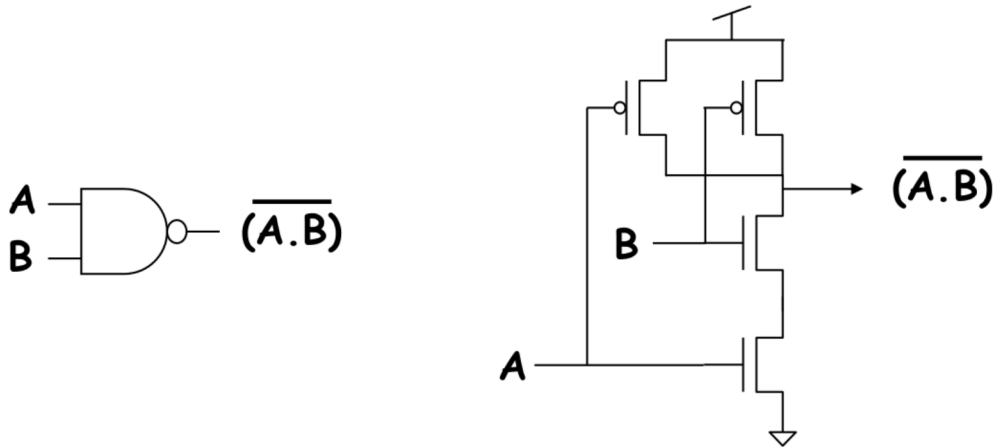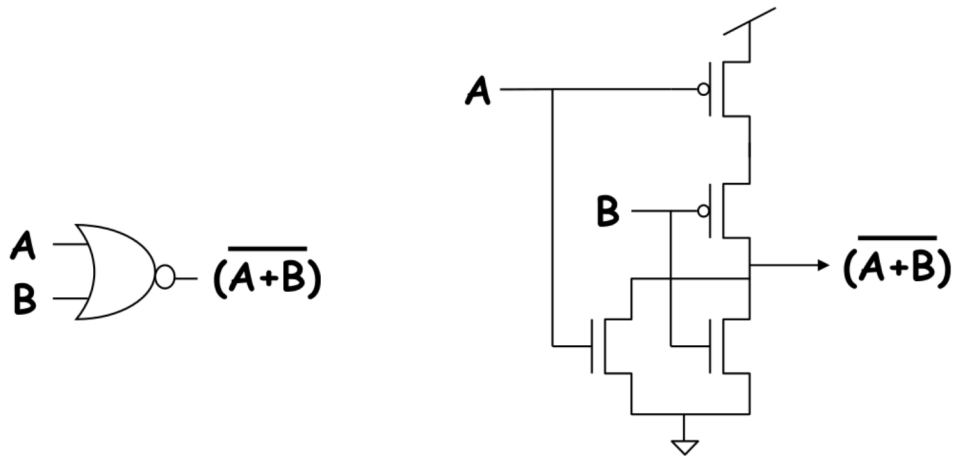# NAND Gate

- When both A and B are high, output is low
- When either A or B is low, output is high

NOR Gate

- When both A and B are low, output is high
- When either A or B is high, output is low