

MSheet

COVER.SchDoc

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CHGOK_ ACOK_ CHGEN_

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LTE_Rst Pow_K_LTE EN_LTE_POW_

WiFi_conection

Eth_RMII

NRES

Debug Board_ID Switch LED WD_EN WD_RES

Amplifier

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Eth_RMII

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Debug Board_ID Switch LED WD_EN WD_RES NRES

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U_Amplifier

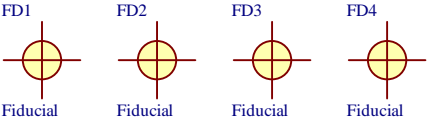
Amplifier Am_OUT

Audio.SchDoc

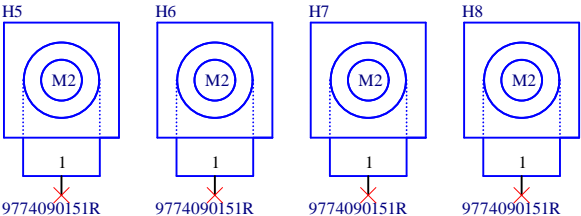
U_Audio

Am_OUT MIC_on_

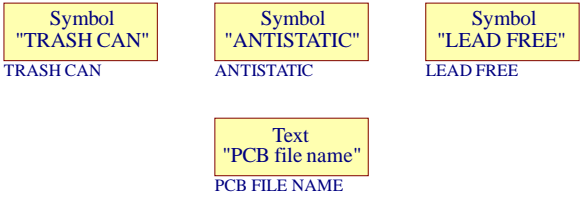
Fiducial marker



Mounting Holes



PCB Symbols



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DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

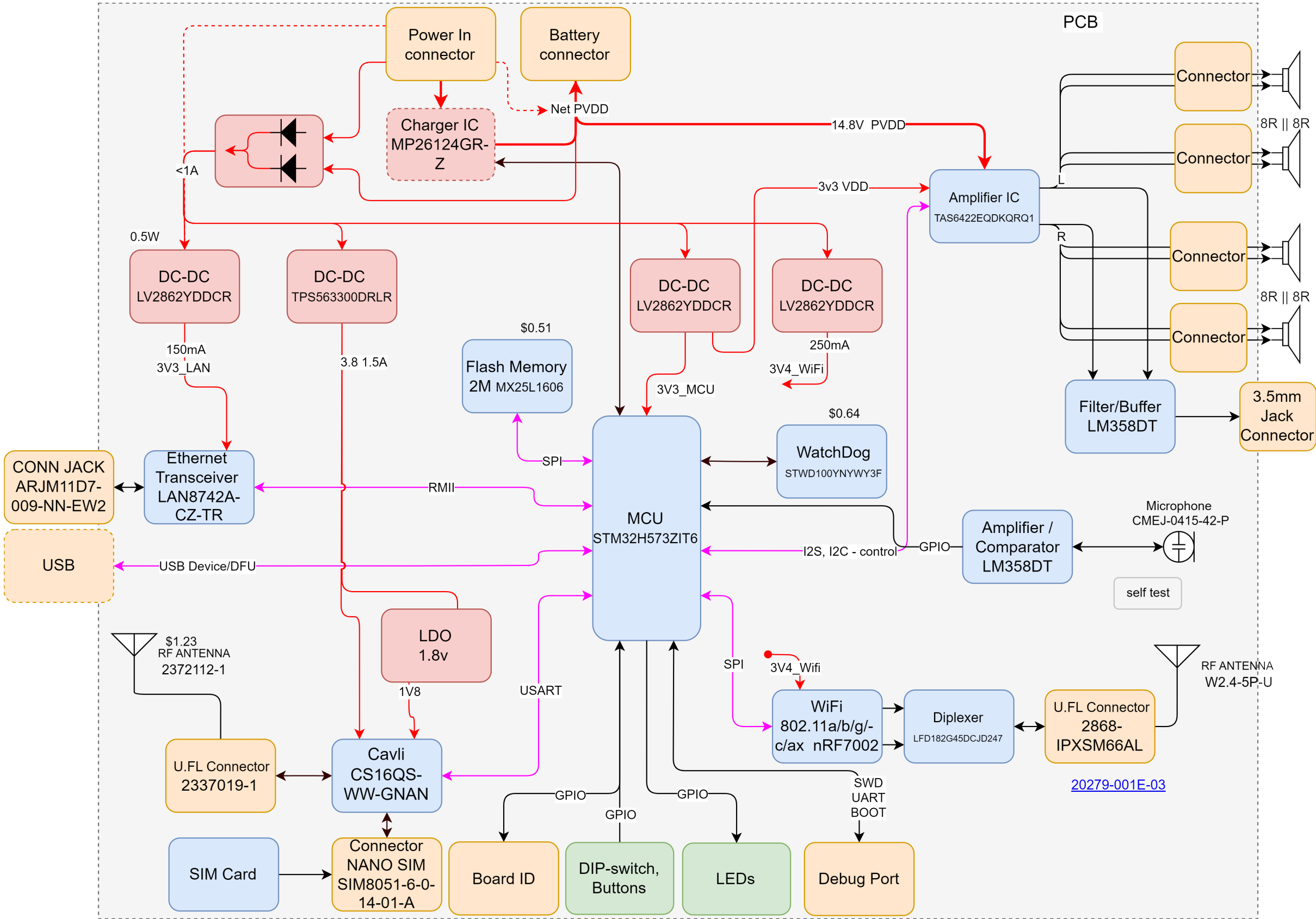
LAYOUT NOTE:
Example text for critical
layout guidelines.

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Rev. History							

Functional Diagram



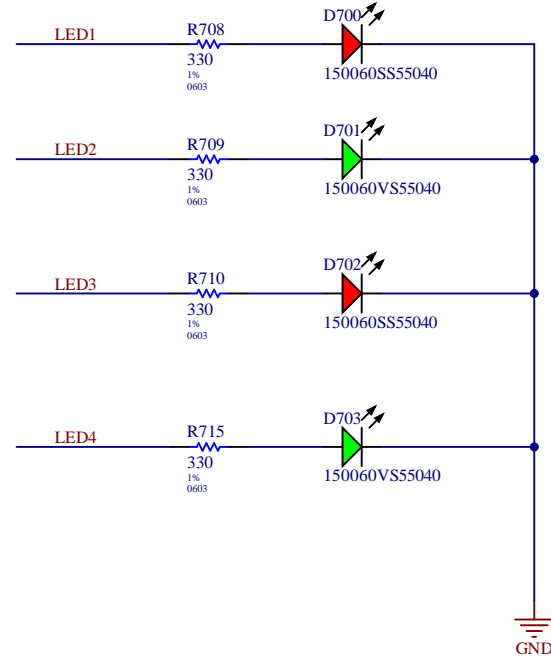
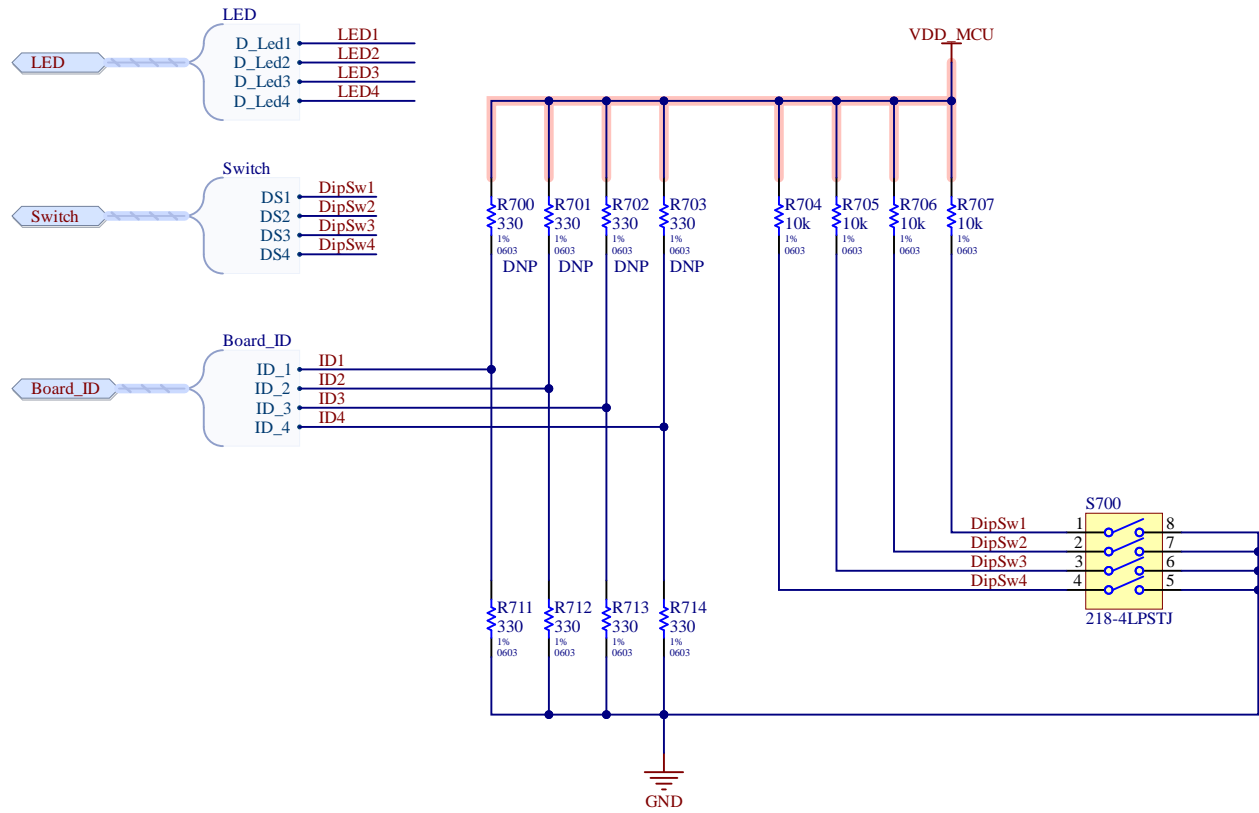
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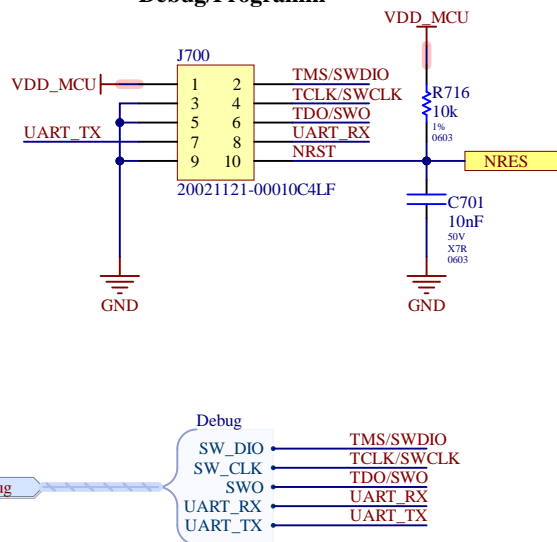
Power



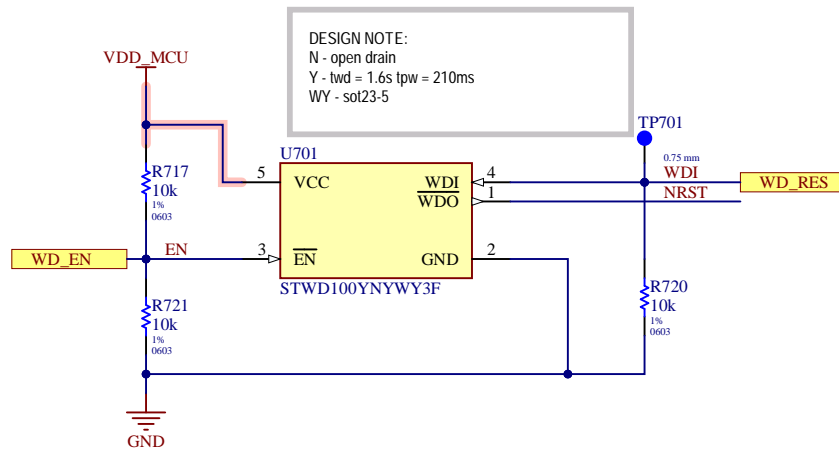
Debug



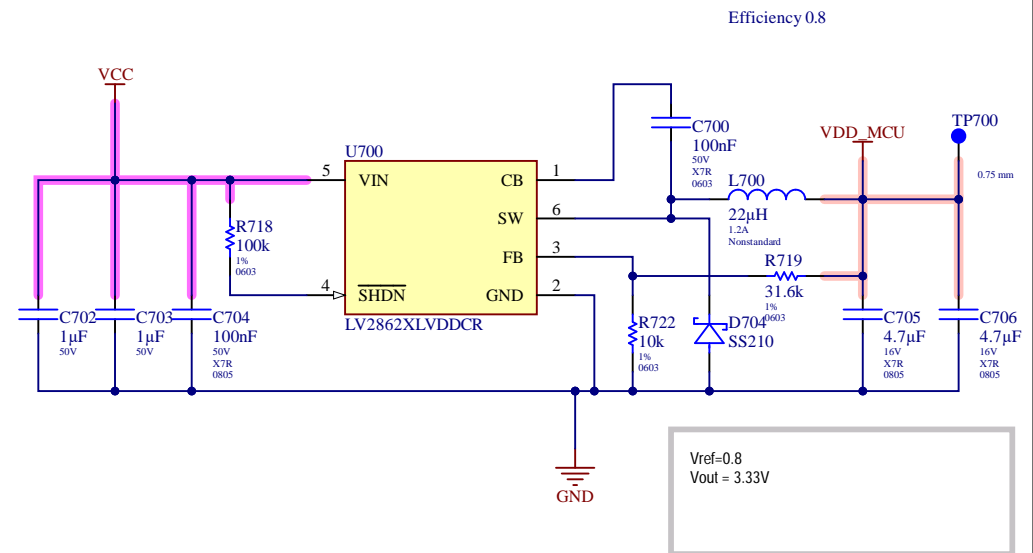
Debug/Programm



WatchDog

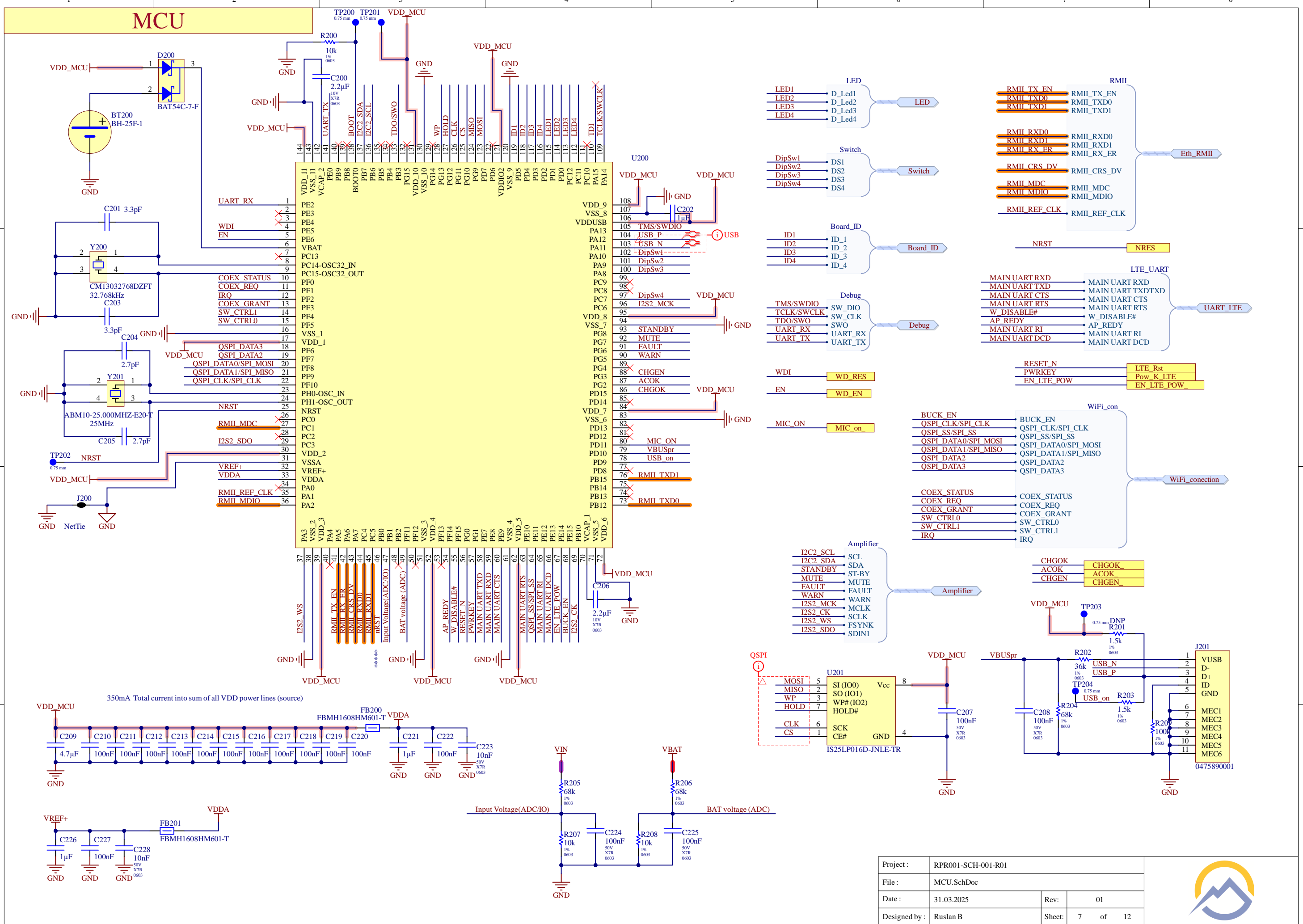


DC-DC MCU



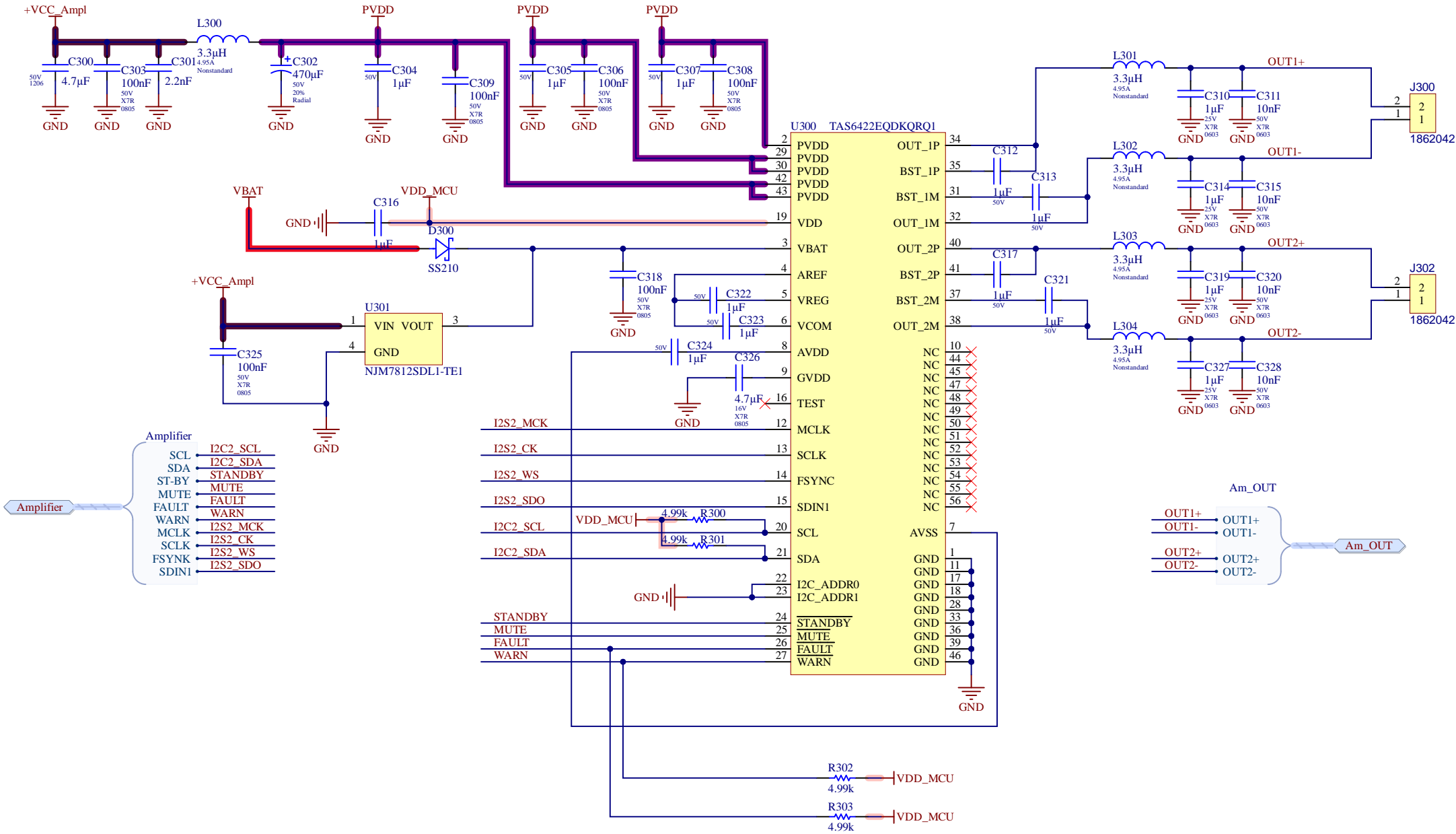
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Amplifier

Power efficiency 0.86

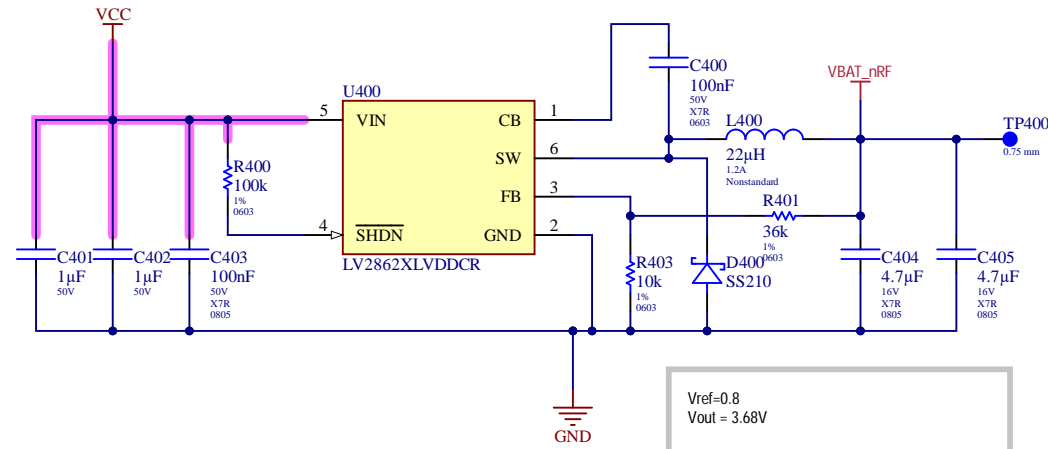


1	2
WiFi	

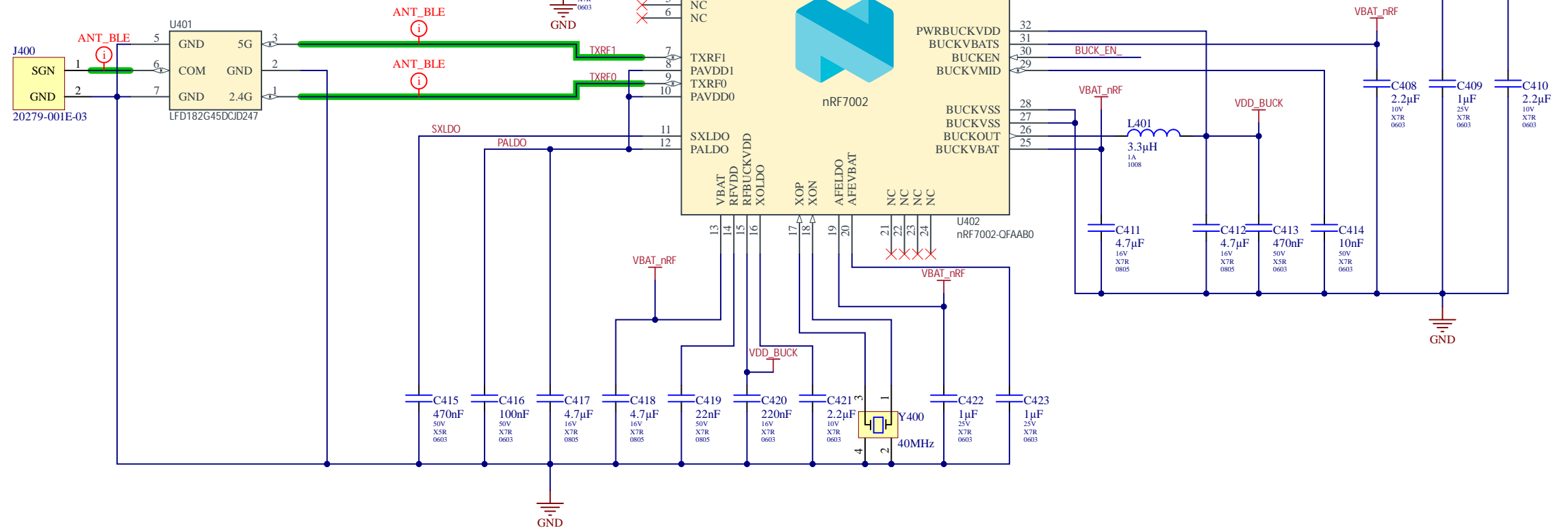
DC-DC

Efficiency 0.8

Maximum supply current - 260mA



Vref=0.8
Vout = 3.68V



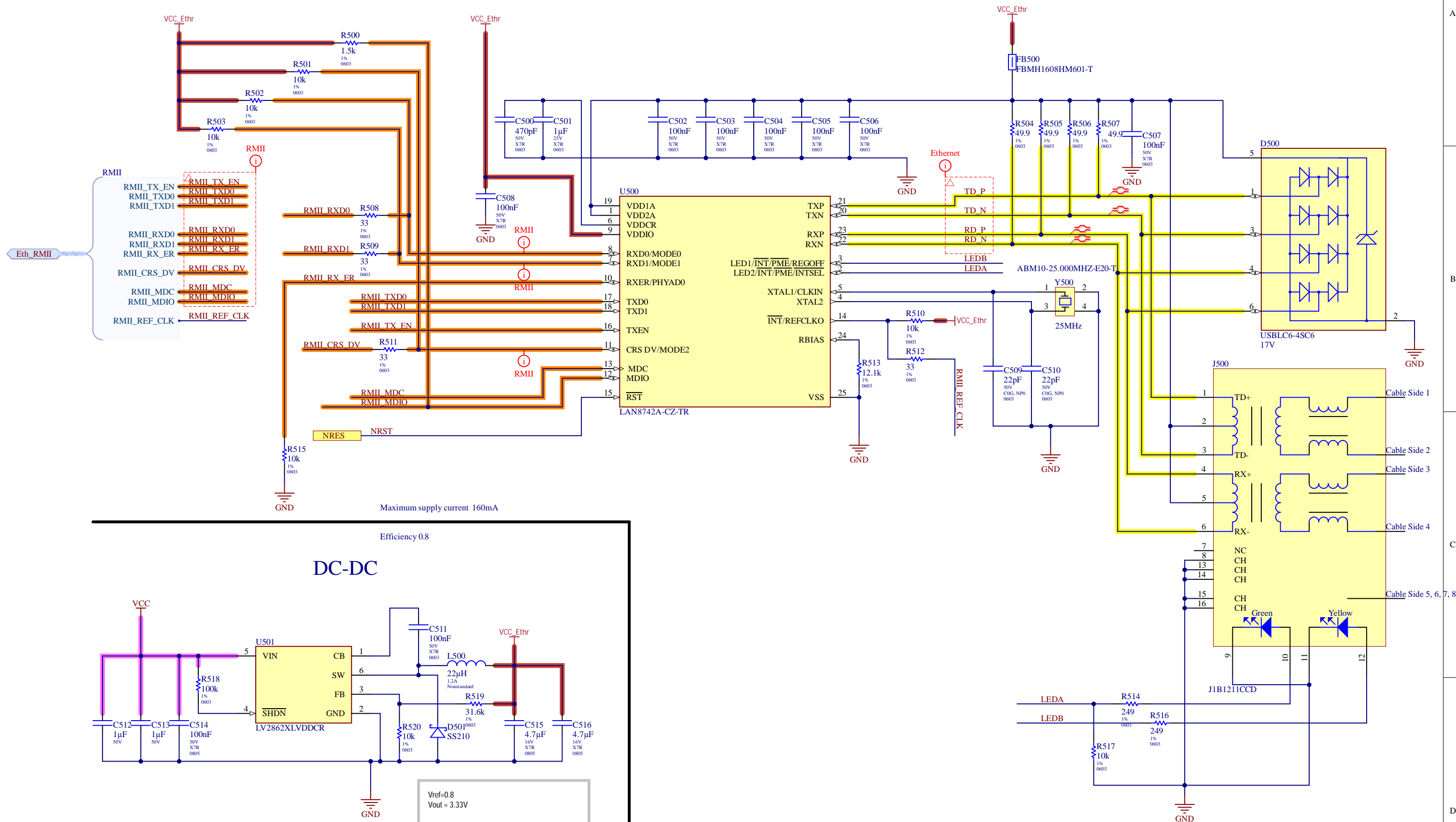
DESIGN NOTE:

- Supply VBAT/BUCKVBAT/BUCKVBATS/AFEVBAT
- Wait ≥ 6 ms
- Assert BUCKEN
- Wait ≥ 1 ms
- Supply IOVDD

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Ethernet

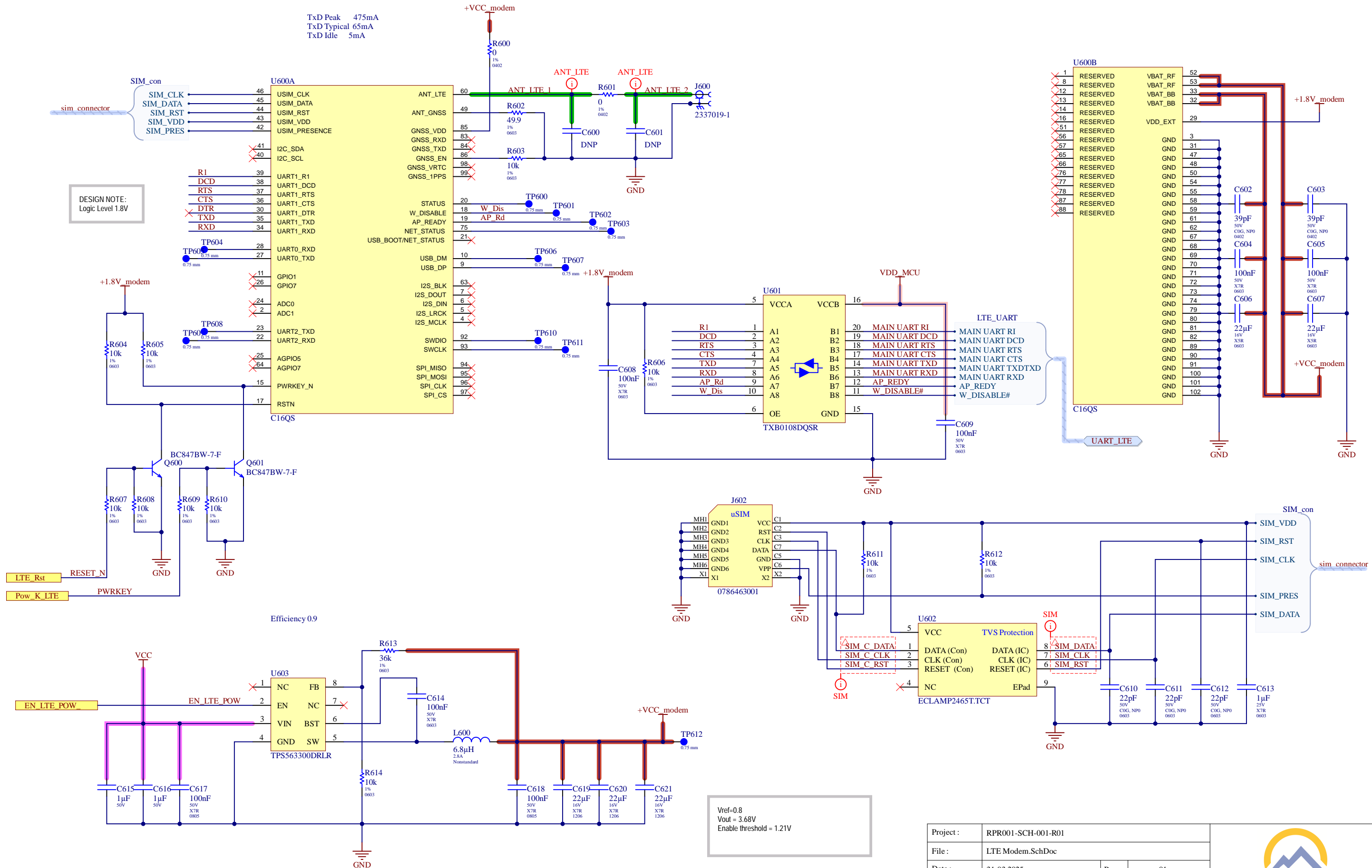


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LTE Modem

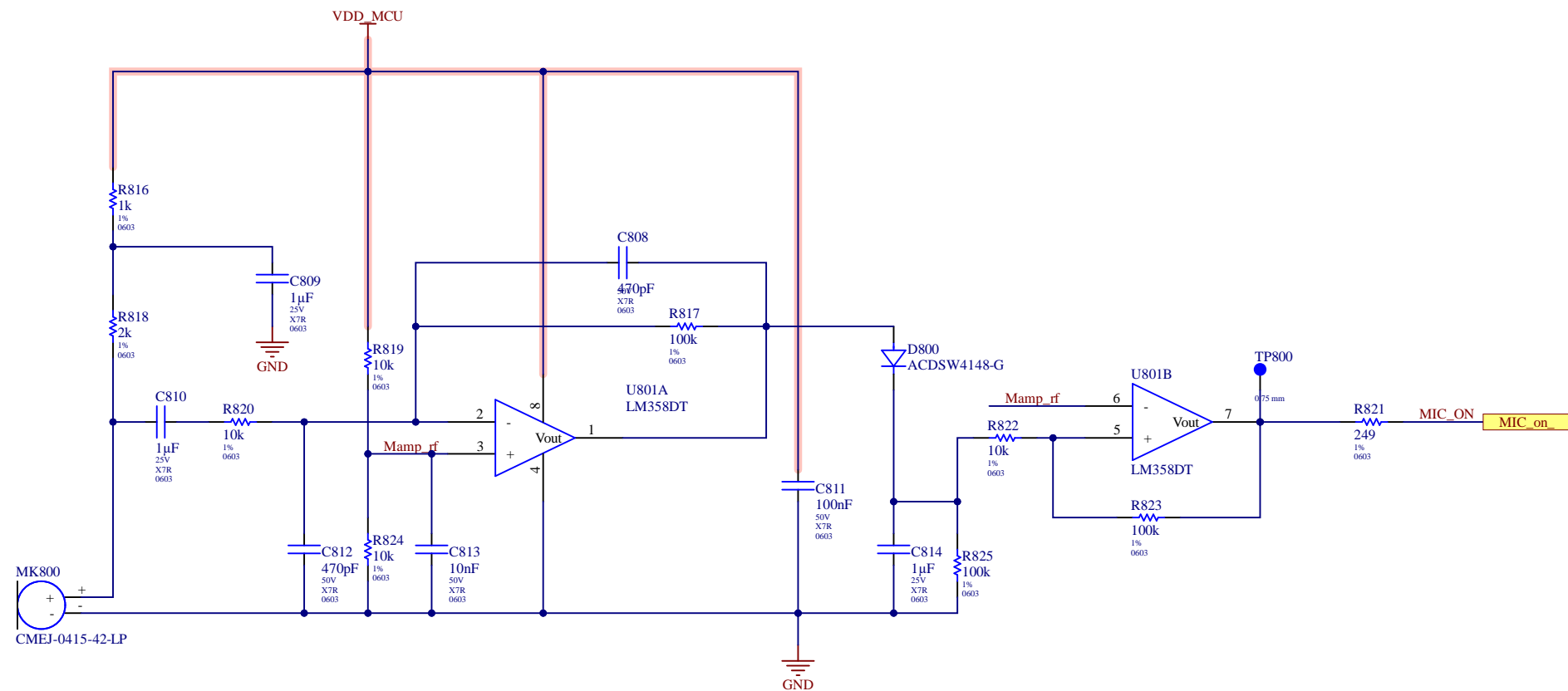
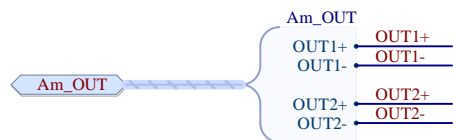
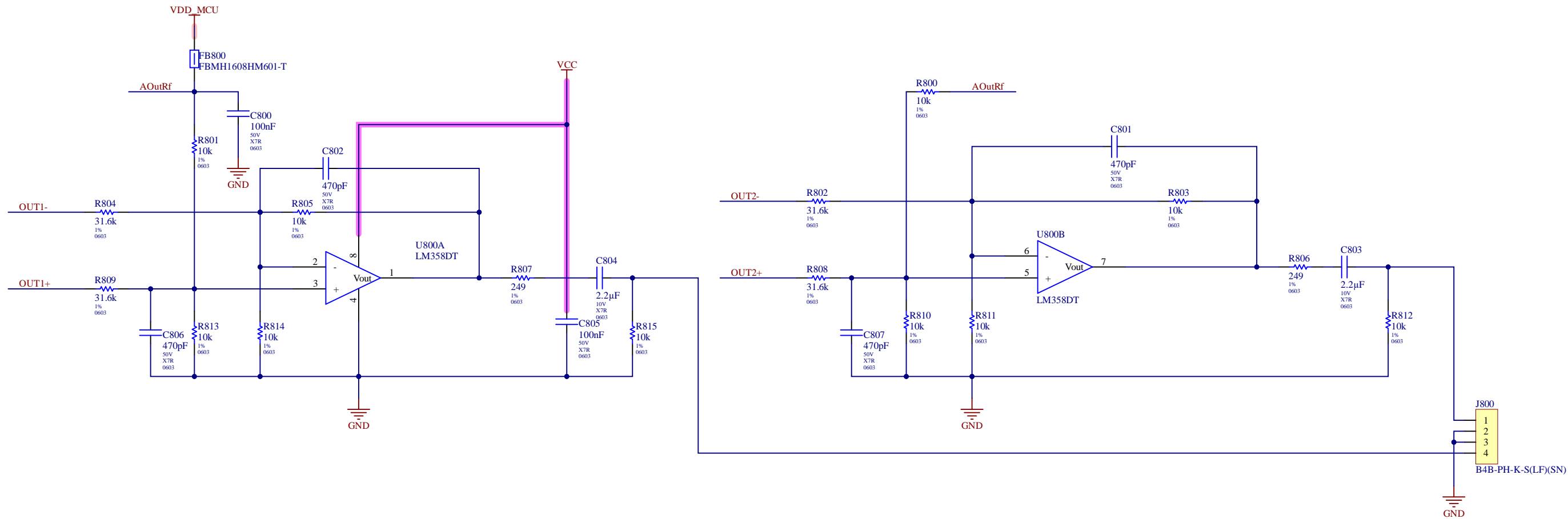
TxD Peak 475mA
TxD Typical 65mA
TxD Idle 5mA



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Audio



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