**Modular Circuits**

Sheet: /CPU/
File: cpu.kicad_sch

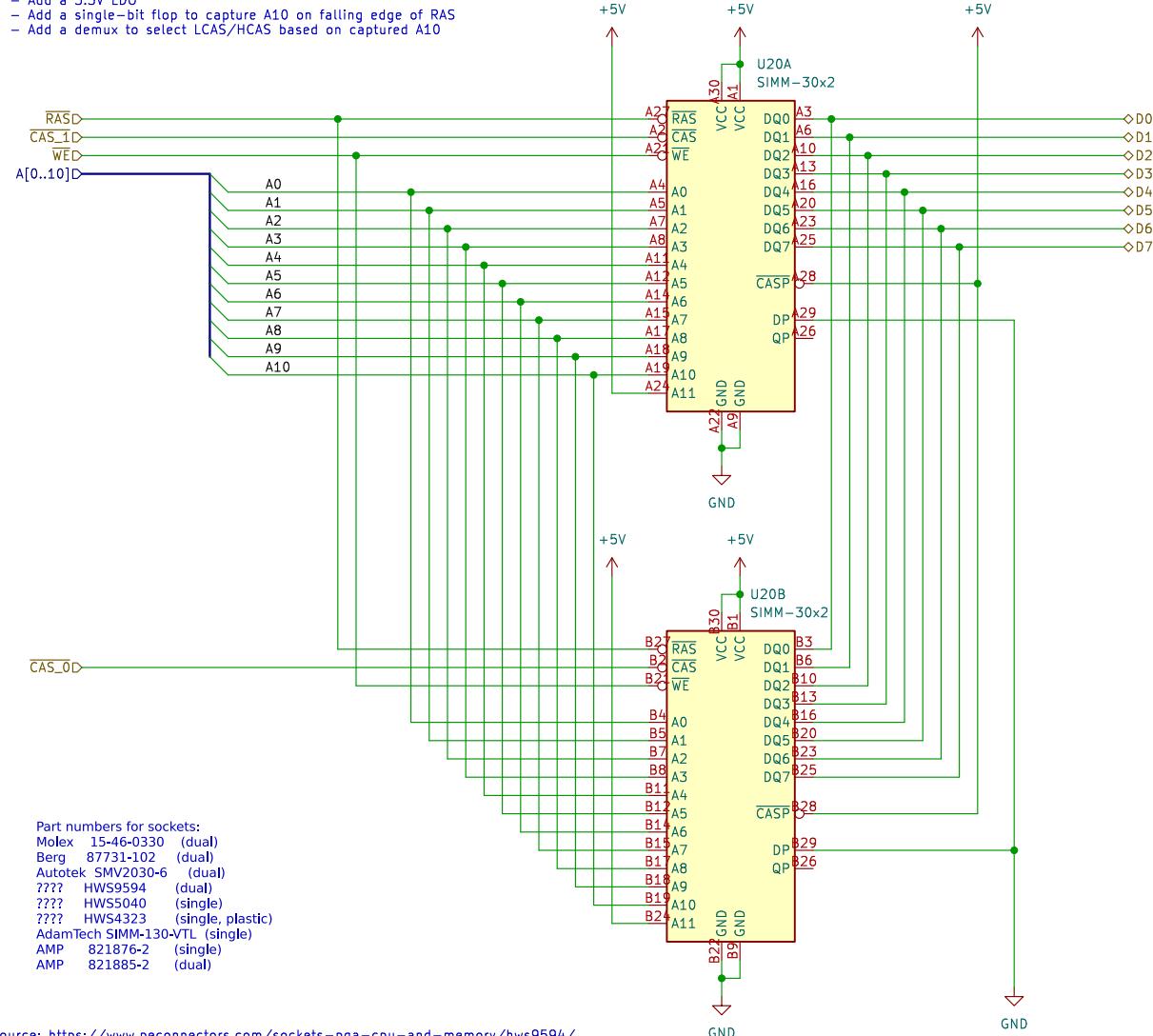
Title: Anachron uATX CPU

Size: A | Date: 2024-02-07
KiCad E.D.A. kicad 7.0.10-7.0.10~ubuntu22.04.1

Rev:
Id: 2/9

1 2 3 4 5

DRAM: There's one FPM DRAM from ISSI still in production: IS41LV16105D.
 This is a 1Mx16bit (3.3V) part. To mount it on a SIMM, one would need to:
 - Implement level-shifters
 - Add a 3.3V LDO
 - Add a single-bit flop to capture A10 on falling edge of RAS
 - Add a demux to select LCAS/HCAS based on captured A10



SIMM socket source: <https://www.peconnectors.com/sockets-pga-cpu-and-memory/hws9594/>

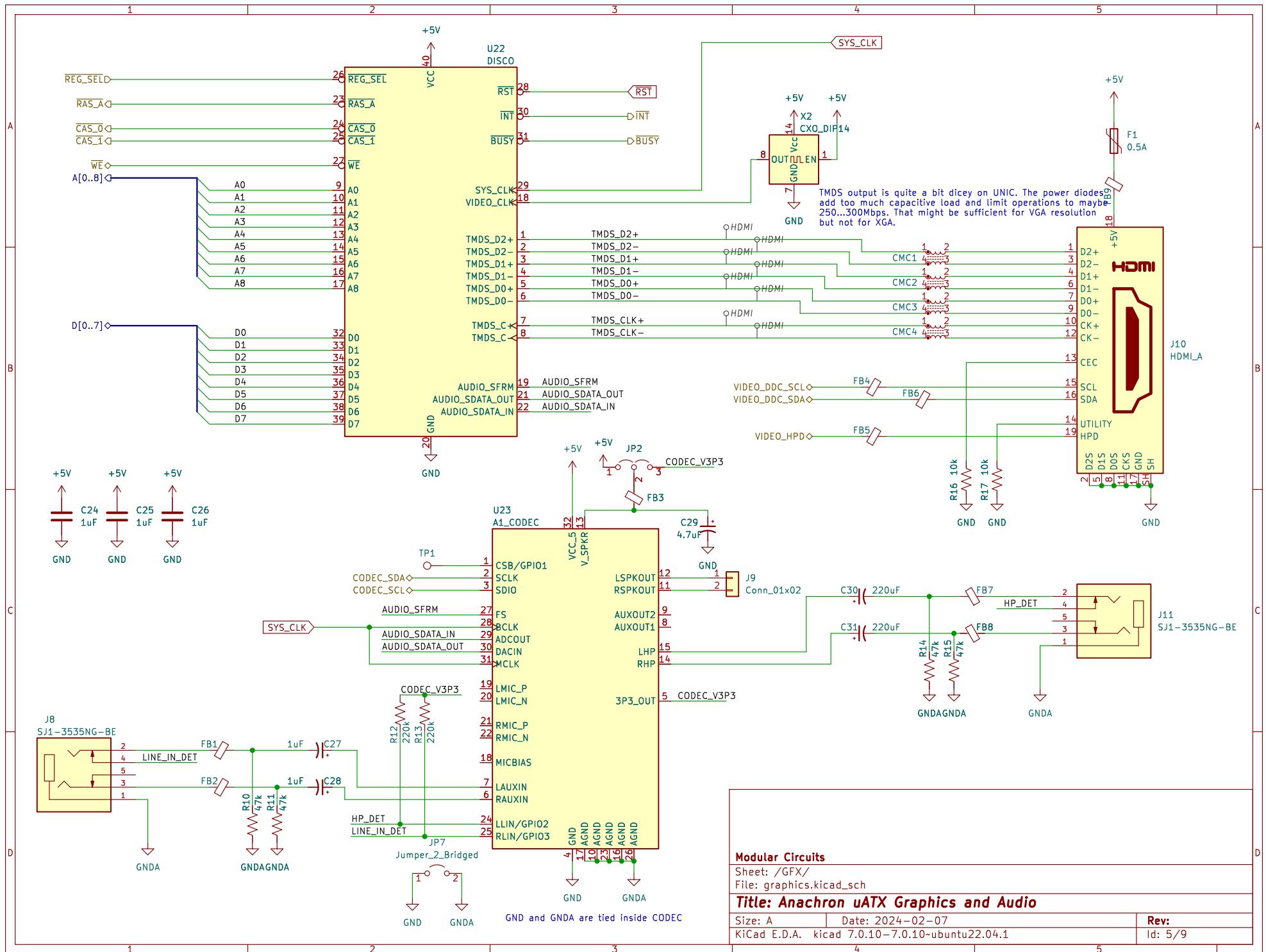
Modular Circuits

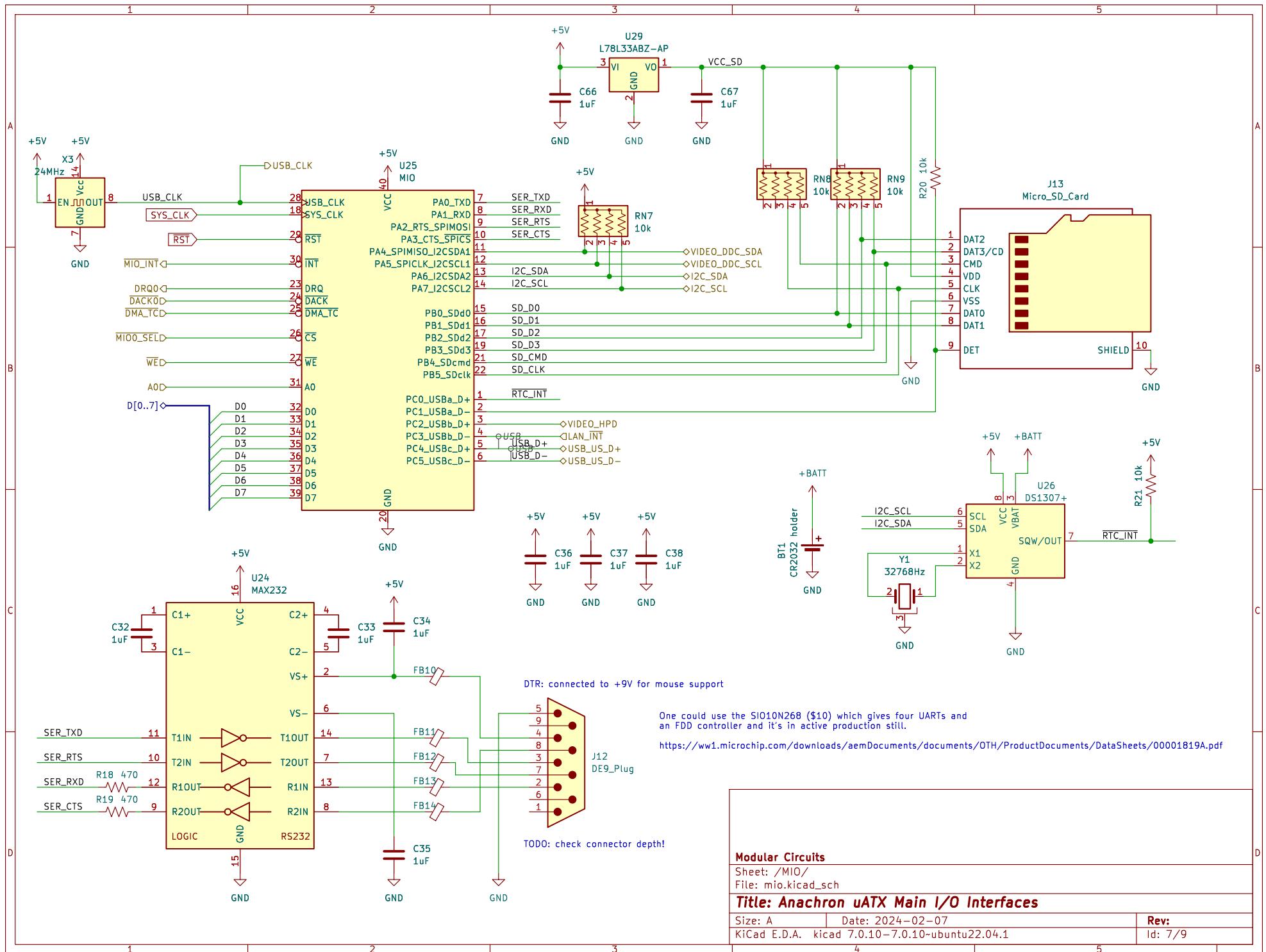
Sheet: /RAM_BANK/
 File: memory.kicad_sch

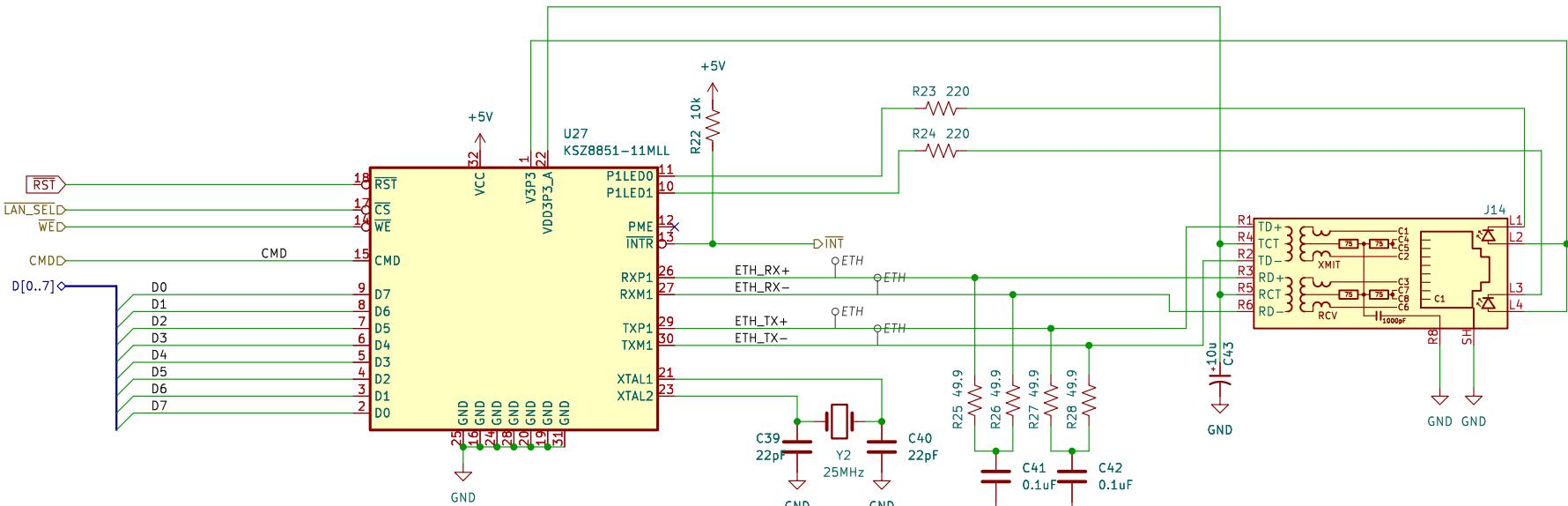
Title: Anachron uATX RAM Bank

Size: A	Date: 2024-02-07
KiCad E.D.A. kicad 7.0.10-7.0.10~ubuntu22.04.1	Rev: 3/9

1 2 3 4 5







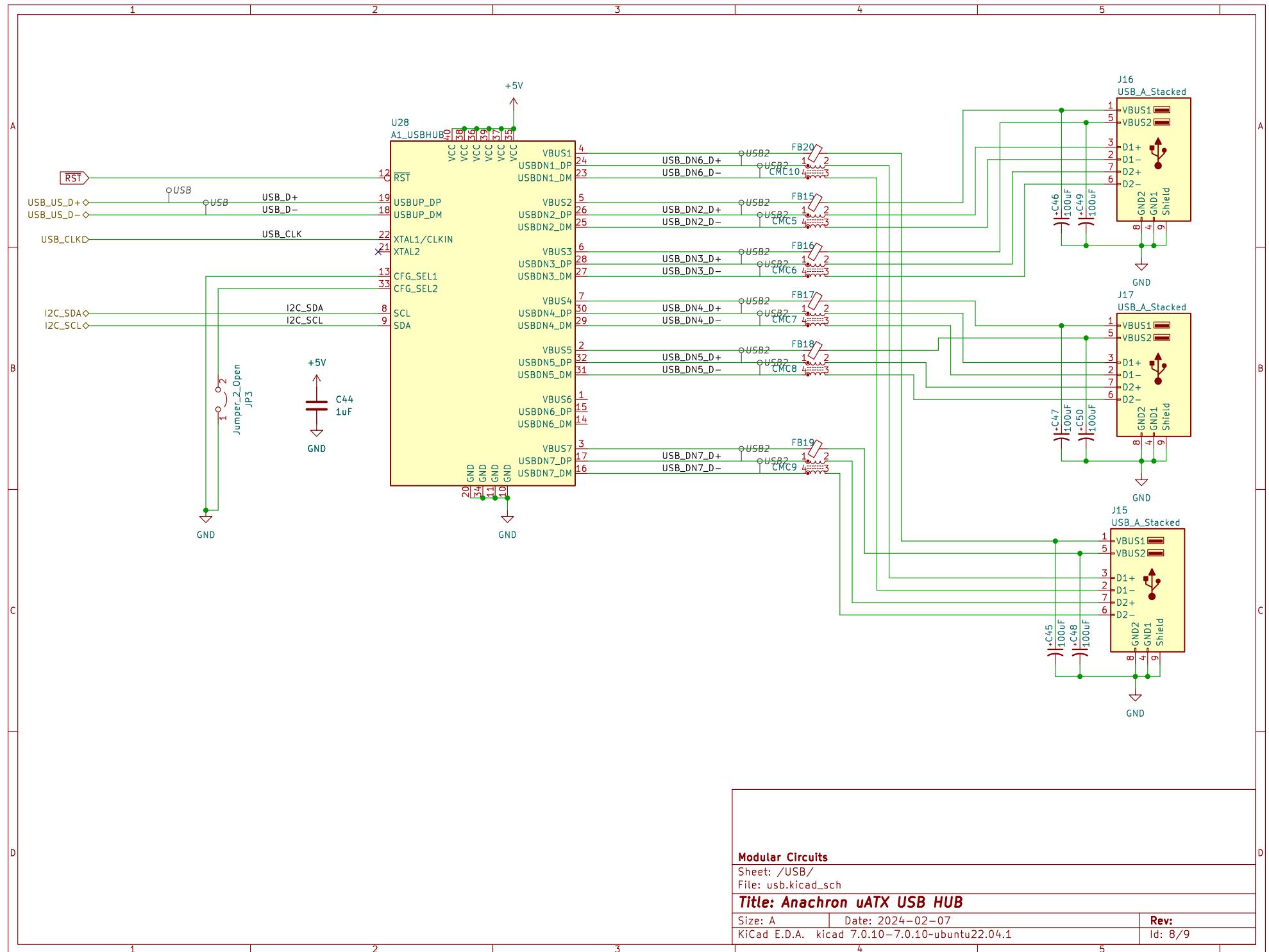
Modular Circuits

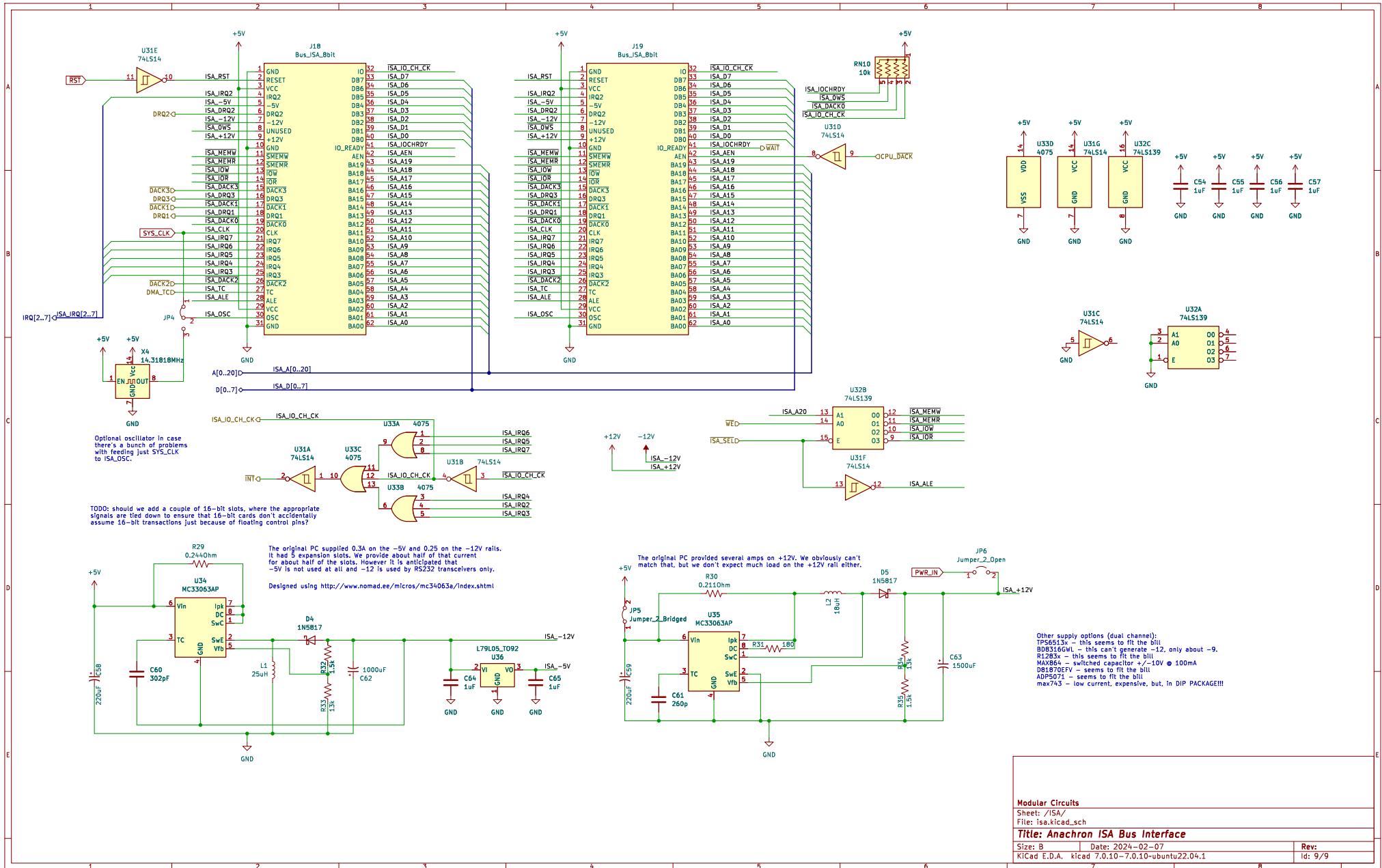
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File: ethernet.kicad_sch

Title: Anachron uATX Ethernet Interface

Size: A | Date: 2024-02-07
KiCad E.D.A. kicad 7.0.10-7.0.10~ubuntu22.04.1

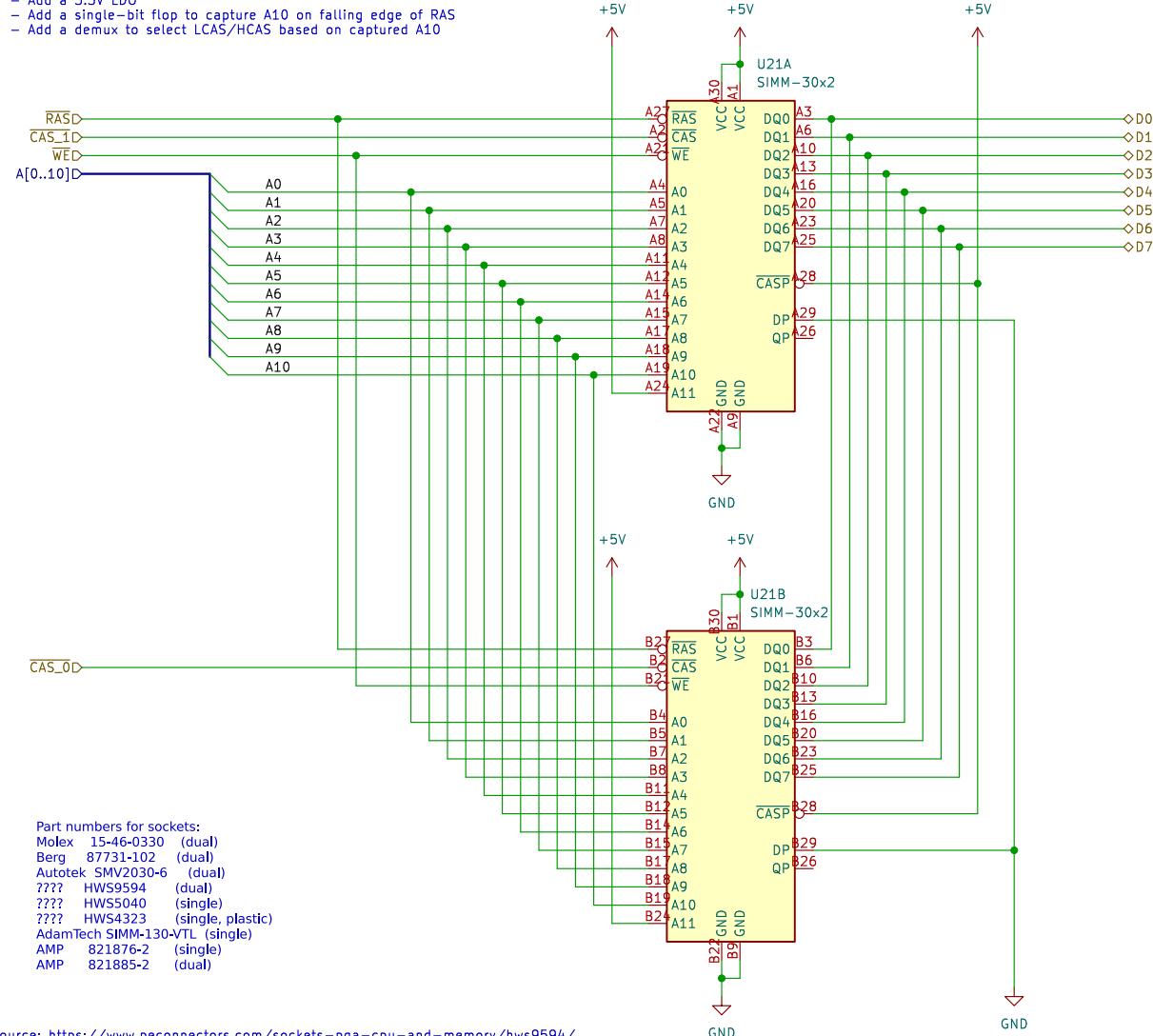
Rev:
Id: 7/9





1 2 3 4 5

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SIMM socket source: <https://www.peconnectors.com/sockets-pga-cpu-and-memory/hws9594/>

Modular Circuits

Sheet: /RAM_BANK1/
 File: memory.kicad_sch

Title: Anachron uATX RAM Bank

Size: A	Date: 2024-02-07
KiCad E.D.A. kicad 7.0.10-7.0.10~ubuntu22.04.1	Rev: 9/9

1 2 3 4 5