

**OBJECTIVE:** Seeking a full-time position as a computer engineer with a strong interest in computer architecture and embedded systems.

**EDUCATION:**

University of Michigan, B.S.E. Computer Engineering

April 2011

**JOB, VOLUNTEERING AND OTHER EXPERIENCES:**

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| U. of Michigan Class Development and Independent Research, June                                                                                                                                                                          | 2010 – Present     |
| <ul style="list-style-type: none"><li>• Implementing lab for new class, Advanced Embedded Systems</li><li>• Created library tracking robot</li><li>• Integrated serial drivers and tracking code to create end user experience</li></ul> |                    |
| U. of Michigan Mars Rover Project                                                                                                                                                                                                        | 2007 – Present     |
| <ul style="list-style-type: none"><li>• Senior Electrical Team Leader</li><li>• Developing control systems and communications</li></ul>                                                                                                  |                    |
| Infrastructure and Networking Analyst Intern, 454 Life Sciences,                                                                                                                                                                         | June-August 2009   |
| <ul style="list-style-type: none"><li>• Worked with people to solve a diverse set of problems</li></ul>                                                                                                                                  |                    |
| Michigan Student Artificial Intelligence Laboratory, RoboCup,                                                                                                                                                                            | 2008 – 2009        |
| <ul style="list-style-type: none"><li>• Developed “mental map” module for robot</li></ul>                                                                                                                                                |                    |
| Keck Biotechnology Laboratory, Medical School, Yale University,                                                                                                                                                                          | 2003/2006/2009     |
| <ul style="list-style-type: none"><li>• Created automated process for data manipulation</li></ul>                                                                                                                                        |                    |
| GraphLogic Inc., Branford, CT                                                                                                                                                                                                            | July - August 2004 |
| <ul style="list-style-type: none"><li>• Tested LIMS software and documented problems for software developers</li></ul>                                                                                                                   |                    |

**RELATED COURSES AND SKILLS:**

- EECS 470 Computer Architecture – Major Design Project
- Used Verilog to create an 8-way Hyper-Threaded processor
- EECS 427 VLSI Design – Major Design Project
- Designed a 16-bit RISC microprocessor
  - Used CAD tools to create schematic and layout of ICs
- EECS 570 Parallel Computer Architecture
- EECS 461 Embedded Control Systems
- Implemented microprocessor based control systems to interface with haptic device
  - Modeled of dynamic systems with MATLAB, Simulink and Stateflow
- EECS 373 Embedded Systems
- Hardware/software microcomputer interfacing with digital logic design/implementation, digital development equipment and assembly language
- EECS 370 Computer Organization
- Datapath and control for multiple implementations of a processor, including performance evaluation, pipelining, caches, virtual memory and IO
- EECS 312 Digital Integrated Circuits
- Analyzed and designed digital circuits in various logic families
  - Software lab work with HSPICE
- EECS 320 Intro to Semiconducting Devices, EECS 498 Smartphone Programming, EECS 280 C++ programming, EECS 270 Logic Design, EECS 203 Discrete Math

Programming and Software: C/C++, MATLAB, Simulink, Verilog, Assembly, MS Office (Word, PowerPoint, Excel), Maple, Mathematica