Objective

To design analog or digital circuits for industry projects and applications

Education

- B. S. Electrical Engineering
- Cumulative G.P.A.: 3.21/4.00
- Relevant Coursework: *Currently enrolled

EECS 312 – Digital Integrated Circuits* **EECS 215** – Introduction to Electronic Circuits EECS 451 - Digital Signal Processing* EECS 216 - Introduction to Signals and Systems

EECS 320 – Introduction to Semiconductor Devices* **EECS 230** – Electromagnetics I

EECS 285 – A Programming Language or **EECS 280** – Programming and Introductory Data Computer System* Structures

Work Experience

TARDEC Intern.....(June 2012 – August 2012)

Tank and Automotive Research, Development and Engineering Center

Engineering Aid (Vehicle power group)

- Presented technical information at a company event
- Research in power electronics for armored vehicles
- Designed alternator efficiency tests

Designing and implementing mobile apps/games on the Windows Phone and Android platforms Projects completed

- Color Coded [goo.gl/0VGw3]
- Binary Converter Pro [goo.gl/yrTmy]
- Olympic Games

High Tech Video TV Service Part time/ summer help......(June 2009 – August 2011)

Technician Assistant and Customer Service Representative

- Diagnostic and repair work on Plasma/LCD/DLP and Direct View Televisions.
- Assisted customers in scheduling of in home and in shop repairs and with various other customer service needs.

Technical Skills

Operating Systems: Windows, Linux, Mac OS X Programming Languages: C++, C#, Java, Assembly, Verilog

Applications: Cadence, MATLAB, Microsoft Office, Git, Visual Studio, Eclipse Laboratory Equipment: Altera DE2 Dev Board, Digital Mulit-meter, Function Generator,

Oscilloscope, Network Analyzer

Professional Organizations

MPowered Entrepreneurship.....(2010 – Present)

- Startup weekend team member (2010)
- Tech Team member (2010 - Present)

• Tech Team member (2010 - Present)
Engineering Society of Detroit......(2010 – Present) CSE Scholars.....(2011 – Present)

Tutored EECS students (2011)

Michigan Exploration Laboratory (MXL).....(2012 – Present)

Member of the hardware design team (2012)