

Sunil Iyer Sadasivan

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Objective

Seeking an internship position within embedded systems, computer architecture, operating systems and/or networks. Will be pursuing Masters of Science in Engineering (ECE) at Carnegie Mellon University (Pittsburgh, PA)

Education

Bachelor of Science in Engineering (Computer Engineering)
University of Michigan

Ann Arbor
May 2009

- **Major GPA:** [3.5/4.0] **GPA:** [3.4/4.0] [Senior Status] w. University Honors
- **Related Coursework:** Computer Networks, Computer Architecture, Operating Systems, Design of Microprocessor Based Systems, Data Structures and Algorithms, Databases, Information Retrieval, Java
- **Projects:**
 - Designed and synthesized a superscalar out-of-order (Tomasulo) execution processor with pre-fetching, load store queue with store forwarding, and local dynamic prediction with branch history table, pattern history table and branch target buffer. (In Verilog). (See tech specs on my homepage.)
 - Designed a POSIX based thread library, external pager and a virtual memory manager, and a secure multithreaded network filesystem (client-server implementation).
 - Designed a mechanical version of *Frogger* using the MPC823 processor. (www.umich.edu/~sunilis/brehobber/)
 - Created working compression/decompression program using Huffman, LZW/LZ78, and RLE algorithms
- **Responsibilities:** Grader for Computer Architecture (EECS 470)

Experience

Cisco Systems NSSTG Architecture Group
Software Engineering Intern

May 2008 - August 2008
San Jose, CA

Interned with the Cross Operating Systems group. The Cross OS group specializes in the building of APIs for the virtualization of the Cisco operating systems (IOS, IOSXR, FreeBSD, QNX). Led performance testing initiatives within the group to obtain specific bottlenecks and issues. Created several performance tests (on actual hardware) and resolutions to further enhance the groups APIs. Expanded test framework.

LEHMAN BROTHERS Infrastructure Engineering Support
Summer IT Analyst

May 2007 - August 2007
Jersey City, NJ

Employed at Lehman Brothers Information Technologies Headquarters. Worked with the Load Balancer Support group in dealing with the various concerns in load balancing algorithms, hot fix updates, Virtual IP provisioning, and configurations of load balancers. Created several scripting programs and macros to ease the workload of the team. Interacted with several high level managers within the finance company.

Lockheed Martin Information Technology
College Student Technical Intern

June 2006 - August 2006
Manassas, VA

Employed at LMIT's contract with the Northern Virginia Electric Cooperative. Collaborated with various IT professionals and computer engineers in helpdesk, networking, and server management issues. to give presentation of internship experience to the President and CEO of LMIT

Leadership

- Computer Science and Engineering Scholars Program (responsible for mentoring and tutoring fellow CSE students)
- Chinmaya Mission Cultural Organization (Volunteered in numerous charity events for 11 years)
- Webmaster for several campus organizations (examples linked on my personal webpage).
- Small Groups Facilitator for the 2008 South Asian Awareness Network conference.
- IASA Service Core: Lead role in organization of Gandhi Day (Volunteering event for 400 people)

Honors

- University Honors and Deans List
- Computer Science and Engineering Scholars Honor Society

Skills / Abilities

- Languages: C/C++, Java, PERL, Verilog, VB/VBA, SQL/Oracle, MATLAB, Alpha/MIPS/PowerPC Architecture
- Environments: UNIX, Ubuntu and Red Hat Linux, Windows, DOS, MAC OS. (90 WPM Typing)
- Applications: Synopsis VCS, Single Step, Visual Studio/.NET, Quartus, Gprof, GDB, CVS/SVN, VI
- US Citizen | DOB: 9/26/1987