

Cerebras Homework Screenshot

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andreadiaz@cer-login-03:~/f
2024-11-21 01:17:16,757 INFO: Exploring data layouts
2024-11-21 01:18:51,601 INFO: Optimizing memory usage
2024-11-21 01:20:30,064 INFO: Gradient accumulation trying micro batch size 64...
2024-11-21 01:20:44,030 INFO: Exploring floorplans
2024-11-21 01:21:00,890 INFO: Exploring data layouts
2024-11-21 01:21:53,115 INFO: Optimizing memory usage
2024-11-21 01:23:01,431 INFO: Gradient accumulation trying micro batch size 512...
2024-11-21 01:23:16,430 INFO: Exploring floorplans
2024-11-21 01:23:34,281 INFO: Exploring data layouts
2024-11-21 01:25:33,879 INFO: Optimizing memory usage
2024-11-21 01:26:46,090 INFO: Gradient accumulation trying full batch size 1024...
2024-11-21 01:26:59,925 INFO: Exploring floorplans
2024-11-21 01:27:09,069 INFO: Exploring data layouts
2024-11-21 01:29:19,519 INFO: Optimizing memory usage
2024-11-21 01:31:00,235 INFO: Gradient accumulation showed a benefit
2024-11-21 01:31:00,839 INFO: Post-layout optimizations for <batch=1024, lanes=11>...
2024-11-21 01:31:00,840 INFO: Post-layout optimizations for <batch=1024, lanes=9>...
2024-11-21 01:31:00,841 INFO: Post-layout optimizations for <microbatch=256, lanes=3>...
2024-11-21 01:31:00,843 INFO: Post-layout optimizations for <batch=1024, lanes=10>...
2024-11-21 01:31:00,848 INFO: Post-layout optimizations for <microbatch=512, lanes=6>...
2024-11-21 01:31:21,167 INFO: Allocating buffers for <microbatch=256, lanes=3>...
2024-11-21 01:31:22,666 INFO: Allocating buffers for <microbatch=512, lanes=6>...
2024-11-21 01:31:23,856 INFO: Allocating buffers for <batch=1024, lanes=10>...
2024-11-21 01:31:24,192 INFO: Allocating buffers for <batch=1024, lanes=9>...
2024-11-21 01:31:25,101 INFO: Code generation for <microbatch=256, lanes=3>...
2024-11-21 01:31:25,455 INFO: Allocating buffers for <batch=1024, lanes=11>...
2024-11-21 01:31:26,809 INFO: Code generation for <microbatch=512, lanes=6>...
2024-11-21 01:31:28,121 INFO: Code generation for <batch=1024, lanes=10>...
2024-11-21 01:31:28,436 INFO: Code generation for <batch=1024, lanes=9>...
2024-11-21 01:31:30,004 INFO: Code generation for <batch=1024, lanes=11>...
2024-11-21 01:31:57,927 INFO: Compiling image...
2024-11-21 01:31:58,043 INFO: Compiling kernels
2024-11-21 01:34:17,011 INFO: Compiling final image
2024-11-21 01:37:22,152 INFO: Compile artifacts successfully written to remote compile directory. Compile hash is: cs_18446360804057867627
2024-11-21 01:37:22,337 INFO: Compile was successful!
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