

# Andrea Lopez

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## WORK EXPERIENCE

### Intel Corporation – Graduate Software Engineer Intern

July 2017 - Sept 2017

- Investigated and selected best method for displaying manufacturing flow charts on internal websites to be adopted by 60 users
- Developed a proof of concept software tool that checked if servers, databases, APIs, inter-process communication framework tools, websites, and windows applications were fully functional after a monthly system patch, and reboot
- Researched methods for inserting data tables with dynamic attributes into databases
- **Technologies Used:** Wiki Enterprise, HTML, Visual Studio, C#, .NET Framework, SQL Server Integration Services, SQL Server Management Services

### Insomniac Inc. – Guest Services Assistant Manager, Ticketing Assistant Manager

June 2014 - Dec 2017

- Supervised teams, sizes ranging from 3-10, in ticket sales and ticket resolution
- Managed Lost & Found team of size 15 through 24-hour operations

### Cal Poly Pomona: Computer Science Department – Student Researcher

June 2015 - Aug 2015

- Created a smart pet door prototype that was a cost-effective substitution to existing pet doors, with mobile and intelligent capabilities

**Technologies Used:** Raspberry Pi 2, Arduino, Bluetooth beacon, linear servo motor

### Citrus College: Mathematics & Physics Department – SIGMA Mentor/Tutor

Aug 2014 - June 2015

- Strengthened student commitment to completing coursework by supporting them with STEM tutoring, as well as encouraging their participation in STEM-related activities
- Counseled students on their personal experiences and shared knowledge that aided their personal development

## PROJECTS

### BdayPal App - iOS Engineer

May 2019 - Present

- Designed, developed, tested, marketed, and launched a birthday app on the Apple AppStore
- App Store Listing: [bit.ly/BdayPalOnAppStore](https://apps.apple.com/us/app/bdaypal/id1444444444)
- Web Page: [bit.ly/BdayPalApp](https://www.bdaypal.com/)

**Technologies Used:** XCode, Swift, Sketch, AppStore Connect

### UC Davis Digit Image Recognition Team - Student Verilog Engineer

Jan 2017 - June 2017

- Designed a custom FPGA accelerator component that performed digit image recognition on sets of 500 images
- Improved the speed of pre-trained neural network by 40% through data compression, storage component selection, preloading data, and neural network optimization while maintaining 90% accuracy

**Technologies Used:** DE1-SOC Altera FPGA, Quartus II, MATLAB, C, Verilog

### UC Davis EEEmerge Team – Student Design Engineer

Sept 2015 - June 2016

- Constructed, wired, and programmed a 2 ft. x 2 ft. infinity mirror, which was activated by hand proximity sensors to provide an interactive exhibit

**Technologies Used:** TI micro-controller, Code Composer Studio, C/C++

### Citrus College Cosmic Research Team – Student Researcher

June 2014 - June 2015

- Improved balloon tracking accuracy by more than 50% with a high-powered GPS unit
- Conducted 4 high altitude balloon test launches that carried scientific payloads which collected temperature, altitude, & sound data to examine the earth's atmosphere conditions

**Technologies Used:** Arduino, Altimeter, Geiger counter, Recorder

## VOLUNTEER

[hackforla.org](https://hackforla.org/) HFLA Core Team – Organization Strategy and Events

May 2018 - Present

[playatech.io](https://playatech.io/) Playa Tech Board – Events

June 2018 - Present

## EDUCATION

Bachelor of Science in Computer Engineering, Emphasis in Digital Systems

December 2017

University of California, Davis

GPA: 3.48