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	PREPARED BY:	EQUIPMENT S/N:	PERFORMED:
	Gray, Andrea	DK-TM4C123G	5-FEB-2019

LABORATORY #4

Introduction to Interrupt I/O

OBJECTIVES

- Increase knowledge and experience with the DK-TM4C123G Development Kit
- Introduce Interrupt and timer peripheral concepts and implications
- Utilize timer and interrupt functions and constants in TivaWare© for the first time
- Continued use of the 'C' programming language and ADC

PURPOSE

The purpose of this laboratory exercise is to strengthen the use and knowledge of the analog to digital conversion while introducing the implementation of timer interrupts in the existing code. The value read in by the analog to digital converter, or ADC, is used to control the interrupts. It is necessary to use two (2) timers with individually processed frequencies. The foreground program blinky, the UART, and the OLED, which are implemented in all the laboratory assignments prior, is used to ensure the programming is running, to switch between display modes, and to display the program data respectively.

SPECIFICATIONS

To have a successful software project for this laboratory exercise a faculty member must sign off (Appendix C) after each of the requirements are met. The program, running on the DK-TM4C123G board under the debug mode, will meet the requirements if it is configured to utilize two (2) timer generated interrupts controlled using the analog input. The project must also be able to service interrupts using the DK-TM4C123G board and its service routines. The timers must be displayed showing one at a constant 1 Hz frequency and one at a varying frequency from 1 Hz to a user determined maximum frequency which is controlled using the potentiometer-controlled ADC.

PROCEDURE

For this procedure, the finalized software project from laboratory exercise three is used as the base/source code with additions from the timer example project, timers.c (incorporated into this project using the #include statement shown in the Report Discussion 1.f). Since the timer example project is not used as the base file for this procedure, it was necessary to change the interrupt vector table contained in startup_ewarm.c.

Process:

The process for this procedure is described below in the general timeline shown by the numbering below. The code retrieval is not included as a main step as it is described above and implied that it is already set up before the process for this exercise begins. Also, the splash screen for this procedure is

maintained from the previous laboratory exercise and was not altered except for the title which depicts the change in team structure and the display of the SysCtlClockGet() function.

1. For this program, the code from the previous lab was cleaned up and sectioned out into separate functions to clear and organize the main function. The main function now only holds variable initializations, OLED and Clock initialization function calls, interrupt enable and disable functions, timer and peripheral set up functions, and the main infinite while loop. The infinite while loop, with interrupts enabled before the loop itself, consistently checks whether foreground programs like blinky and the flood character display should be enabled or not.
2. The main function's reorganization made is so many new global variables were created. These global variables were used because a lot of the variables are constantly being passed between functions.
3. Once the reorganization and set up of the software program for this exercise was complete, the next step was to configure the different operating frequency in SysCtlClockSet() with 16 MHz and 66 MHz and to initialize each of the peripherals. In this exercise the peripherals for the timers had to be set up along with the UART, ADC, and GPIO peripherals. These were set up in the main function and since the splash screen was not altered the next step was to modify the blinky call so that it was not affected by the interrupts.
4. Ensuring that the blinky call was not affected by the timer interrupts was not very difficult, as the only thing that needed to be done was ensure that the timer interrupt handler consistently checks for a UART character input if the user wants to turn the blinky on or off. Otherwise blinky will remain in the state that it is set to and continue to blink along with the passing of each infinite while loop iteration. The infinite while loop must be called continuously and therefore the blinky must maintain a steady 'heartbeat' to ensure visually that the program did not get stuck in an ISR.
5. While the timer interrupts were outlined by the timers.c file used, there were some trivial modifications that were needed to be done to ensure that this laboratory exercise executed properly. The interrupt service routine needs to display the SRV results, test the UART for character inputs and process them, and read the ADC value when necessary (using ADCDataGet() and ADCIntStatus()) and display the appropriate results. For this the value displayed to the OLED had to be created and assigned to change with the clock cycles of the program, the interrupt status needs to be continually updated to not interfere with processes such as writing to the OLED (GRStringDraw, GrRectFill, etc.) and process the ADC with the potentiometer input.
6. A counter variable is included in this program which can be enabled and disabled by a user input. The timer frequency is also controlled using the TimerLoadSet() function with its third argument being SysCtlClockGet() for both timer 1 and 0. SysCtlClockGet() is in sysctl.c on line 2741 and it sets up the clock functionality and determines how many clock cycles the timer will wait before asserting an interrupt signal.
7. Configuring the display modes to the OLED was by far the most difficult part of this procedure. The display mode control is in the timer 0 ISR (displaying the requested number of operations per second and the serviced or actual number of operations per second) and uses UART to cycle through the display mode. It is necessary to not have any of the display mode controlling be done in the foreground so that it is not prone to starvation by high frequency interrupts.
8. Once it was ensured that the code all runs smoothly and as expected the sign offs were obtained and the lab was completed.

REPORT DISCUSSION

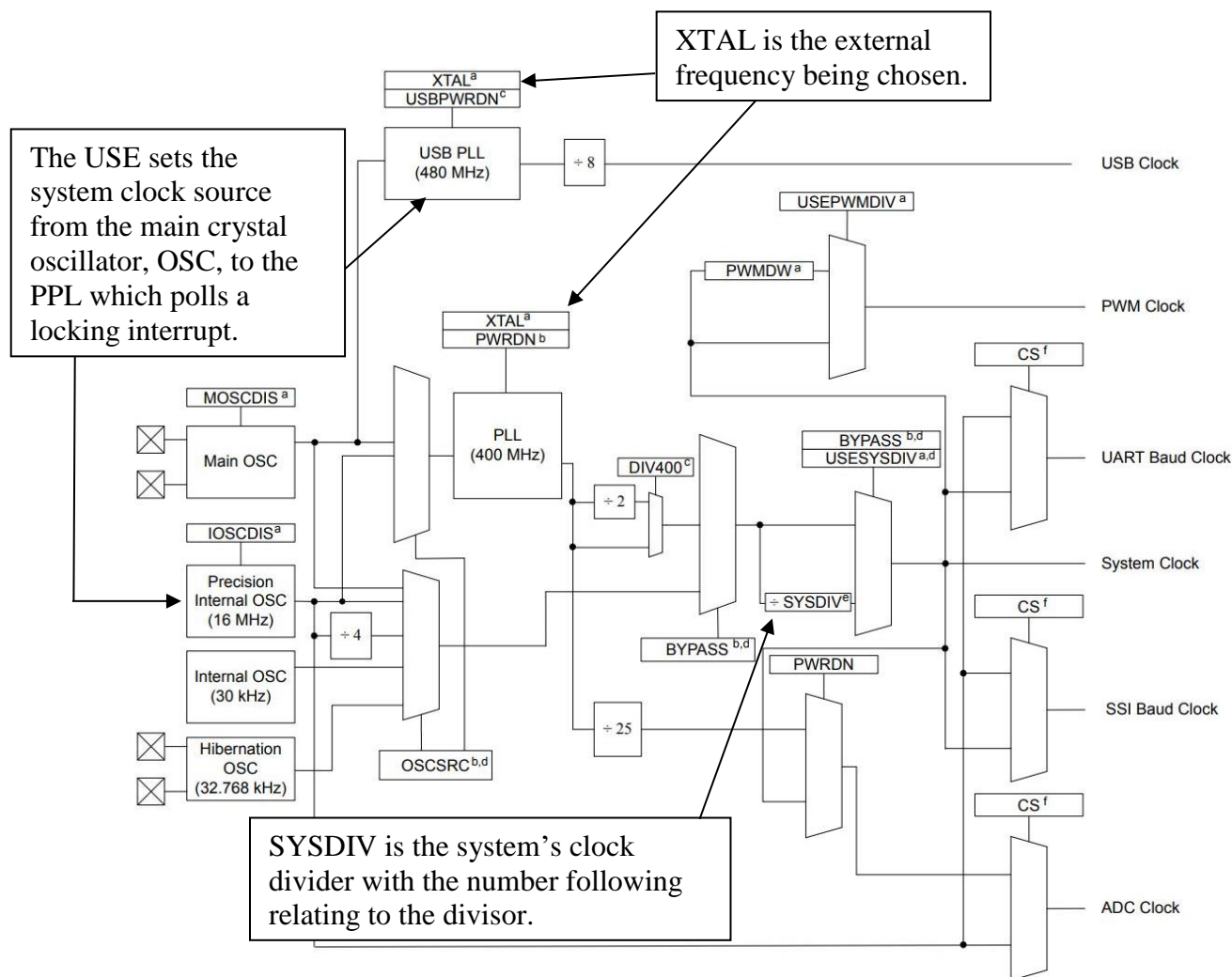
1. Functions and/or # defines used for the first time in this software project:
 - a. #include "inc/hw_ints.h"
 - b. #include "inc/hw_types.h"
 - c. #include "driverlib/debug.h"
 - d. #include "driverlib/interrupt.h"
 - e. #include "driverlib/fpu.h"
 - f. #include "driverlib/timer.h"
 - g. #define floodPeriod
 - h. #define parPeriod
 - i. void initializations(void);
 - j. void displayOLED(uint32_t a[3]);
 - k. void menuSwitch(void);
2. Definitions:
 - a. **Polled Input/Output:** A polled I/O is a loop iteration based I/O. While this method is safer it is costly in timing. Polling consistently checks the state of a device to see if it is ready to be utilized as seen in the code for blinky.
 - b. **Interrupt Input/Output:** For an interrupt driven I/O the interrupt is only called when needed, saving time and memory. For interrupts the computer can spend time doing allocated tasks and only calling a specific procedure when necessary.
 - c. **Vector Table:** A vector table regarding this laboratory exercise is the table of interrupts given in startup_ewarm.c. This table holds both the interrupt handlers and requests with an individual vector being the address of an interrupt handler for efficient calling and readability.
3. The Timer0 and Timer1 peripherals in this project use polled input/output and the UART, GPIO, and ADC peripherals use interrupt driven input/output.
4. The processor knows which function to execute as a service routine when an interrupt is pending because the function names are in the interrupt vector table where the processor goes to when an interrupt is pending. The only place, other than the function definition that the name of the function used as the service routines given in the source code of this software project is in startup_ewarm.c.
5. When the high-frequency timer (Timer 1) is running fast enough to starve the foreground process (in this case blinky) the 1 Hz timer (Timer 0) still executes because the ISR affects foreground tasks not the background timer interrupt tasks. Also, in this procedure Timer0 has a higher priority than Timer1 so Timer0 is executed before it is starved. If the timer roles were swapped, then the starvation would occur before the 1Hz timer and performance would be drastically affected. This is feasible under the conditions that ---.
6. Considering the frequencies 66MHz to 16MHz, the correlation between the recorded results and the operational frequency is that when the operation was CPU-bound the clock speed ran at a rate almost four times greater than the speed when the operation was IO-bound for both frequencies. This is because of the fact that the CPU-bound operation allows for the processing speed to directly increase with the sliding wiper of the potentiometer where as in an IO-bound operation the program must wait for the constant processing speed of, in this case, the OLED which is significantly lower.
7. In Figure 1.0 below, the function of each of the flag fields used as arguments to the ui32Config parameter of the SysCtlClockSet() is described.

The parameters for the function using the frequency 16MHz (Line 106 in Appendix A):

```
SysCtlClockSet(SYSCTL_SYSDIV_3 | SYSCTL_USE_PLL | SYSCTL_XTAL_16MHZ | SYSCTL_OSC_MAIN);
```

The parameters for the function using the frequency 66MHz (Line 116 in Appendix A):

```
SysCtlClockSet(SYSCTL_SYSDIV_1 | SYSCTL_USE_OSC | SYSCTL_OSC_MAIN | SYSCTL_XTAL_16MHZ);
```



- Note:**
- a. Control provided by **RCC** register bit/field.
 - b. Control provided by **RCC** register bit/field or **RCC2** register bit/field, if overridden with **RCC2** register bit **USERCC2**.
 - c. Control provided by **RCC2** register bit/field.
 - d. Also may be controlled by **DSLCLKCFG** when in deep sleep mode.
 - e. Control provided by **RCC** register **SYSDIV** field, **RCC2** register **SYSDIV2** field if overridden with **USERCC2** bit, or **[SYSDIV2,SYSDIV2LSB]** if both **USERCC2** and **DIV400** bits are set.
 - f. Control provided by **UARTCC**, **SSICC**, and **ADCCC** register field.

Figure 1.0 Main Clock Tree

8. In this laboratory exercise I was surprised by the useful functionality of interrupts. I am used to using polled driven code and understanding the efficiency of interrupts is very insightful. One of the main setbacks was figuring out how to exactly integrate the timers into the code. The instructions for this laboratory seemed much lighter and more abstract than usual. This led to a huge delay in my program's completion. In the future I would most likely try to complete the program starting from the timers.c file instead of my previous laboratory C file to see if it would make the integration easier.

CONCLUSION

While this lab was significantly one of the toughest software programs in this course to integrate and run, the amount that was gained by the process was worth the effort. In the previous labs the takeaway was a skill that may not be used in everyday programming, ADC and UART, but for this laboratory exercise the knowledge and use of interrupts is a valuable skill to know for the future. Also, the first hand understanding of how simple changes to code can improve or degrade efficiency so drastically is a unique insight that is not able to be seen so materialistically in prior courses. In this lab I also drastically organized my code which made the procedure itself seem less confusing and the code itself more readable, which may sound obvious, but I was shocked at how putting some of the code into separate functions really changed the view of my software project. In conclusion, whether it was something miniscule or important, this exercise proved to be a very insightful project.

APPENDICES:

APPENDIX A: Lab Code

The code for this exercise is shown starting on the next page.

```

1  //*****
2  //
3  // CS322.50 Laboratory 4 Software File
4  // Developed by: Andrea Gray (c)
5  // Version: 1.5 19-FEB-2019
6  //
7  //*****
8
9  //*****
10 //
11 // Copyright (c) 2011-2017 Texas Instruments Incorporated.
12 //
13 // This is part of revision 2.1.4.178 of the DK-TM4C123G Firmware Package.
14 //
15 //*****
16 #include <stdio.h>
17 #include <string.h>
18 #include <stdint.h>
19 #include <stdbool.h>
20 #include "inc/hw_ints.h"
21 #include "inc/hw_memmap.h"
22 #include "inc/hw_types.h"
23 #include "driverlib/debug.h"
24 #include "driverlib/fpu.h"
25 #include "driverlib/gpio.h"
26 #include "driverlib/interrupt.h"
27 #include "driverlib/sysctl.h"
28 #include "driverlib/timer.h"
29 #include "glib/glib.h"
30 #include "drivers/cfal96x64x16.h"
31 #include "driverlib/uart.h"
32 #include "driverlib/adc.h"
33 #define blinkyOnPeriod 100000
34 #define blinkyOffPeriod 100000
35 #define scale 4095
36
37 //*****
38 //
39 // Globals
40 //
41 //*****
42 int jumpTick;
43 int color;
44 int whileLoop;
45 int clockTick;
46 int actualVal;
47 int RQTimerLoad;
48 int8_t shouldDisplayCounter;
49 int32_t blinkyHandler;
50 int32_t local_char;
51 uint32_t requestedVal[3];
52 char AV[50];
53 char CT[50];
54 tContext g_sContext;
55 tRectangle sRect;
56
57 //*****
58 //
59 // Function Declarations
60 //
61 //*****
62 void splash(void);
63 void blinky(void);
64 void clear(void);
65 void initializations(void);
66 void printMenu(void);
67 void putString(char *str);
68 void getADC(void);
69 void displayOLED(uint32_t a[3]);
70 void menuSwitch(void);
71
72 //*****
73 //
74 //! This example application demonstrates the use of the timers to generate
75 //! periodic interrupts. Each interrupt handler will toggle its own indicator
76 //! on the display.
77 //
78 //*****
79
80 int main(void) {
81
82     //
83     // Global Variable Intializations
84     //
85     shouldDisplayCounter = 0; // Maintains Timer1 OLED unless specified otherwise
86     blinkyHandler = 1; // Maintains LED 'heartbeat' unless specified otherwise
87     clockTick = 0; // Counts the number of cycles as a time keeper
88     whileLoop = 1; // Maintains indefinite while loop unless program exits
89
90     //
91     // Enable lazy stacking for interrupt handlers. This allows floating-point
92     // instructions to be used within interrupt handlers, but at the expense of
93     // extra stack usage.
94     //
95     FPU_LazyStackingEnable();

```

```

192 GrContextBackgroundSet(&g_sContext, ClrBlack);
193 GrStringDrawCentered(&g_sContext, AV, -1, 68, 38, 1);
194 clockTick = 0;
195 }
196
197 //*****
198 //
199 // The interrupt handler for the second timer interrupt.
200 //
201 //*****
202 void Timer1IntHandler(void) {
203     TimerIntClear(TIMER1_BASE, TIMER_TIMA_TIMEOUT); // Clear the timer interrupt.
204     clockTick++; // Increase clock count each second with the interrupt call.
205
206     // Printing out the clock counter value if called for.
207     if (shouldDisplayCounter == 1) {
208         GrContextBackgroundSet(&g_sContext, ClrBlack);
209         sprintf(CT, " %d ", clockTick);
210         GrStringDrawCentered(&g_sContext, CT, -1, 68, 50, 1);
211     }
212 }
213
214 //*****
215 //
216 // The UART interrupt handler.
217 //
218 //*****
219 void UARTIntHandler(void){
220     uint32_t ui32Status;
221     ui32Status = UARTIntStatus(UART0_BASE, true); // Get the interrupt status.
222     UARTIntClear(UART0_BASE, ui32Status); // Clear the interrupt for UART
223
224     //
225     // Loop while there are characters in the receive FIFO.
226     //
227     while(UARTCharsAvail(UART0_BASE))
228     {
229         // Read the next character from the UART and write it back to the UART.
230         UARTCharPut(UART0_BASE, UARTCharGet(UART0_BASE));
231     }
232 }
233
234 //*****
235 //
236 // The function that is called when the program is first executed to set up
237 // the TM4C123G board, the peripherals, the timers, and the interrupts.
238 //
239 //*****
240 void initializations() {
241     IntMasterDisable(); // Disable processor interrupts for configurations.
242     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOG); // Enable GPIO G usage.
243
244     //
245     // Check if the LED peripheral access is enabled and wait if not.
246     //
247     while(!SysCtlPeripheralReady(SYSCTL_PERIPH_GPIOG)) {}
248     GPIOPinTypeGPIOOutput(GPIO_PORTG_BASE, GPIO_PIN_2); // GPIO output is pin 2.
249     CFAL96x64x16Init(); // Initialize the OLED display driver.
250
251     // Initialize the OLED graphics context.
252     GrContextInit(&g_sContext, &g_sCFAL96x64x16);
253     GrContextFontSet(&g_sContext, g_psFontFixed6x8);
254     SysCtlPeripheralEnable(SYSCTL_PERIPH_UART0); // Enable UART 0 usage.
255     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA); // Enable GPIO A usage.
256
257     // Set GPIO A0 and A1 as UART Pins.
258     GPIOPinTypeUART(GPIO_PORTA_BASE, GPIO_PIN_0 | GPIO_PIN_1);
259
260     // configure uart for 115200 baud rate
261     UARTConfigSetExpClk(UART0_BASE, SysCtlClockGet(), 115200, (UART_CONFIG_WLEN_8
262     | UART_CONFIG_STOP_ONE | UART_CONFIG_PAR_NONE));
263     clear(); // Clear any outputs or inputs from the PUTTY window.
264
265     //*****
266     //
267     // Configure ADC0 for a single-ended input and a single sample. Once the
268     // sample is ready, an interrupt flag will be set. Using a polling method,
269     // the data will be read then displayed on the console via UART0.
270     //
271     //*****
272
273     // Enable the peripherals for GPIOD and ADC.
274     SysCtlPeripheralEnable(SYSCTL_PERIPH_ADC0);
275     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
276     GPIOPinTypeADC(GPIO_PORTD_BASE, GPIO_PIN_7); // Set GPIO D4 as an ADC pin.
277     ADCSequenceDisable(ADC0_BASE, 3); // Disable sample sequence 3.
278
279     // Configure sample sequence 3: processor trigger, priority = 0.
280     ADCSequenceConfigure(ADC0_BASE, 3, ADC_TRIGGER_PROCESSOR, 0);
281
282     //
283     // Configure step 0 on sequence 3: channel 4. Configure the interrupt
284     // flag to be set when the sample is done (ADC CTL IE). Signal last

```



```

285 // conversion on sequence 3 (ADC_CTL_END).
286 //
287 ADCSequenceStepConfigure(ADC0_BASE, 3, 0, ADC_CTL_CH4 | ADC_CTL_IE |
288                          ADC_CTL_END);
289 ADCSequenceEnable(ADC0_BASE, 3); // Enable sequence 3.
290 SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER0); // Enable usage of Timer 0.
291 SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER1); // Enable usage of Timer 1.
292
293 // Configure the two 32-bit periodic timers.
294 TimerConfigure(TIMER0_BASE, TIMER_CFG_PERIODIC);
295 TimerConfigure(TIMER1_BASE, TIMER_CFG_PERIODIC);
296 TimerLoadSet(TIMER0_BASE, TIMER_A, SysCtlClockGet());
297 TimerLoadSet(TIMER1_BASE, TIMER_A, SysCtlClockGet() / 10000);
298
299 // Setup the interrupts for the timer timeouts.
300 IntEnable(INT_TIMER0A);
301 IntEnable(INT_TIMER1A);
302 TimerIntEnable(TIMER0_BASE, TIMER_TIMA_TIMEOUT);
303 TimerIntEnable(TIMER1_BASE, TIMER_TIMA_TIMEOUT);
304
305 // Enable the timers.
306 TimerEnable(TIMER0_BASE, TIMER_A);
307 TimerEnable(TIMER1_BASE, TIMER_A);
308 }
309
310 //*****
311 //
312 // Gets input from ADC.
313 //
314 //*****
315 void getADC() {
316     ADCProcessorTrigger(ADC0_BASE, 3); // Trigger the ADC conversion.
317     while(! (ADCIntStatus(ADC0_BASE, 3, false))); //Wait for an ADC reading.
318     ADCSequenceDataGet(ADC0_BASE, 3, requestedVal); // Put the reading into a var.
319
320     // This adjusts the variable used for timer loading depending on if the
321     // clock counter is enabled or not.
322     if (shouldDisplayCounter == 1)
323         RQTimerLoad = (1000*requestedVal[0])/4095;
324     else
325         RQTimerLoad = (2000000*requestedVal[0])/4095;
326
327     // Printing the value being requested by the ADC peripheral.
328     char RV[50];
329     sprintf(RV, "%d", RQTimerLoad);
330     GrContextBackgroundSet(&g_sContext, CLrBlack);
331     GrStringDrawCentered(&g_sContext, RV, -1, 68, 26, 1);
332 }
333
334 //*****
335 //
336 // PuTTY window clearing function.
337 //
338 //*****
339 void clear() { UARTCharPut(UART0_BASE, 12); }
340
341 //*****
342 //
343 // Using the character output function as a base for a parent function
344 // used to output an entire string to the OLED one character at a time.
345 //
346 //*****
347 void putString(char *str) {
348     for(int i = 0; i < strlen(str); i++)
349         UARTCharPut(UART0_BASE, str[i]);
350 }
351
352 //*****
353 //
354 // Print menu function that takes the complete menu as a string and
355 // utilizes the print string function created above to output the entire menu
356 // in one transmission block to the PuTTY window.
357 //
358 //*****
359 void printMenu() {
360     // String below is separated only for report use -- multi-line strings are
361     // not supported by C PL.
362     char*menu = "\rMenu Selection: \n\rE - Erase Terminal Window\n\rL - Flash LED
363     \n\rM - Print the Menu\n\rQ - Quit this program\n\rT - Enable Count\n\r";
364     putString(menu);
365 }
366
367 //*****
368 //
369 // If the input character is not invalid, begin the character matching
370 // statement below through the switch statement and act accordingly to
371 // the user input.
372 //
373 //*****
374 void menuSwitch() {
375     if (local_char != -1) {
376         UARTCharPut(UART0_BASE, local_char); // Send a character to UART.
377     }

```



```

378 // Begin character input matching to menu option.
379 switch(local_char) {
380 case 'E': // Clear PuTTY window
381     clear();
382     break;
383
384 case 'L': //LED toggle
385     if(blinkyHandler == 0)
386         blinkyHandler = 1;
387     else
388         blinkyHandler = 0;
389     GPIOWrite(GPIO_PORTG_BASE, GPIO_PIN_2, 0);
390     break;
391
392 case 77: // Re-print menu
393     printMenu();
394     break;
395
396 case 81: // Quit program
397     IntMasterDisable();
398     putString("\n\rBYE!"); // Goodbye message to PuTTY
399     // Re-draw OLED with goodbye statement in red font.
400     sRect.i16XMin = 0;
401     sRect.i16YMin = 0;
402     sRect.i16XMax = GrContextDpyWidthGet(&g_sContext) - 1;
403     sRect.i16YMax = GrContextDpyHeightGet(&g_sContext) - 1;
404     GrContextForegroundSet(&g_sContext, ClrBlack);
405     GrContextBackgroundSet(&g_sContext, ClrBlack);
406     GrRectFill(&g_sContext, &sRect);
407     GrContextForegroundSet(&g_sContext, ClrRed);
408     GrContextFontSet(&g_sContext, g_psFontFixed6x8);
409     GrStringDrawCentered(&g_sContext, "Goodbye", -1,
410                          GrContextDpyWidthGet(&g_sContext) / 2, 30, false);
411     whileLoop = 0;
412     break;
413
414 case 'T': // Display Counter
415     if (shouldDisplayCounter == 0) {
416         shouldDisplayCounter = 1;
417     }
418     else {
419         sRect.i16XMin = 50;
420         sRect.i16YMin = 45;
421         sRect.i16XMax = 96;
422         sRect.i16YMax = 64;
423         GrContextForegroundSet(&g_sContext, ClrBlack);
424         GrRectFill(&g_sContext, &sRect);
425         shouldDisplayCounter = 0;
426         GrContextForegroundSet(&g_sContext, ClrWhite);
427     }
428     break;
429 }
430 }
431 }
432 }
433
434 //*****
435 //
436 // Blinky LED "heartbeat" function.
437 //
438 //*****
439 void blinky() {
440     if(blinkyHandler == blinkyOnPeriod) {
441         GPIOWrite(GPIO_PORTG_BASE, GPIO_PIN_2, GPIO_PIN_2);
442         blinkyHandler = -blinkyOffPeriod;
443     }
444
445     if(blinkyHandler == -1) {
446         GPIOWrite(GPIO_PORTG_BASE, GPIO_PIN_2, 0);
447         blinkyHandler = 1;
448     }
449 }
450
451 //*****
452 //
453 // Splash Screen
454 //
455 //*****
456 void splash() {
457     tRectangle Rect;
458     int16_t xValLast = 0;
459     int16_t yValLast = 38;
460     int16_t xVal = 0;
461     int32_t yVal = 38;
462     while(1) {
463         xVal+=2;
464         if(jumpTick == -1) {
465             jumpTick = 0;
466         }
467
468         if(jumpTick != -1) {
469             yVal = 38 - (10*jumpTick-jumpTick*jumpTick);
470             jumpTick++;

```

```

471     if(jumpTick > 10) {
472         jumpTick = -1;
473     }
474 }
475
476 GrContextInit(&g_sContext, &g_sCFAL96x64x16); // Sets OLED context.
477 switch(color) {
478     case 0:
479         GrContextForegroundSet(&g_sContext, ClrBlue);
480         break;
481     case 1:
482         GrContextForegroundSet(&g_sContext, ClrRed);
483         break;
484     case 2:
485         GrContextForegroundSet(&g_sContext, ClrGreen);
486         break;
487     case 3:
488         GrContextForegroundSet(&g_sContext, ClrBlack);
489         sRect.il6XMin = 0;
490         sRect.il6YMin = 0;
491         sRect.il6XMax = xVal;
492         sRect.il6YMax = GrContextDpyHeightGet(&g_sContext);
493         GrRectFill(&g_sContext, &sRect);
494         break;
495 }
496
497 GrCircleFill(&g_sContext, xValLast, yValLast, 5);
498 GrContextForegroundSet(&g_sContext, ClrWhite);
499 GrCircleFill(&g_sContext, xVal, yVal, 5);
500 GrContextFontSet(&g_sContext, g_psFontCml2/*g_psFontFixed6x8*/);
501 GrFlush(&g_sContext);
502 xValLast = xVal;
503 yValLast = yVal;
504
505 if(xVal >= 106) {
506     xVal = -13;
507     color++;
508 }
509
510 if(color == 4) {
511     break;
512 }
513
514 SysCtlDelay(refreshRate);
515 }
516
517 GrContextForegroundSet(&g_sContext, ClrBlack);
518 Rect.il6XMin = 0;
519 Rect.il6YMin = 0;
520 Rect.il6XMax = 96;
521 Rect.il6YMax = 64;
522 GrContextForegroundSet(&g_sContext, ClrBlack);
523 GrRectFill(&g_sContext, &Rect);
524 GrContextForegroundSet(&g_sContext, ClrWhite);
525 GrContextFontSet(&g_sContext, g_psFontFixed6x8);
526 char clockRate[8];
527 sprintf(clockRate, "%d Hz", SysCtlClockGet());
528 GrStringDrawCentered(&g_sContext, clockRate, -1, 96 / 2, 40, 0);
529 SysCtlDelay(SysCtlClockGet()/3);
530 Rect.il6XMin = 0;
531 Rect.il6YMin = 0;
532 Rect.il6XMax = 96;
533 Rect.il6YMax = 64;
534 GrContextForegroundSet(&g_sContext, ClrBlack);
535 GrRectFill(&g_sContext, &Rect);
536 sRect.il6XMin = 0;
537 sRect.il6YMin = 0;
538 sRect.il6XMax = 96;
539 sRect.il6YMax = 64;
540 GrContextBackgroundSet(&g_sContext, ClrBlack);
541 GrRectFill(&g_sContext, &sRect);
542 GrContextForegroundSet(&g_sContext, ClrWhite);
543
544 // Initialize timer status display.
545 GrContextFontSet(&g_sContext, g_psFontFixed6x8);
546 GrStringDraw(&g_sContext, "Rq:", -1, 16, 26, 0);
547 GrStringDraw(&g_sContext, "Ac:", -1, 16, 36, 0);
548 GrStringDraw(&g_sContext, "Ct:", -1, 16, 50, 0);
549
550 }

```

Gray



APPENDIX C: Instructor Sign Off

Instructor Sign-off

Each laboratory group must complete a copy of this sheet.

Student A Name Andrea Gray

Student B Name _____

DK-TM4C123G Board Used 20

Lab Station Used 04

1. Demonstrate operation of your a project on the DK-TM4C123G board which is performing a variable number of interrupt service routine executions per second.

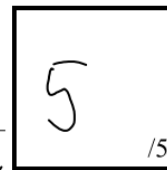
Instructors's Initials DF Date/Time 2/27/19 11:20 AM

2. Demonstrate a program which can display the number of times each second that the variable rate service routine is able to be performed together with switching between each of the following permutations.

Operating Frequency	Number of ISR executions per second	
	With an OLED display each variable frequency service call	Without an OLED display in variable frequency service call
16 MHz	210	415310
66 MHz	365	1473393

Answer any questions the lab faculty may have, and demonstrate to the lab faculty that you have met the requirements of this laboratory exercise.

Instructors's Initials DF Date/Time 2/27/19 11:22 AM



APPENDIX D: Definitions

- **ADC**—Analog to Digital Converter
- **OLED**—Organic Light Emitting Diode on the TivaWare® TM4C123G Board
- **PDL**—Peripheral Driver Library
- **ISR**—Interrupt Service Routine

APPENDIX E: Citation

- ¹ : *Tiva™ TM4C123GH6PM Microcontroller: Data Sheet*. Rev. E. [eBook]. Austin: Texas Instruments, 2014, p.898. Available at: <http://www.ti.com/lit/ds/symlink/tm4c123gh6pm.pdf>. [Accessed: Feb 10, 2019].