

	EMBRY-RIDDLE AERONAUTICAL UNIVERSITY Department of Electrical, Computer, and Software Engineering CEC322: Microprocessor Laboratory (Spring 2019), SECTION PC51 Due Date: 16-APR-2019 Turned in: 16-APR-2019	
	PREPARED BY:	EQUIPMENT S/N:
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		PERFORMED: 09-APR-2019

LABORATORY #9

Using Assembly Language to Accelerate the Operation of an ISR

OBJECTIVES

- Increase knowledge and experience with the DK-TM4C123G Development Kit
- Continued use of the 'C' programming language
- Increases understanding of the engineering design process
- Write ARM assembly language routines
- Use assembly language to accelerate a critical portion of the software project
- Utilize interrupts to generate high frequency operation

PURPOSE

The purpose of this laboratory exercise is to combine the structure and usage of both the Assembly and C languages in one program to accelerate the portion of a program with the most expensive usage of CPU cycles in terms of fractional consumption. It is also a goal of this procedure to correctly and accurately write, develop, and assembly software using UAL.

SPECIFICATIONS

For this laboratory exercise, it is required to demonstrate to the appropriate faculty that the 'C' software project created in conjunction with the development board which is able to run under debug mode and completes the assignment requirements dictated below.

This program must show the accurate implementation assembly language code written to replace the variable frequency interrupt service routine (ISR) previously developed in 'C' in the fourth laboratory exercise. The sped-up operation must be displayed to validate operation of the program.

PROCEDURE

The code is completed in the 'C' programming language with all sources listed in the process as they are used. The TivaWare® TM4C123G Development Kit and a potentiometer are the only hardware devices utilized for this procedure. All new technical terms and acronyms are defined in Appendix D at the end of this report.

Process:

1. Code Retrieval
 - 1.1. This program was built from the Lab4.c code © Andrea Gray 2019 for reliability and compiling reassurance purposes.
2. Overview
 - 2.1. The objective of this software is to perform the following tasks successfully in 'C'
 - 2.1.1. Display ADC data
 - 2.1.2. Switch between 16 and 80 MHz
 - 2.1.3. Splash screen as done in previous exercises
 - 2.1.4. I/O through UART to establish communication with the user and the program

- 2.1.4.1. Quit option
- 2.1.5. Blinky “heartbeat”
- 2.2. The following task must be performed and integrated successfully in Assembly Language Code
 - 2.2.1. Incrementation of the serviced counter
- 3. Translation
 - 3.1. Since the only part of the code that needed to be changed was the clock frequency and the ISR for Timer 1, the procedure for this exercise is concise
 - 3.2. Clock frequency options were changed from 16 and 60 MHz to 16 and 80 MHz
 - 3.3. Timer1IntHandler() was reduced in lines to allow assembly translation to be easier
 - 3.3.1. Printing of data was allocated to Timer0IntHandler()
 - 3.3.2. The final Timer1IntHandler() is shown below in Figure 3.3.2.1

```
void Timer1IntHandler(void) {
    TimerIntClear(TIMER1_BASE, TIMER_TIMA_TIMEOUT); // Clear the timer interrupt.
    ServicedCount++; // Increase serviced count each second with the interrupt call.
}
```

Figure 3.3.2.1: Timer 1 Interrupt Handling Function – ‘C’

- 3.4. A breakpoint was inserted at the function above to use the disassembly feature of IAR to gather the assembly code of the ISR.
- 4. Vector Table Changes
 - 4.1. The new interrupt service routine had to be implemented into the startup-ewarm ISR vector table to allow the function to be properly called
 - 4.2. Declared both Timer1IntHandler() and isr_asm_start.s at the beginning of the startup-ewarm
 - 4.2.1. Manually changed the value in the vector table to switch between the assembly and C version of the interrupt service routine for timer 1

REPORT DISCUSSION

1. The fastest frequency of operation observed was 3,029,675 Hz. The clock period of this frequency was 25 CPU cycles, which was calculated by $CPU\ Cycles = \frac{SysCtlClockGet()}{ADC\ Value}$ and was represented as the period value in the code and on the sign-off in the appendices.
2. There are seven instructions, from start to finish, in the post-assembled version of the assembly language routine created for this project.
3. Using the frequency, period, and instruction count from the questions above, the number of cycles per instruction for the variable frequency ISR, when operating at maximum frequency, is calculated below.

$$\begin{aligned}
 \frac{Time}{Program} &= \frac{Instructions}{Program} * \frac{Cycles}{Instruction} * \frac{Time}{Cycle} \\
 \frac{1}{3029675} &= \frac{12}{Cycles} * \frac{1}{80000000} * \frac{1}{1} \\
 \frac{Cycles}{Instruction} &= \frac{80000000}{3029675} * \frac{1}{12} \\
 \frac{Cycles}{Instruction} &= 2.20
 \end{aligned}$$

4. The Assembly ISR laboratory exercise, this exercise, is 1,154,086 executions per seconds faster than the Timer Lab results. This means that the implementation of assemble language into the same general program speeds up execution by 161.53 %.

5. The machine language equivalent of `pop {pc}` in assembly language, usually the last instruction in the language, is `0x21bc : 0xbd00 POP {pc}`.
6. In this exercise, in addition to the use of assembly language, the techniques used to accelerate the incrementation in this project are listed below
 - ⇒ Optimization of Assembly Code
 - ⇒ Optimization of C code
 - ⇒ Wrote the `TimerIntClear()` function in Assembly instead of branching to it
7. A surprise in this exercise was that the period changed between 22 and 24 between operational frequency changes for the assembly language implementation. Dr. Davis was initially surprised to see this outcome as well and suggested that our optimizations may not be complete due to this reaction. It was challenging in this exercise to implement the assembly. Although not completely new to either of the authors, the correct implementation and translation of the language proved to be still quite a new topic for both Drew and Andrea. The bugs and errors that were encountered and overcome in the laboratory four exercise, which this procedure was based from, came back into light this time around and since there has been some time between exercises four and nine, they proved to be just as confusing now as they were then. In the future it would be suggested that the students look over the laboratory exercise four again to become reacquainted with its functions and procedures; just to make the implementation of the assembly code a little bit easier.

CONCLUSION

In conclusion, this laboratory exercise, while still not mastered by either author, was quite easy in regards to their history with assembly language. The code took the `Timer1IntHandler()` from laboratory four exercise and rewrote it in assembly language, then implemented it back into the exercise. The function `isr_asm_start()` was used to replace the `Timer1IntHandler()` ISR call. For both cases of 16 MHz and 80 MHz the program proved to be greatly accelerated by the instantiation of the assembly language code.

APPENDICES:

APPENDIX A: 'C' Lab Code

```

1 //*****
2 // Authors: Andrea Gray and Drew Grobmeier
3 //
4 // CEC322 Lab #9: sdf
5 // Development reference: Lab4.c
6 //
7 //*****
8 #include <stdio.h>
9 #include <stdint.h>
10 #include <stdbool.h>
11 #include <string.h>
12
13 #include "inc/hw_ints.h"
14 #include "inc/hw_memmap.h"
15 #include "inc/hw_types.h"
16
17 #include "grlib/grlib.h"
18
19 #include "drivers/cfal96x64x16.h"
20
21 #include "driverlib/uart.h"
22 #include "driverlib/adc.h"

```

```

23 #include "driverlib/gpio.h"
24 #include "driverlib/fpu.h"
25 #include "driverlib/interrupt.h"
26 #include "driverlib/sysctl.h"
27 #include "driverlib/timer.h"
28 #include "driverlib/debug.h"
29
30 #define LEDOn 100000 // defines how long the LED will stay lit
31 #define LEDOff 100000 // defines how long the LED will remain off
32
33 //*****
34 //
35 // Globals
36 //
37 //*****
38 int whileLoop = 1; // Maintains indefinite while loop unless program exits
39 int actualVal;
40 int ADCLoadValue;
41 int ServicedCount = 0;
42
43 int32_t BlinkyToggle = 1; // Maintains LED 'heartbeat' unless specified otherwise
44 int32_t CharacterInput; // Keeps the character input into PuTTY
45
46 uint32_t ADCValue[3];
47
48 char ServicedValue[50];
49 char PeriodValue[50];
50
51 tContext Context; // OLED drawing contextual structuring
52 tRectangle sRect; // Rectangle parameters for banner structuring
53
54 //*****
55 //
56 // Function Declarations
57 //
58 //*****
59 void splash(void); // splash screen display function
60 void blinky(void); // "Heartbeat" function
61 void clear(void); // clear the PuTTY window
62 void initializations(void); // Sets-up the software and hardware for usage
63 void printMenu(void); // re-prints the menu options to PuTTY
64 void putString(char *str); // prints a string to the OLED
65 void menuSwitch(void); // Switches between menu options depending on the input
66 void getADC(void); // Reading the value from the ADC
67
68 //*****
69 //
70 // The error routine that is called if the driver library encounters an error.
71 //
72 //*****
73 #ifdef DEBUG
74 void __error__(char *pcFilename, uint32_t ui32Line) {}
75 #endif
76
77 //*****
78 //
79 // The interrupt handler for the first timer interrupt.
80 //
81 //*****
82 void Timer0IntHandler(void) {
83     TimerIntClear(TIMER0_BASE, TIMER_TIMA_TIMEOUT); // Clear the timer interrupt.
84     getADC(); // Gather values obtained by the ADC
85
86     // setting the load value
87     TimerLoadSet(TIMER1_BASE, TIMER_A, (SysCtlClockGet()/ ADCLoadValue));
88
89     // Printing out the values that have been serviced.
90     sprintf(ServicedValue, "Srv: %d", ServicedCount);
91     GrContextBackgroundSet(&Context, ClrBlack);
92     GrStringDraw(&Context, ServicedValue, 20, 5, 38, 1);
93
94     // Printing out the ADC requested value if called for.
95     GrContextBackgroundSet(&Context, ClrBlack);
96     sprintf(PeriodValue, "Per: %d", SysCtlClockGet()/ADCLoadValue);
97     GrStringDraw(&Context, PeriodValue, 20, 5, 50, 1);
98
99     ServicedCount = 0;
100 }
101
102 //*****
103 //
104 // The interrupt handler for the second timer interrupt.
105 //
106 //*****
107 void Timer1IntHandler(void) {
108     TimerIntClear(TIMER1_BASE, TIMER_TIMA_TIMEOUT); // Clear the timer interrupt.
109     ServicedCount++; // Increase serviced count each second with the interrupt call.
110 }
111
112 //*****
113 //
114 // ADC Input Gathering
115 //
116 //*****
117 void getADC() {
118     ADCProcessorTrigger(ADC0_BASE, 3); // Trigger the ADC conversion.
119     while(! (ADCIntStatus(ADC0_BASE, 3, false))); //Wait for an ADC reading.
120     ADCSequenceDataGet(ADC0_BASE, 3, ADCValue); // Put the reading into a var.
121     ADCLoadValue = (ADCValue[0]* (SysCtlClockGet() / 80000) +1);
122
123     // Printing the value being requested by the ADC peripheral.
124     char RequestedString[50];
125     sprintf(RequestedString, "Req: %d", ADCLoadValue);
126     GrContextBackgroundSet(&Context, ClrBlack);
127     GrStringDraw(&Context, RequestedString, 20, 5, 26, 1);
128 }
129
130

```

```

131
132 //*****
133 //
134 // The UART interrupt handler.
135 //
136 //*****
137 void UARTIntHandler(void) {
138     uint32_t ui32Status; // Holds the interrupt status
139     ui32Status = UARTIntStatus(UART0_BASE, true); // Get the interrupt status.
140     UARTIntClear(UART0_BASE, ui32Status); // Clear the interrupt for UART
141     while(UARTCharsAvail(UART0_BASE)) { // Loop while there are characters in the receive FIFO.
142         CharacterInput = UARTCharGetNonBlocking(UART0_BASE); // Read the next char from UART and write it back
143         menuSwitch(); // Act accordingly via user request
144     }
145 }
146
147 //*****
148 //
149 // The function that is called when the program is first executed to set up
150 // the TM4C123G board, the peripherals, the timers, and the interrupts.
151 //
152 //*****
153 void initializations(void) {
154
155     //*****
156     // LED
157     //*****
158     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOG); // Enable GPIO G usage.
159     while(!SysCtlPeripheralReady(SYSCTL_PERIPH_GPIOG)); // Wait until LED is ready
160     GPIOPinTypeGPIOOutput(GPIO_PORTG_BASE, GPIO_PIN_2); // GPIO output is pin 2.
161
162     //*****
163     // OLED
164     //*****
165     CFAL96x64x16Init(); // Initialize the OLED display driver.
166     GrContextInit(&Context, &g_sCFAL96x64x16); // Initialize OLED graphics
167     GrContextFontSet(&Context, g_psFontFixed6x8); // Fix the font type
168
169     //*****
170     // UART
171     //*****
172     SysCtlPeripheralEnable(SYSCTL_PERIPH_UART0); // Enable UART 0 usage.
173     while(!SysCtlPeripheralReady(SYSCTL_PERIPH_UART0)); // wait until UART 0 is ready
174     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA); // Enable GPIO A usage.
175     while(!SysCtlPeripheralReady(SYSCTL_PERIPH_GPIOA)); // Wait until GPIO A is ready
176     GPIOPinTypeUART(GPIO_PORTA_BASE, GPIO_PIN_0 | GPIO_PIN_1); // Set Pins A0 and A1 for UART
177
178     // Configure UART for 115200 baud rate, 8 in 1 operation
179     UARTConfigSetExpClk(UART0_BASE, SysCtlClockGet(), 115200, (UART_CONFIG_WLEN_8 | UART_CONFIG_STOP_ONE | UART_CONFIG_PAR_NONE));
180
181     //*****
182     // ADC
183     //*****
184     // Configure ADC0 for a single-ended input and a single sample. Once the
185     // sample is ready, an interrupt flag will be set. Using a polling method,
186     // the data will be read then displayed on the console via UART0.
187     //*****
188     SysCtlPeripheralEnable(SYSCTL_PERIPH_ADC0); // Enable ADC0
189     SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD); // Enable GPIO D
190     GPIOPinTypeADC(GPIO_PORTD_BASE, GPIO_PIN_7); // Set GPIO D7 as an ADC pin.
191     ADCSequenceDisable(ADC0_BASE, 3); // Disable sample sequence 3.
192
193     // Configure sample sequence 3: processor trigger, priority = 0.
194     ADCSequenceConfigure(ADC0_BASE, 3, ADC_TRIGGER_PROCESSOR, 0);
195
196     // Configure step 0 on sequence 3: channel 4. Configure the interrupt
197     // flag to be set when the sample is done (ADC_CTL_IE). Signal last
198     // conversion on sequence 3 (ADC_CTL_END).
199     ADCSequenceStepConfigure(ADC0_BASE, 3, 0, ADC_CTL_CH4 | ADC_CTL_IE | ADC_CTL_END);
200     ADCSequenceEnable(ADC0_BASE, 3); // Enable sequence 3.
201
202     //*****
203     // TIMERS
204     //*****
205     SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER0); // Enable usage of Timer 0.
206     SysCtlPeripheralEnable(SYSCTL_PERIPH_TIMER1); // Enable usage of Timer 1.
207
208     // Configure the two 32-bit periodic timers.
209     TimerConfigure(TIMER0_BASE, TIMER_CFG_PERIODIC);
210     TimerConfigure(TIMER1_BASE, TIMER_CFG_PERIODIC);
211     TimerLoadSet(TIMER0_BASE, TIMER_A, SysCtlClockGet());
212     TimerLoadSet(TIMER1_BASE, TIMER_A, SysCtlClockGet() / 10000);
213
214     // Setup the interrupts for the timer timeouts.
215     IntEnable(INT_TIMER0A);
216     IntEnable(INT_TIMER1A);
217     TimerIntEnable(TIMER0_BASE, TIMER_TIMA_TIMEOUT);
218     TimerIntEnable(TIMER1_BASE, TIMER_TIMA_TIMEOUT);
219
220     // Enable the timers.
221     TimerEnable(TIMER0_BASE, TIMER_A);
222     TimerEnable(TIMER1_BASE, TIMER_A);
223
224     //*****
225     // MISCELLANEOUS
226     //*****
227     clear(); // Clear any outputs or inputs from the PuTTY window.
228     GrFlush(&Context); // Flush any cached operations
229 }
230
231 //*****
232 //
233 // PuTTY window clearing function.
234 //
235 //*****
236 void clear() { UARTCharPut(UART0_BASE, 12); }
237
238 //*****

```

```

239 //
240 // Using the character output function as a base for a parent function
241 // used to output an entire string to the OLED one character at a time.
242 //
243 //*****
244 void putString(char *str) {
245     for(int i = 0; i < strlen(str); i++) { // Loop through the string
246         UARTCharPut(UART0_BASE, str[i]); // print each individual character
247     }
248 }
249
250 //*****
251 //
252 // Print menu function that takes the complete menu as a string and
253 // utilizes the print string function created above to output the entire menu
254 // in one transmission block to the PuTTY window.
255 //
256 //*****
257 void
258 printMenu() {
259     char*menu = "\nMenu Selection: \n\rC - Erase Terminal Window\n\rL - Flash LED\n\rM - Print the Menu\n\rQ - Quit this program\n\r";
260     putString(menu);
261 }
262
263 //*****
264 //
265 // If the input character is not invalid, begin the character matching
266 // statement below through the switch statement and act accordingly to
267 // the user input.
268 //
269 //*****
270 void menuSwitch() {
271     if (CharacterInput != -1) { // Run only if the character in PuTTY is valid
272         UARTCharPut(UART0_BASE, CharacterInput); // Send a character to UART.
273     }
274     switch(CharacterInput) { // Begin character input matching to menu option.
275     case 'C': // Clear PuTTY window
276         clear();
277         break;
278     case 'L': //LED toggle
279         if(BlinkyToggle == 0)
280             BlinkyToggle = 1;
281         else
282             BlinkyToggle = 0;
283         GPIOPinWrite(GPIO_PORTG_BASE, GPIO_PIN_2, 0);
284         break;
285     case 'M': // Re-print menu
286         UARTCharPut(UART0_BASE, 5);
287         printMenu();
288         break;
289     case 'Q': // Quit program
290         IntMasterDisable();
291         putString("\n\rBYE!"); // Goodbye message to PuTTY
292         sRect.i16XMin = 0;
293         sRect.i16YMin = 0;
294         sRect.i16XMax = GrContextDpyWidthGet(&Context) - 1;
295         sRect.i16YMax = GrContextDpyHeightGet(&Context) - 1;
296         GrContextForegroundSet(&Context, ClrBlack);
297         GrContextBackgroundSet(&Context, ClrBlack);
298         GrRectFill(&Context, &sRect);
299         GrContextForegroundSet(&Context, ClrRed);
300         GrContextFontSet(&Context, g_psFontFixed6x8);
301         GrStringDrawCentered(&Context, "Goodbye", -1, GrContextDpyWidthGet(&Context) / 2, 30, false); // Goodbye message to OLED in red.
302         whileLoop = 0; // If the user said to quit the whileLoop will NO LONGER be able to be ran
303         break;
304     default:
305         char invalid[25] = "\n\rInvalid. Try Again: ";
306         char*ptr = invalid;
307         putString(ptr);
308         break;
309     }
310 }
311
312 //*****
313 //
314 // Blinky LED "heartbeat" function.
315 //
316 //*****
317 void blinky() {
318     if(BlinkyToggle == LEDOn) {
319         GPIOPinWrite(GPIO_PORTG_BASE, GPIO_PIN_2, GPIO_PIN_2);
320         BlinkyToggle = -LEDOff;
321     }
322     if(BlinkyToggle == -1) {
323         GPIOPinWrite(GPIO_PORTG_BASE, GPIO_PIN_2, 0);
324         BlinkyToggle = 1;
325     }
326 }
327
328 //*****
329 //
330 // Splash Screen
331 //
332 //*****
333 void splash() {
334     // splash screen display parameters
335     tRectangle screen;
336     screen.i16XMin = 0;
337     screen.i16XMax = 96; // Maximum width of the OLED
338     screen.i16YMin = 0;

```



```

347 screen.il6YMax = 64; // Maximum height of the OLED
348
349 // clear the screen from any reminaing displays
350 GrContextForegroundSet(&Context, ClrBlack);
351 GrRectFill(&Context, &screen);
352
353 // loading block parameters
354 for(int length = 10; length <= 86; length++) {
355     tRectangle loading;
356     loading.il6XMin = 9;
357     loading.il6XMax = length;
358     loading.il6YMin = 26;
359     loading.il6YMax = 39;
360     GrContextForegroundSet(&Context, ClrSalmon);
361     if (length%10 == 0) {
362         GrStringDrawCentered(&Context, "Loading. ", 11, 48, 20, true);
363     }
364     else if (length%10 == 1) {
365         GrStringDrawCentered(&Context, "Loading.. ", 11, 48, 20, true);
366     }
367     else if (length%10 == 2) {
368         GrStringDrawCentered(&Context, "Loading...", 11, 48, 20, true);
369     }
370     else {
371         GrStringDrawCentered(&Context, "Loading ", 11, 48, 20, true);
372     }
373     SysCtlDelay(150000);
374     GrContextForegroundSet(&Context, ClrDeepSkyBlue);
375     GrRectFill(&Context, &loading);
376 }
377
378 // clear the screen so that it is set for the main screen
379 GrContextForegroundSet(&Context, ClrBlack);
380 GrRectFill(&Context, &screen);
381 }
382
383
384
385 //*****
386 //
387 // Main function
388 //
389 //*****
390 int main(void) {
391     // Enable lazy stacking for interrupt handlers. This allows floating-point
392     // instructions to be used within interrupt handlers, but at the expense of
393     // extra stack usage.
394     FPU_LazyStackingEnable();
395
396     //*****
397     //
398     // Checking if #define is set for the use of 66MHz for the Clock frequency.
399     //
400     //*****
401     #define MHz80 // Uncommented for use of 66MHz, Commented otherwise.
402
403     #ifdef MHz80
404         // Setting the clock to run directly from the crystal.
405         SysCtlClockSet(SYSCTL_SYSDIV_2_5 | SYSCTL_USE_PLL | SYSCTL_OSC_MAIN | SYSCTL_XTAL_16MHZ);
406
407     #else // If not using 66MHz, then set to 16MHz
408         // Setting the clock to run directly from the crystal.
409         SysCtlClockSet(SYSCTL_SYSDIV_1 | SYSCTL_USE_OSC | SYSCTL_OSC_MAIN | SYSCTL_XTAL_16MHZ);
410
411     #endif
412
413     IntMasterDisable(); // Disable interrupts while set up is in progress
414
415     // Calling the initial functions to set up TM4C123G, its peripherals,
416     // OLED and clear PuTTY window for menu output.
417     initializations();
418     splash(); // Prints out the splash screen to OLED.
419     clear(); // Clears the PuTTY window for neatness.
420     printMenu(); // Prints the UART menu for user IO.
421
422     // Fill the part of the screen defined below to create a banner.
423     sRect.il6XMin = 0;
424     sRect.il6YMin = 0;
425     sRect.il6XMax = GrContextDpyWidthGet(&Context) - 1;
426     sRect.il6YMax = 9;
427     GrContextForegroundSet(&Context, ClrSlateGray);
428     GrRectFill(&Context, &sRect);
429     GrContextForegroundSet(&Context, ClrWhite);
430     GrStringDrawCentered(&Context, "00010000 01000000", -1, GrContextDpyWidthGet(&Context) / 2, 4, 0);
431
432     IntMasterEnable(); // Enables Interrupts
433
434     //*****
435     //
436     // The main while loop the function will stay in unless acted upon by the
437     // user through UART or an interrupt timer is being serviced.
438     //
439     //*****
440     while(whileLoop != 0) {
441         // Calling the 'heartbeat' function if specified to do so.
442         if(BlinkyToggle != 0) {
443             blinky();
444             BlinkyToggle++;
445         }
446
447         // Checking to see if the user has input a character and acting accordingly.
448         while(UARTCharsAvail(UART0_BASE)) {
449             CharacterInput = UARTCharGetNonBlocking(UART0_BASE);
450             menuSwitch();
451         }
452     }
453 }
454

```

APPENDIX C: Assembly Lab Code

```
1  #include "inc/hw_memmap.h"
2  #include "inc/hw_timer.h"
3      name isr_asm
4      section .text:CODE
5      extern ServicedCount
6      public isr_asm_start
7  isr_asm_start:
8      push{lr};
9
10     MOV    R1, #1
11     MOV32  R0, TIMER1_BASE
12     STR    R1, [R0, #0x24]
13
14     MOV32  R0, ServicedCount
15     LDR    R1, [[R0]]
16     ADD    R1, R1, #1
17     STR    R1, [R0]
18
19     pop {pc} ; return
20     end
```


APPENDIX C: Instructor Sign Off

Instructor Sign-off

Each laboratory group must complete a copy of this sheet.

Student A Name Andrea Gray

Student B Name Drew Grobmeier

TM4C Board Used 28

Lab Station Used 09

1. Demonstrate operation of a software project which calls an assembly language written subroutine. It is anticipated this will be the execution of an assembly language interrupt service routine as described herein, but execution of any assembly language written routine is sufficient for this signoff.

Instructors's Initials [Signature] Date/Time _____

2. Demonstrate operation of a software project which meets all of the requirements set for in the laboratory procedure. This is execution of a high speed interrupt service routine written in assembly language. At least (2) frequencies must be evaluated for each version ISR, note if the frequencies evaluated are different than those given below..

Operating Frequency	Number of ISR executions per second	
	ISR Written in 'C'	ISR Written in Assembly
16 MHz	103562	199300
80MHz	1875589	3029675

676E3
P=22

✓
P=25

Answer any questions the lab faculty may have, and demonstrate to the lab faculty that you have met the requirements of this laboratory exercise.

Instructors's Initials [Signature] Date/Time 15-APR 4:49pm

All Rights Met

5
/5

APPENDIX D: Definitions

I2C: Inter-Integrated Circuit

UAL: Unified Assembly Language

APPENDIX D: Citation and Resources

- [1] *Tiva™ TM4C123GH6PM Microcontroller: Data Sheet*. Rev. E. [eBook]. Austin: Texas Instruments, 2014, p.898. Available: <http://www.ti.com/lit/ds/symlink/tm4c123gh6pm.pdf>. [Accessed: 3-APR-2019].
- [2] Dr. Brian Davis, *Laboratory Exercise #9*, Embry-Riddle Aeronautical University Prescott Department of Computer, Mechanical, and Software Engineering, 2019.
- [3] *TivaWare™ Peripheral Driver Library: User's Guide*. Rev. 2016. [eBook]. Austin: Texas Instruments, 2013, p.354-355. Available: <http://www.ti.com/lit/ug/spmu298d/spmu298d.pdf>. [Accessed: 3-APR-2019].