## slurm\_hardware\_synthesis.py

```
import argparse
import os
from subprocess import call
# example call: python3 slurm hardware synthesis.py jacobi3d
# grab all arguments
parser = argparse.ArgumentParser()
parser.add_argument("name") # name of the design we want to synthesis
parser.add_argument("-N", default=1) # nodes, default: 1
parser.add_argument("-n", default=8) # task, default: 8
parser.add_argument("-c", default=8) # cores, default: 8
parser.add argument("--mem", default=131072) # memory, default: 131072MB=128GB
parser.add_argument("-o", default="outfile") # stdout, default: saved to outfile_NAME
parser.add_argument("-e", default="errfile") # stderr, default: saved to errfile_NAME
parser.add_argument("-t", default="24:00:00") # time requested hh:mm:ss, default: 24h
parser.add argument("--partition", default="long") # cluster queue, default: long
args = parser.parse_args()
if args.name is None:
    raise Exception("No design name specified, exit.")
header = [("-N", args.N), ("-n", args.n), ("-c", args.c), ("--mem", args.mem),
          ("-o", "{}_{}".format(args.o, args.name)), ("-e", "{}_{}".format(args.e, args.name)), ("-t", args.t),
          ("--partition", args.partition)]
home dir = os.path.expanduser("~")
_SDK_PATH = "source /apps/ault/intelFPGA_pro/19.1/hld/init_stratix.sh\n"
PYTHON PATH = "module load python/3.7.2\n"
CMAKE PATH = "module load cmake/3.14.0\n"
_GCC_PATH = "module load gcc/8.3.0\n"
# generate slurm batch job file
with open("{}/{}.sh".format(home_dir, args.name), "w") as f:
    f.write("#!/bin/sh\n")
    for item in header:
        f.write("#SBATCH {} {}\n".format(item[0], item[1]))
    f.write(_SDK_PATH)
    f.write( PYTHON PATH)
    f.write(_CMAKE_PATH)
    f.write(GCC PATH)
    f.write("cd {}/.dacecache/{}/build/\n".format(os.getcwd(), args.name))
    f.write("make intelfpga_compile_{}_hardware\n".format(args.name))
call(["sbatch", "{}/{}.sh".format(home dir, args.name)])
```