

# Andre Bododea

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## Education

- 09/2013 - 07/2017** **BEng Honours Electronics & Software Engineering, [University of Edinburgh](#), UK**  
***Relevant Courses:*** Digital System Design, Analogue Circuits, Computer Design, Computer Architecture, Software Testing, Software Engineering.  
***Honours Project:*** Optimisation of Gaussian Process Regression implementations for mobile computing platforms.  
***Edinburgh Award:*** Received for notable contributions to the student community.
- 09/2009 - 06/2013** **[Lowell High School](#), San Francisco, USA**  
*GPA 4.0; SAT score of 2100; SAT Subject Tests in Math Level 2, Physics, English Literature, U.S. History*

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## Professional Experience

- 08/2014 - 05/2015** **University Of Edinburgh, Resident Assistant (RA), Edinburgh, UK**
  - Did some stuff around the halls. Took care of students and whatnot, cool stuff bro.
  - All kinds of duties, managing, event planning, don't forget to say you're responsible.
- 06/2012 - 08/2012** **[San Francisco Parks & Recreation](#), Youth Restoration, San Francisco, USA**
  - Did restoration work in the major national parks in the San Francisco Bay Area via organization of Youth Crews in the parks.
  - Also held event planning responsibilities and organized multi-day camping trips for the crews.

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## Extracurricular Activities

- 06/2014 - 08/2014** **Tutoring, San Francisco, USA**
  - Tutored high school students in Advanced Placement Computer Science and Advanced Placement Calculus BC.
- 09/2012 - 05/2013** **[Lowell High School Robotics Team](#), Electronics Team Leader & Founding Member, San Francisco, USA**
  - Lead the electronics team to contribute to the design and building of my school's first competition robot.
  - Made it all the way to the regional Sacramento FRC competition, where our robot ultimately won the Chairman's Award.

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## Skills and Programming Languages

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|------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>Programming</b>     | Java, Python, C, C++, OpenCL, OpenGL, $\text{\LaTeX}$                                                                                                                                                 |
| <b>Electronics</b>     | Good fundamental Analogue and Digital circuit analysis skills, some experience with FPGA programming in Verilog, experience with circuit design and testing using CAD software with SPICE simulation. |
| <b>Languages</b>       | <i>English</i> (native), <i>Romanian</i> (fluent), <i>French</i> (basic).                                                                                                                             |
| <b>Other Interests</b> | I like 2 code and that is all.                                                                                                                                                                        |