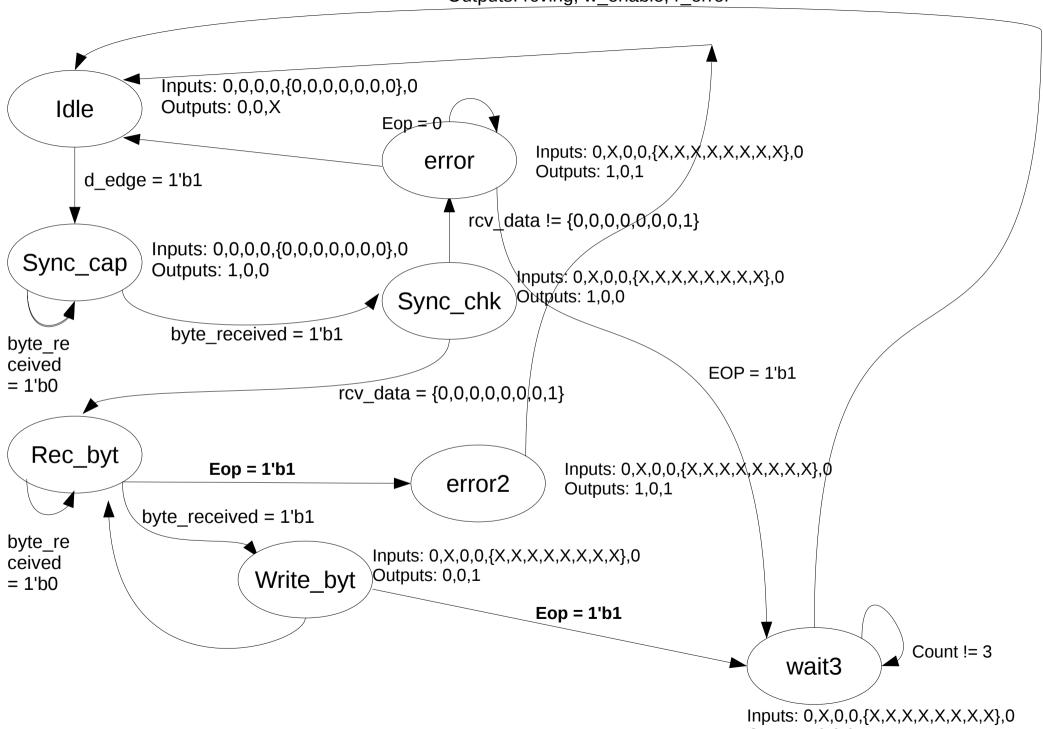
## State-Transition Diagram for the RCU

Inputs: n rst, d edge, shift enable, byte received, rcv data[7:0], eop Outputs: rcving, w enable, r error



Outputs: 0,0,0