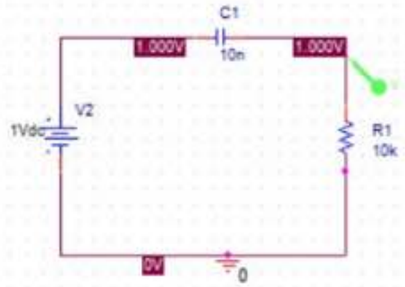
OrCAD Capture CIS - Lite - [/ - (SCHEMATIC1 : PAGE1)]

File Design Edit View Tools Place SI Analysis PSpice Accessories Options Window Help

cadence

SCHEMATIC1-CCp1

Start Page | CCP1* | PAGE1* | SCHEMATI...*



Place Part

Part

L

Part List: Filtered - \"L\"

IRGS14C40L/IRF
IRL3713SL/IRF
KM61257L/SRAM
KM6165L/SRAM
KM64257L/SRAM
KM681000L/SRAM
KMM6264AL/MISC
L/ANALOG

Libraries:

THERMISTOR_VISHAY
THYRISTR
TLINE
TRANSISTOR
TYCO_ELEC
XTAL

Packaging
Parts per Pkg: 1
Part:
Type: Homogeneous

Normal Convert

Search for Part

```
INFO(ORNET-1156): PSpice netlist generation complete
INFO(ORCAP-2191): Creating PSpice Netlist
INFO(ORNET-1041): Writing PSpice Flat Netlist C:\Cadence\CCP1-PSpiceFiles\SCHEMATIC1\SCHEMATIC1.net
INFO(ORNET-1156): PSpice netlist generation complete
INFO(ORCAP-2191): Creating PSpice Netlist
INFO(ORNET-1041): Writing PSpice Flat Netlist C:\Cadence\CCP1-PSpiceFiles\SCHEMATIC1\SCHEMATIC1.net
INFO(ORNET-1156): PSpice netlist generation complete
```

Laborator 1 02:10:13

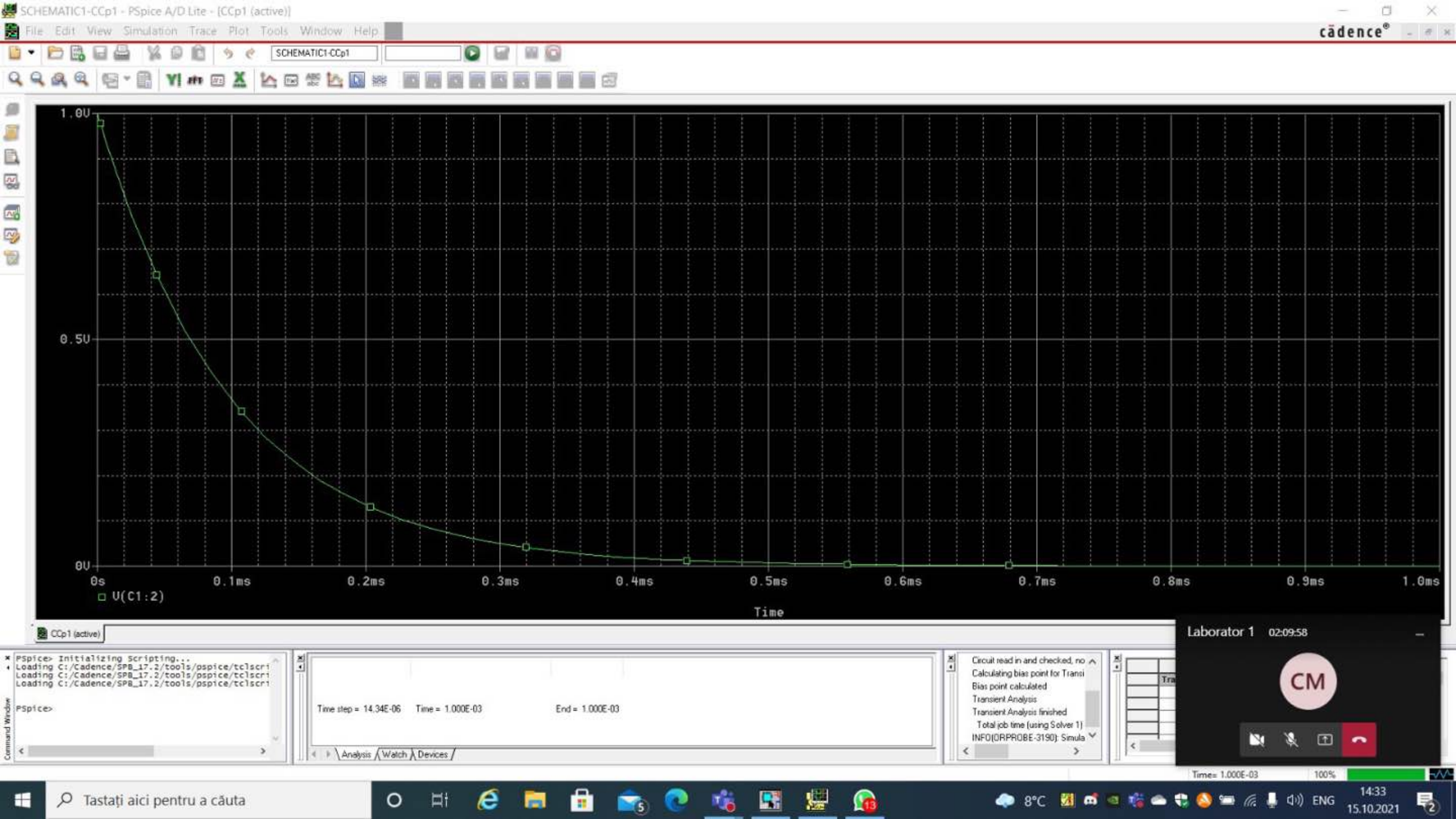
CM

0 items selected

Scale=100% X=0 Y=0

Tastați aici pentru a căuta

8°C ENG 14:34 15.10.2021


SCHEMATIC1-CCp1 - PSpice A/D Lite - [CCp1 (active)]
File Edit View Simulation Trace Plot Tools Window Help
cadence
SCHEMATIC1-CCp1
1.0U
0.5U
0U
0s
0.1ms
0.2ms
0.3ms
0.4ms
0.5ms
0.6ms
0.7ms
0.8ms
0.9ms
1.0ms
V(C1:2)
Time
CCp1 (active)
PSpice> Initializing Scripting...
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
PSpice>
Command Window
Time step = 14.34E-06 Time = 1.000E-03 End = 1.000E-03
Analysis Watch Devices
Circuit read in and checked, no
Calculating bias point for Transi
Bias point calculated
Transient Analysis
Transient Analysis finished
Total job time (using Solver 1)
INFO(ORPROBE-3190): Simula
Laborator 1 02:09:58
CM
Time= 1.000E-03
100%
Tastați aici pentru a căuta
8°C
ENG
14:33
15.10.2021

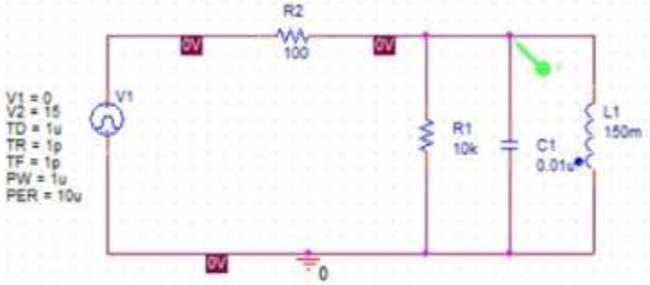
OrCAD Capture CIS - Lite - [/ - (SCHEMATIC1 : PAGE1)]

File Design Edit View Tools Place SI Analysis PSpice Accessories Options Window Help

cadence

SCHEMATIC1-CCp1

Start Page | CCP1* | PAGE1*



Place Part

Part

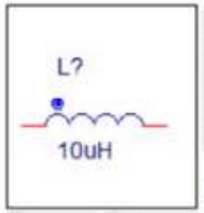
L

Part List: Filtered - \"L\"

IRGS14C40L/IRF
IRL3713SL/IRF
KM61257L/SRAM
KM6165L/SRAM
KM64257L/SRAM
KM681000L/SRAM
KMM6264AL/MISC
L/ANALOG

Libraries:

THERMISTOR_VISHAY
THYRISTR
TLINE
TRANSISTOR
TYCO_ELEC
XTAL



Packaging
Parts per Pkg: 1
Part:
Type: Homogeneous

Normal Convert

Search for Part

(2.00, 2.40)
INFO(ORCAP-2191): Creating PSpice Netlist
INFO(ORNET-1041): Writing PSpice Flat Netlist C:\Cadence\CCP1-PSpiceFiles\SCHEMATIC1\SCHEMATIC1.net
INFO(ORNET-1156): PSpice netlist generation complete
INFO(ORCAP-2191): Creating PSpice Netlist
INFO(ORNET-1041): Writing PSpice Flat Netlist C:\Cadence\CCP1-PSpiceFiles\SCHEMATIC1\SCHEMATIC1.net
INFO(ORNET-1156): PSpice netlist generation complete

Laborator 1 02:05:27

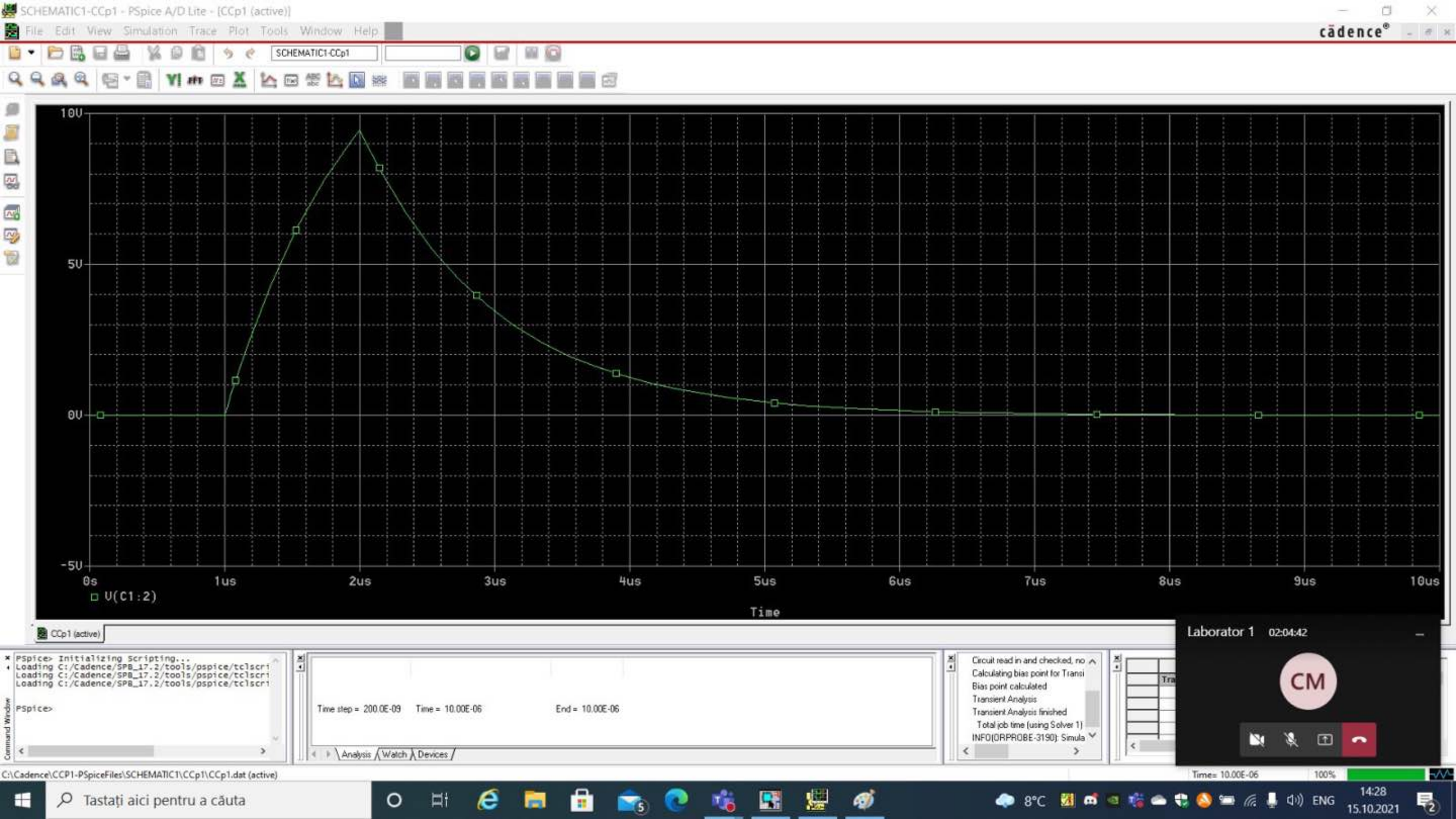
CM

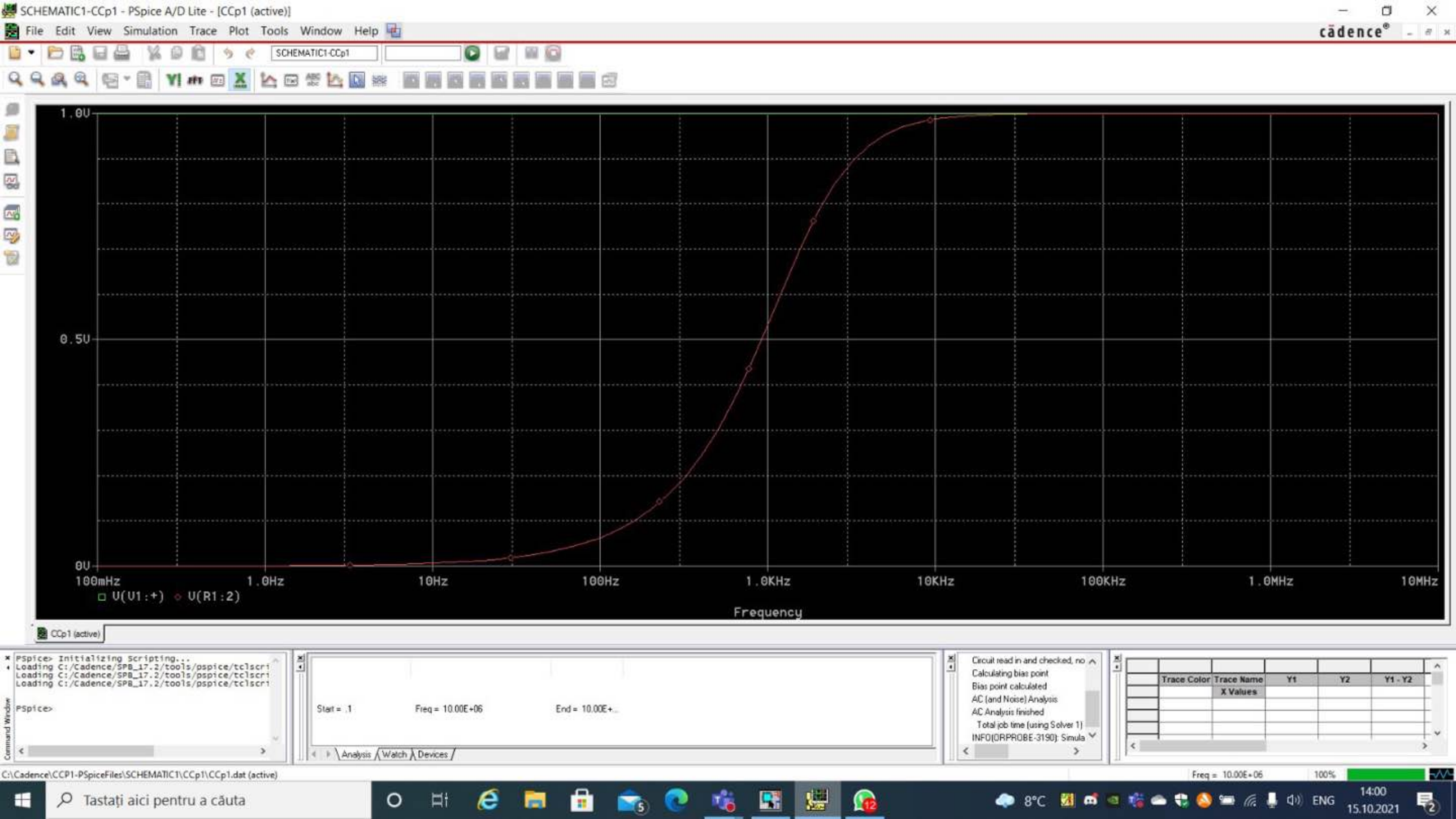
0 items selected

Scale=100% X=0 Y=0

Tastați aici pentru a căuta

8°C ENG 14:29 15.10.2021


SCHEMATIC1-CCp1 - PSpice A/D Lite - [CCp1 (active)]
File Edit View Simulation Trace Plot Tools Window Help
cadence
SCHEMATIC1-CCp1
10V
5V
0V
-5V
0s
1us
2us
3us
4us
5us
6us
7us
8us
9us
10us
V(C1:2)
Time
CCp1 (active)
PSpice> Initializing Scripting...
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
PSpice>
Command Window
Time step = 200.0E-09 Time = 10.00E-06 End = 10.00E-06
Analysis Watch Devices
Circuit read in and checked, no
Calculating bias point for Transi
Bias point calculated
Transient Analysis
Transient Analysis finished
Total job time (using Solver 1)
INFO(ORPROBE-3190): Simula
Laborator 1 02:04:42
CM
C:\Cadence\CCP1-PSpiceFiles\SCHEMATIC1\CCp1\CCp1.dat (active)
Time= 10.00E-06
100%
Tastați aici pentru a căuta
8°C
ENG
14:28
15.10.2021


SCHEMATIC1-CCp1 - PSpice A/D Lite - [CCp1 (active)]
File Edit View Simulation Trace Plot Tools Window Help
cadence
SCHEMATIC1-CCp1
1.0U
0.5U
0U
100mHz
1.0Hz
10Hz
100Hz
1.0KHz
10KHz
100KHz
1.0MHz
10MHz
V(U1:+) V(R1:2)
Frequency
CCp1 (active)
PSpice> Initializing Scripting...
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
Loading C:/Cadence/SPB_17.2/tools/pspice/tclscri
PSpice>
Command Window
Start = .1
Freq = 10.00E+06
End = 10.00E+..
Analysis Watch Devices
Circuit read in and checked, no
Calculating bias point
Bias point calculated
AC (and Noise) Analysis
AC Analysis finished
Total job time (using Solver 1)
INFO(ORPROBE-3190): Simula
Trace Color Trace Name Y1 Y2 Y1 - Y2
X Values
C:\Cadence\CCP1-PSpiceFiles\SCHEMATIC1\CCp1\CCp1.dat (active)
Freq = 10.00E+06
100%
Tastați aici pentru a căuta
8°C
ENG
14:00
15.10.2021