MPEG4AAC LC Decoder on C64x+/C674x

User's Guide

![Texas Instruments]

Read This First

About This Manual

This document describes how to install and work with Texas Instruments' (TI) MPEG4AAC LC Decoder implementation on the C64x+/C674x platform. It also provides a detailed Application Programming Interface (API) reference and information on the sample application that accompanies this component.

TI's codec implementations are based on the eXpressDSP Digital Media (XDM) standard. XDM is an extension of the eXpressDSP Algorithm Interface Standard (XDAIS).

Intended Audience

This document is intended for system engineers who want to integrate TI's codecs with other software to build a multimedia system based on the C64x+/C674x platform.

This document assumes that you are fluent in the C language, have a good working knowledge of Digital Signal Processing (DSP), digital signal processors, and DSP applications. Good knowledge of eXpressDSP Algorithm Interface Standard (XDAIS) and eXpressDSP Digital Media (XDM) standard will be helpful.

How to Use This Manual

This document includes the following chapters:

❑ **Chapter 1 - Introduction**, provides a brief introduction to the XDAIS and XDM standards. It also provides an overview of the codec and lists its supported features.

❑ **Chapter 2 - Installation Overview**, describes how to install, build, and run the codec.

❑ **Chapter 3 - Sample Usage**, describes the sample usage of the codec.

❑ **Chapter 4 - API Reference**, describes the data structures and interface functions used in the codec.

❑ **Chapter 5 – Frequently Asked Questions,** provides answers to few frequently asked questions related to using this decoder.

❑ **Appendix A - Optimal Function Placement Order for Minimizing Instruction Cache Penalty**, describes the optimal function

placement order for minimizing cache penalty due to instruction cache misses.

Related Documentation From Texas Instruments

The following documents describe TI's DSP algorithm standards such as, XDAIS and XDM. To obtain a copy of any of these TI documents, visit the Texas Instruments website at www.ti.com.

❑ *TMS320 DSP Algorithm Standard Rules and Guidelines* (literature number SPRU352) defines a set of requirements for DSP algorithms that, if followed, allow system integrators to quickly assemble production-quality systems from one or more such algorithms.

❑ *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360) describes all the APIs that are defined by the TMS320 DSP Algorithm Interface Standard (also known as XDAIS) specification.

❑ *Technical Overview of eXpressDSP - Compliant Algorithms for DSP Software Producers* (literature number SPRA579) describes how to make algorithms compliant with the TMS320 DSP Algorithm Standard, which is part of TI's eXpressDSP technology initiative.

❑ *Using the TMS320 DSP Algorithm Standard in a Static DSP System* (literature number SPRA577) describes how an eXpressDSP-compliant algorithm may be used effectively in a static system with limited memory.

❑ *DMA Guide for eXpressDSP-Compliant Algorithm Producers and Consumers* (literature number SPRA445) describes the DMA architecture specified by the TMS320 DSP Algorithm Standard (XDAIS). It also describes two sets of APIs used for accessing DMA resources: the IDMA2 abstract interface and the ACPY2 library.

❑ *eXpressDSP Digital Media (XDM) Standard API Reference* (literature number SPRUEC8)

The following documents describe TMS320 devices and related support tools:

❑ *Design and Implementation of an eXpressDSP-Compliant DMA Manager for C6X1X* (literature number SPRA789) describes a C6x1x-optimized (C6211, C6711) ACPY2 library implementation and DMA Resource Manager.

❑ *TMS320c64x+ Megamodule* (literature number SPRAA68) describes the enhancements made to the internal memory and describes the new features which have been added to support the internal memory architecture's performance and protection.

❑ *TMS320C64x+ DSP Megamodule Reference Guide* (literature number SPRU871) describes the C64x+ megamodule peripherals.

❑ *TMS320C64x to TMS320C64x+ CPU Migration Guide* (literature number SPRAA84) describes migration from the Texas Instruments TMS320C64x™ digital signal processor (DSP) to the TMS320C64x+™ DSP.

❑ *TMS320C6000 Optimizing Compiler v 6.0 Beta User's Guide* (literature number SPRU187N) explains how to use compiler tools such as compiler, assembly optimizer, standalone simulator, library-build utility, and C++ name demangler.

❑ *TMS320C64x/C64x+ DSP CPU and Instruction Set Reference Guide* (literature number SPRU732) describes the CPU architecture, pipeline, instruction set, and interrupts of the C64x and C64x+ DSPs.

❑ TMS320DM6446 Digital Media System-on-Chip (literature number SPRS283)

❑ *TMS320DM6446 Digital Media System-on-Chip Errata (Silicon Revision 1.0)* (literature number SPRZ241) describes the known exceptions to the functional specifications for the TMS320DM6446 Digital Media System-on-Chip (DMSoC).

❑ *TMS320DM6443 Digital Media System-on-Chip* (literature number SPRS282)

❑ *TMS320DM6443 Digital Media System-on-Chip Errata (Silicon Revision 1.0)* (literature number SPRZ240) describes the known exceptions to the functional specifications for the TMS320DM6443 Digital Media System-on-Chip (DMSoC).

❑ *TMS320DM644x DMSoC DSP Subsystem Reference Guide* (literature number SPRUE15) describes the digital signal processor (DSP) subsystem in the TMS320DM644x Digital Media System-on-Chip (DMSoC).

❑ *TMS320DM644x DMSoC ARM Subsystem Reference Guide* (literature number SPRUE14) describes the ARM subsystem in the TMS320DM644x Digital Media System on a Chip (DMSoC).

Related Documentation

You can use the following documents to supplement this user guide:

❑ *MPEG2 AAC standards document [ISO/IEC 13818-7:2003]*

❑ *MPEG4 AAC standards document [ISO/IEC 14496-3:1999(E)]*

Abbreviations

The following abbreviations are used in this document.

Table 1-1. List of Abbreviations

Abbreviation	Description
AAC	Advanced Audio Coding
ADIF	Audio Data Interchange Format
ADTS	Audio Data Transport Stream
HE	High Efficiency

Abbreviation	Description
HQ	High Quality
LC	Low Complexity
LP	Low Power
PNS	Perceptual Noise Substitution
PCM	Pulse Code Modulation
RMS	Root Mean Square
SBR	Spectral Band Replication
XDAIS	eXpressDSP Algorithm Interface Standard
XDM	eXpressDSP Digital Media

Text Conventions

The following conventions are used in this document:

❑ Text inside back-quotes (") represents pseudo-code.

❑ Program source code, function and macro names, parameters, and command line commands are shown in a `mono-spaced` font.

Product Support

When contacting TI for support on this codec, quote the product name (MPEG4AAC LC Decoder on C64x+/C674x) and version number. The version number of the codec is included in the Title of the Release Notes that accompanies this codec.

Trademarks

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Introduction

This chapter provides a brief introduction to XDAIS and XDM. It also provides an overview of TI's implementation of the MPEG4AAC LC Decoder on the C64x+/C674x platform and its supported features.

1.1 Overview of XDAIS and XDM

TI's multimedia codec implementations are based on the eXpressDSP Digital Media (XDM) standard. XDM is an extension of the eXpressDSP Algorithm Interface Standard (XDAIS).

1.1.1 XDAIS Overview

An eXpressDSP-compliant algorithm is a module that implements the abstract interface IALG. The IALG API takes the memory management function away from the algorithm and places it in the hosting framework. Thus, an interaction occurs between the algorithm and the framework. This interaction allows the client application to allocate memory for the algorithm and also share memory between algorithms. It also allows the memory to be moved around while an algorithm is operating in the system. In order to facilitate these functionalities, the IALG interface defines the following APIs:

- ❑ `algAlloc()`
- ❑ `algInit()`
- ❑ `algActivate()`
- ❑ `algDeactivate()`
- ❑ `algFree()`

The `algAlloc()` API allows the algorithm to communicate its memory requirements to the client application. The `algInit()` API allows the algorithm to initialize the memory allocated by the client application. The `algFree()` API allows the algorithm to communicate the memory to be freed when an instance is no longer required.

Once an algorithm instance object is created, it can be used to process data in real-time. The `algActivate()` API provides a notification to the algorithm instance that one or more algorithm processing methods is about to be run zero or more times in succession. After the processing methods have been run, the client application calls the `algDeactivate()` API prior to reusing any of the instance's scratch memory.

The IALG interface also defines three more optional APIs `algControl()`, `algNumAlloc()`, and `algMoved()`. For more details on these APIs, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

1.1.2 XDM Overview

In the multimedia application space, you have the choice of integrating any codec into your multimedia system. For example, if you are building a video decoder system, you can use any of the available video decoders (such as MPEG4, H.263, or H.264) in your system. To enable easy integration with the client application, it is important that all codecs with similar functionality use similar APIs. XDM was primarily defined as an

extension to XDAIS to ensure uniformity across different classes of codecs (For example audio, video, image, and speech). The XDM standard defines the following two APIs:

❑   control()

❑   process()

The control() API provides a standard way to control an algorithm instance and receive status information from the algorithm in real-time. The control() API replaces the algControl() API defined as part of the IALG interface. The process() API does the basic processing (encode/decode) of data.

Apart from defining standardized APIs for multimedia codecs, XDM also standardizes the generic parameters that the client application must pass to these APIs. The client application can define additional implementation specific parameters using extended data structures.

The following figure depicts the XDM interface to the client application.

As depicted in the figure, XDM is an extension to XDAIS and forms an

```
┌─────────────────────────────────────┐
│          Client Application          │
└─────────────────────────────────────┘
                  ⇕
┌─────────────────────────────────────┐
│            XDM Interface             │
├─────────────────────────────────────┤
│        XDAIS Interface (IALG)        │
├─────────────────────────────────────┤
│         TI's Codec Algorithms        │
└─────────────────────────────────────┘
```
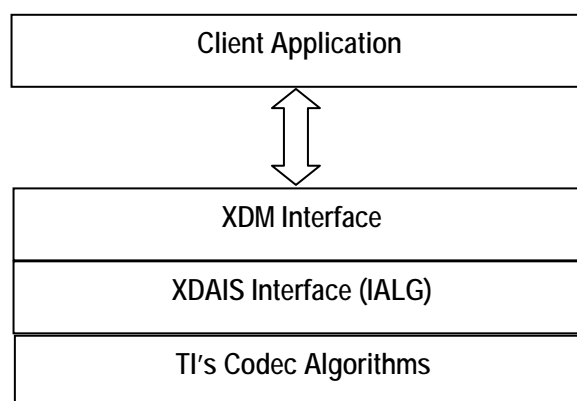
interface between the client application and the codec component. XDM insulates the client application from component-level changes. Since TI's multimedia algorithms are XDM-compliant, it provides you with the flexibility to use any TI algorithm without changing the client application code. For example, if you have developed a client application using an XDM-compliant MPEG4 video decoder, then you can easily replace MPEG4 with another XDM-compliant video decoder, say H.263, with minimal changes to the client application.

For more details, see eXpressDSP Digital Media (XDM) Standard API Reference (literature number SPRUEC8).

1.2 Overview of MPEG4AAC LC Decoder

Advanced Audio Coding (AAC) is an audio data compression format. This coding technique uses perceptual filter bank, which is a sophisticated masking model, noise-shaping techniques, and channel coupling. It adopts noiseless coding and bit-allocation to provide the maximum compression within the constraints of providing the highest possible quality at smaller bit-rates.

1.3 Supported Services and Features

This user guide accompanies TI's implementation of MPEG4AAC LC Decoder on the C64x+/C674x platform.

This version of the codec has the following supported features:

❑ Supports MPEG4 AAC Low Complexity (LC) object type implementations

❑ Supports MPEG2 AAC Low Complexity (LC) object type implementations

❑ Supports mono and stereo output

❑ Supports RAW data input format

❑ Supports Audio Data Interchange Format (ADIF) , and Audio Data Transport Stream (ADTS) input formats. These files are encoded with ISO/IEC 13818-7 or 14496-3 compliant encoders.

❑ Supports bit-rates ranging from 8kbps to 1152kbps.

❑ Supports sampling frequency range of 8 kHz – 96 kHz as per ISO/IEC 14496-3 standard.

❑ Supports maximum bit-rate based on the sampling frequency as per standard

❑ eXpressDSP Digital Media v1.0(XDM1.0 IAUDDEC1) compliant

❑ This release supports both COFF and ELF ABI, release package contains separate libraries and project files for COFF and ELF ABI.

Installation Overview

This chapter provides a brief description on the system requirements and instructions for installing the codec component. It also provides information on building and running the sample test application.

2.1   System Requirements

This section describes the hardware and software requirements for the normal functioning of the codec component.

2.1.1   *Hardware*

This codec has been built and tested on the DM8148 EVM with XDS 510 USB.

This codec can be used on any of TI's C64x+/C674x based platforms such as DM8148, DM644x, DM648, DM643x, DM6467, OMAP35xx , DM816x, DM814x, OMAP-L1x and their derivatives.

2.1.2   *Software*

The following are the software requirements for the normal functioning of the codec:

❑   **Development Environment:** This project is developed using Code Composer Studio version 4.2.0.10018 for C6000from Texas Instruments.

❑   **Code Generation Tools:** This project is compiled, assembled, archived, and linked using the code generation tools version 7.2.2.

❑   DSP/BIOS: This project has been validated with DSP/BIOS version 5.41.02.14 for COFF ABI and 6.31 for ELF ABI.

2.2   Installing the Component

The codec component is released as a compressed archive. To install the codec, extract the contents of the zip file onto your local hard disk. The zip file extraction creates a top-level directory called 100_A_AAC_LC_D_1_41_00, under which another directory named C674X_AAC_LC is created.

Figure 2-1 shows the sub-directories created in C674X_AAC_LC directory.
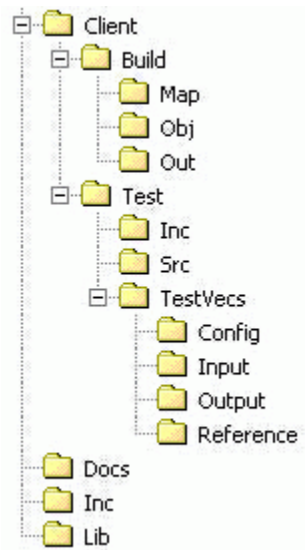
Figure 2-1. Component Directory Structure

Table 2-1 provides a description of the sub-directories created in the C674X_AAC_LC directory.

Table 2-1. Component Directories

Sub-Directory	Description
\Inc	Contains XDM related header files which allow interface to the codec library
\Lib	Contains the codec library file
\Docs	Contains user guide and datasheet
\Client\Build	Contains the sample test application project (.pjt) file
\Client\Build\Map	Contains the memory map generated on compilation of the code
\Client\Build\Obj	Contains the intermediate .asm and/or .obj file generated on compilation of the code
\Client\Build\Out	Contains the final application executable (.out) file generated by the sample test application
\Client\Test\Src	Contains application C files
\Client\Test\Inc	Contains header files needed for the application code
\Client\Test\TestVecs\Input	Contains input test vectors
\Client\Test\TestVecs\Output	Contains output generated by the codec
\Client\Test\TestVecs\Reference	Contains read-only reference output to be used for verifying against codec output
\Client\Test\TestVecs\Config	Contains configuration parameter files

2.3 Before Building the Sample Test Application

This codec is accompanied by a sample test application. To run the sample test application, you need DSP/BIOS. This version of the codec has been validated with DSP/BIOS version 5_41_02_14 for COFF ABI and 6.31 for ELF ABI.

2.4 Building and Running the Sample Test Application

This codec is accompanied by a sample test application. This application will run in TI's Code Composer Studio development environment. To build and run the sample application in Code Composer Studio, follow these steps:

1) Verify that you have installed TI's Code Composer Studio version 4.2.0.10018 and code generation tools version 7.2.2.

2) Verify that the codec object library aacdec_tii_lc_coff.l64P and aacdec_tii_lc_elf.l64P exists in the \Lib sub-directory.

3) Open the test application project file, TestAppDecoder_LC_COFF.pjt, TestAppDecoder_LC_ELF.pjt in Code Composer Studio. This file is available in the \Client\Build sub-directory.

4) Select **Project > Build Active Project** to build the sample test applications TestAppDecoder_LC_COFF.pjt and TestAppDecoder_LC_ELF.pjt. This creates an executable file, TestAppDecoder_lc_coff.out and TestAppDecoder_lc_elf.out respectively in the \Client\Build\Out sub-directory.

5) Select proper Target in CCS4.2..

6) Select **Target >Launch TI Debugger**, browse to the \Client\Build\Out sub-directory, select the codec executable created in step 4, and load it into Code Composer Studio in preparation for execution.

7) Select **View > Debug > Run/ Target > Run** to execute the sample test application.

   The sample test application takes the input files stored in the \Client\Test\TestVecs\Input sub-directory, runs the codec, and uses the reference files stored in the \Client\Test\TestVecs\Reference sub-directory to verify that the codec is functioning as expected.

8) On successful completion, the application displays one of the following messages for each frame:

   o "Decoder compliance test passed/failed" (for compliance check mode)

   o "Decoder output dump completed" (for output dump mode)

2.5 Configuration Files

This codec is shipped along with a generic configuration file (Testvecs.cfg) that specifies input and output/reference files for the sample test application.

2.5.1 Generic Configuration File

The sample test application shipped along with the codec uses the configuration file, Testvecs.cfg for determining the input and output/reference files for running the codec and checking for compliance. The Testvecs.cfg file is available in the \Client\Test\TestVecs\Config sub-directory.

The format of the Testvecs.cfg file is:

```
X
Input
Output/Reference
Y
```

where:

❑ X may be set as:

   o 1 - for compliance checking, no output file is created

   o 0 - for writing the output to the output file

❑ Input is the input file name (use complete path).

❑ Output/Reference is the output file name (if X is 0) or reference file name (if X is 1).

❑ Y may be set as

   o 1 - for stereo as desired channel mode

   o 0 - for mono as desired channel mode

A sample Testvecs.cfg file is as shown:

```
1
..\..\Test\TestVecs\Input\davinci.aac
..\..\Test\TestVecs\Reference\davinci_ref.pcm
1

0
..\..\Test\TestVecs\Input\davinci.aac
..\..\Test\TestVecs\Output\davinci.pcm
1
```

2.6 Standards Conformance and User-Defined Inputs

To check the conformance of the codec for the default input file shipped along with the codec, follow the steps as described in Section 2.4.

To check the conformance of the codec for other input files of your choice, follow these steps:

1) Copy the input files to the \Client\Test\TestVecs\Inputs sub-directory.

2) Copy the reference files to the \Client\Test\TestVecs\Reference sub-directory.

3) Edit the configuration file, Testvecs.cfg available in the \Client\Test\TestVecs\Config sub-directory. For details on the format of the Testvecs.cfg file, see Section 2.5.1.

4) Execute the sample test application. On successful completion, the application displays one of the following message for each frame:

   o "Decoder compliance test passed/failed" (if X is 1)

   o "Decoder output dump completed" (if x is 0)

If you have chosen the option to write to an output file (x is 0), you can use any standard file comparison utility to compare the codec output with the reference output and check for conformance.

Note:

The comparison is valid only with a set of vectors provided as part of the release package

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2.7 Uninstalling the Component

To uninstall the component, delete the codec directory from your hard disk.

Sample Usage

This chapter provides a detailed description of the sample test application that accompanies this codec component.

3.1   Overview of the Test Application

The test application exercises the `IAUDDEC1` base class of the MPEG4AAC LC Decoder library. The main test application files are TestAppDecoder.c and TestAppDecoder.h. These files are available in the \Client\Test\Src and \Client\Test\Inc sub-directories respectively.

Figure 3-1 depicts the sequence of APIs exercised in the sample test application.
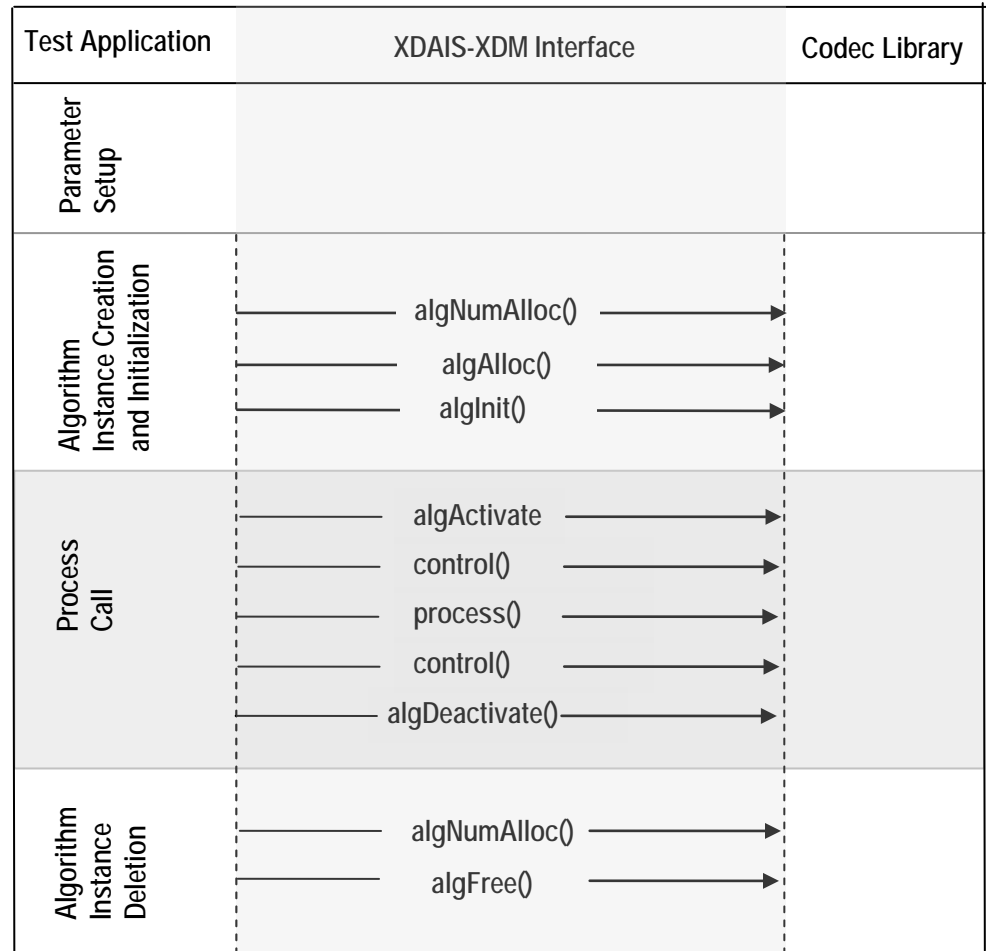
Figure 3-1. Test Application Sample Implementation

> **Note:**
>
> Audio codecs do not use al`gActivate()` and al`gDeactivate()` APIs.

The test application is divided into four logical blocks:

❑ Parameter setup

❑ Algorithm instance creation and initialization

❑ Process call

❑ Algorithm instance deletion

3.1.1 *Parameter Setup*

Each codec component requires various codec configuration parameters to be set at initialization. For example, a video codec requires parameters such as video height, video width, and so on. The test application obtains the required parameters from the Decoder configuration files.

In this logical block, the test application does the following:

1) Opens the generic configuration file, Testvecs.cfg and reads the input file name and output file name.

   For more details on the configuration files, see Section 2.5.

2) Reads the input bit-stream into the application input buffer.

After successful completion of these steps, the test application does the algorithm instance creation and initialization.

3.1.2 *Algorithm Instance Creation and Initialization*

In this logical block, the test application accepts the various initialization parameters and returns an algorithm instance pointer. The following APIs are called in sequence:

1) `algNumAlloc()` - To query the algorithm about the number of memory records it requires.

2) `algAlloc()` - To query the algorithm about the memory requirement to be filled in the memory records.

3) `algInit()` - To initialize the algorithm with the memory structures provided by the application.

A sample implementation of the create function that calls `algNumAlloc()`, `algAlloc()`, and `algInit()` in sequence is provided in the `ALG_create()` function implemented in the alg_create.c file.

3.1.3 Process Call

After algorithm instance creation and initialization, the test application does the following:

1) Sets the dynamic parameters (if they change during run-time) by calling the `control()` function with the `XDM_SETPARAMS` command.

2) Sets the input and output buffer descriptors required for the `process()` function call. The input and output buffer descriptors are obtained by calling the `control()` function with the `XDM_GETBUFINFO` command.

3) Calls the `process()` function to encode/decode a single frame of data. The behavior of the algorithm can be controlled using various dynamic parameters (see Section 4.2.1.7). The inputs to the process function are input and output buffer descriptors, pointer to the `IAUDDEC1_InArgs` and `IAUDDEC1_OutArgs` structures.

There could be any ordering of `control()` and `process()` functions. The following APIs are called in sequence:

1) `control()` (optional) - To query the algorithm on status or setting of dynamic parameters and so on, using the six available control commands.

2) `process()` - To call the Decoder with appropriate input/output buffer and arguments information.

3) `control()` (optional) - To query the algorithm on status or setting of dynamic parameters and so on, using the six available control commands.

The do-while loop encapsulates frame level `process()` call and updates the input buffer pointer every time before the next call. The do-while loop breaks off either when an error condition occurs or when the input buffer exhausts. It also protects the `process()` call from file operations by placing appropriate calls for cache operations. The test application does a cache invalidate for the valid input buffers before `process()` and a cache write back invalidate for output buffers after `process()`.

In the sample test application, after calling `process()`, the output data is either dumped to a file or compared with a reference file.

3.1.4   *Algorithm Instance Deletion*

Once decoding/encoding is complete, the test application deletes the current algorithm instance. The following APIs are called in sequence:

1) `algNumAlloc()` - To query the algorithm about the number of memory records it used.

2) `algFree()` - To query the algorithm to get the memory record information.

A sample implementation of the delete function that calls `algNumAlloc()` and `algFree()` in sequence is provided in the `ALG_delete()` function implemented in the alg_create.c file.

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API Reference

This chapter provides a detailed description of the data structures and interfaces functions used in the codec component.

4.1 Symbolic Constants and Enumerated Data Types

This section summarizes all the symbolic constants specified as either
#define macros and/or enumerated C data types. For each symbolic
constant, the semantics or interpretation of the same is also provided.

Table 4-1. List of Enumerated Data Types

Group or Enumeration Class	Symbolic Constant Name	Description or Evaluation
IAUDIO_ChannelMode	IAUDIO_1_0	Mono
	IAUDIO_2_0	Stereo
	IAUDIO_11_0	Dual Mono
	IAUDIO_3_0	Left, Right, Center. Not supported in this version of AAC Decoder.
	IAUDIO_2_1	Left, Right, Sur. Not supported in this version of AAC Decoder.
	IAUDIO_3_1	Left, Right, Center, Sur. Not supported in this version of AAC Decoder.
	IAUDIO_2_2	Left, Right, SurL, SurR. Not supported in this version of AAC Decoder.
	IAUDIO_3_2	Left, Right, Center, SurL, SurR. Not supported in this version of AACDecoder.
	IAUDIO_2_3	Left, Right, SurL, SurR, surC. Not supported in this version of AAC Decoder.
	IAUDIO_3_3	Left, Right, Center, SurL, SurR, surC. Not supported in this version of AAC Decoder.
	IAUDIO_3_4	Left, Right, Center, SurL, SurR, sideL, sideR. Not supported in this version of AAC Decoder.
IAUDIO_PcmFormat	IAUDIO_BLOCK	Left channel data followed by right channel data. Note: For single channel (mono), right channel data will be same as left channel data.

Group or Enumeration Class	Symbolic Constant Name	Description or Evaluation
	IAUDIO_INTERLEAVED	Left and right channel data interleaved.
		Note: For single channel (mono), right channel data will be same as left channel data.
IAUDIO_DualMonoMode	IAUDIO_DUALMONO_LR	Play / Encode both left and right channel.
	IAUDIO_DUALMONO_LEFT	Play / Encode only left channel.
	IAUDIO_DUALMONO_RIGHT	Play / Encode only right channel.
	IAUDIO_DUALMONO_LR_MIX	Mix and play
IAUDIO_EncMode	IAUDIO_CBR	Constant bit-rate
	IAUDIO_VBR	Variable bit-rate
XDM_DataFormat	XDM_BYTE	Big endian stream
	XDM_LE_16	16-bit little endian stream
	XDM_LE_32	32-bit little endian stream
	XDM_LE_64	64 bit little endian stream
	XDM_BE_16	16 bit big endian stream
	XDM_BE_32	32 bit big endian stream
	XDM_BE_64	64 bit big endian stream
XDM_CmdId	XDM_GETSTATUS	Query algorithm instance to fill Status structure
	XDM_SETPARAMS	Set run-time dynamic parameters through the DynamicParams structure
	XDM_RESET	Reset the algorithm
	XDM_SETDEFAULT	Initialize all fields in DynamicParams structure to default values specified in the library
	XDM_FLUSH	Handle end of stream conditions. This command forces algorithm instance to output data without additional input.
		Not applicable for MPEG4AAC LC Decoder. Just returns IALG_EOK.
	XDM_GETBUFINFO	Query algorithm instance regarding the properties of input and output buffers

Group or Enumeration Class	Symbolic Constant Name	Description or Evaluation
	XDM_GETVERSION	Query the algorithm version. Not applicable to MPEG4AAC LC Decoder. Just returns `IALG_EOK`.
XDM_ErrorBit		The bit fields in the 32-bit error code are interpreted as shown.
	XDM_PARAMSCHANGE	Bit 8 ❑ 1 - Sequence Parameters Change ❑ 0 - Ignore Not applicable for AAC-LC Decoder.
	XDM_APPLIEDCONCEALMENT	Bit 9 ❑ 1 - Applied concealment ❑ 0 - Ignore Not applicable for MPEG4AAC LC Decoder.
	XDM_INSUFFICIENTDATA	Bit 10 ❑ 1 - Insufficient input data ❑ 0 - Ignore
	XDM_CORRUPTEDDATA	Bit 11 ❑ 1 - Invalid data ❑ 0 - Ignore
	XDM_CORRUPTEDHEADER	Bit 12 ❑ 1 - Corrupted frame header ❑ 0 - Ignore
	XDM_UNSUPPORTEDINPUT	Bit 13 ❑ 1 - Unsupported feature/parameter in input ❑ 0 - Ignore
	XDM_UNSUPPORTEDPARAM	Bit 14 ❑ 1 - Unsupported input parameter or configuration ❑ 0 - Ignore Not applicable for MPEG4AAC LC Decoder.
	XDM_FATALERROR	Bit 15 ❑ 1- Fatal error (stop decoding) ❑ 0 - Recoverable error

Note:

The remaining bits that are not mentioned in `XDM_ErrorBit` are interpreted as:

❏ Bit 16-32:Reserved

❏ Bit 0-7: Codec and implementation specific

The algorithm can set multiple bits to 1 depending on the error condition.

The MPEG4AAC LC Decoder specific error status messages are listed in Table 4-2. The Value column indicates the decimal value of the last 8-bits reserved for codec specific error statuses.

Table 4-2. MPEG4AAC LC Decoder Error Status

Symbolic Constant Name	Value	Description or Evaluation
AACDEC_TII_NO_ERROR	0	Successful decoding
AACDEC_TII_SYNC_NOT_FOUND	1	ADTS stream sync word not found
AACDEC_TII_NOT_ENOUGH_CH	2	Not enough channels supported (multichannel error for LC)
AACDEC_TII_PRED_DETECTED	3	Prediction detected, but not supported
AACDEC_TII_LFE_DETECTED	4	LFE channel detected, but not supported
AACDEC_TII_GAIN_DETECTED	5	Gain control not implemented
AACDEC_TII_CCE_DETECTED	6	Coupling channel detected, but not supported
AACDEC_TII_PULSE_NOT_ALLOWED	7	Pulse is not supported for short window
AACDEC_TII_SFB_SHORT	8	Maximum scale factor band too large (Word16 window)
AACDEC_TII_HUFFMAN_ERROR	9	Error in Huffman decoding
AACDEC_TII_PROFILE_ERROR	10	Profile is not LC
AACDEC_TII_ELE_NOT_SUPPORTED	11	Element is different from SCE, CPE, LFE
AACDEC_TII_SAM_FREQ_NOT_SUPPORTED	12	Sampling frequency is not supported
AACDEC_TII_ADTS_FRAME_LEN_INCORRECT	13	ADTS frame length incorrect
AACDEC_TII_INSUFFICIENT_INPUT_DATA	14	Data in input buffer is insufficient to decode frame

Symbolic Constant Name	Value	Description or Evaluation
AACDEC_TII_INVALID_SIZE	15	Invalid size of input structure.
AACDEC_TII_CH_ELE_COMBINA TION_ERROR	16	Incorrect channel element combination
AACDEC_TII_INVALID_POINTE R	17	Invalid pointer error
AACDEC_TII_CRC_MISMATCH_E RROR	18	Cyclic redundancy check error
AACDEC_TII_INSTANCE_TAG_M ISMATCH	19	Instance tag mismatch error
AACDEC_TII_INVALID_PARAME TER	20	Generic code for invalid parameters
AACDEC_TII_INVALID_SBR_ST REAM	21	Invalid SBR stream (not applicable to AAC LC decoder)

Note:

The errors AACDEC_TII_ADTS_FRAME_LEN_INCORRECT, AACDEC_TII_INSTANCE_TAG_MISMATCH, AACDEC_TII_CRC_MISMATCH_ERROR , AACDEC_TII_CH_ELE_COMBINATION_ERROR, and AACDEC_TII_ADTS_FRAME_LEN_INCORRECT are just warnings. Output is valid when decoder returns these error.

4.2 Data Structures

This section describes the XDM defined data structures that are common across codec classes. These XDM data structures can be extended to define any implementation specific parameters for a codec component.

4.2.1 Common XDM Data Structures

This section includes the following common XDM data structures:

❑ `XDM1_SingleBufDesc`

❑ `XDM1_BufDesc`

❑ `XDM_AlgBufInfo`

❑ `IAUDDEC1_Fxns`

❑ `IAUDDEC1_Params`

❑ `IAUDDEC1_DynamicParams`

❑ `IAUDDEC1_InArgs`

❑ `IAUDDEC1_Status`

❑ `IAUDDEC1_OutArgs`

4.2.1.1   XDM1_SingleBufDesc

‖ Description

This structure defines the single buffer descriptor for input and output buffers.

‖ Fields

Field	Datatype	Input/Output	Description
*buf	XDAS_Int8	Input	Pointer to a buffer address
bufSize	XDAS_Int32	Input	Size of each buffer in bytes
accessMask	XDAS_Int32	Output	Mask filled by the algorithm

4.2.1.2   XDM1_BufDesc

‖ Description

This structure defines the buffer descriptor for input and output buffers.

‖ Fields

Field	Datatype	Input/Output	Description
numBufs	XDAS_Int32	Input	Number of buffers
descs[XDM_MAX_IO_BUFFERS]	XDM1_SingleBufDesc	Input	Buffer descriptors

4.2.1.3   XDM_AlgBufInfo

‖ Description

This structure defines the buffer information descriptor for input and output buffers. This structure is filled when you invoke the control() function with the XDM_GETBUFINFO command.

‖ Fields

Field	Datatype	Input/Output	Description
minNumInBufs	XDAS_Int32	Output	Number of input buffers
minNumOutBufs	XDAS_Int32	Output	Number of output buffers

Field	Datatype	Input/ Output	Description
minInBufSize[XDM_ MAX_IO_BUFFERS]	XDAS_Int32	Output	Size in bytes required for each input buffer
minOutBufSize[XDM _MAX_IO_BUFFERS]	XDAS_Int32	Output	Size in bytes required for each output buffer

---

Note:

For MPEG4AAC LC Decoder, the buffer details are:

❑ Number of input buffer required is 1.

❑ Number of output buffer required is 1.

❑ The input buffer size (in bytes) is 3840 bytes.

❑ The output buffer size (in bytes) is 4096 bytes for 16 bit audio sample size, two channel output (stereo).

4.2.1.4   IAUDDEC1_Fxns

‖ Description

This structure contains pointers to all the XDAIS and XDM interface functions.

‖ Fields

Field	Datatype	Input/ Output	Description
ialg	IALG_Fxns	Input	Structure containing pointers to all the XDAIS interface functions. For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).
*process	XDAS_Int32	Input	Pointer to the process() function
*control	XDAS_Int32	Input	Pointer to the control() function

4.2.1.5   IAUDDEC1_Params

‖ **Description**

This structure defines the creation parameters for an algorithm instance object. Set this data structure to NULL, if you are not sure of the values to specify for these parameters.

‖ **Fields**

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
outputPCMWidth	XDAS_Int32	Input	Number of bits per output PCM Sample Sample (16 bits/sample or 24 bits/sample). Default is 16 bits/sample.
pcmFormat	XDAS_Int32	Input	Output PCM Format Block/Interleaved. Default is interleaved.
dataEndianness	XDAS_Int32	Input	Endianness of output PCM data. See XDM_DataFormat enumeration for details. Default is XDM_LE_16.

> **Note:**
>
> ❑ The current MPEG4AAC_LC Decoder implementation supports XDM_LE_16 format for output PCM data..
>
> ❑ MPEG4 AAC-LC Decoder implementation supports both IAUDIO_BLOCK and IAUDIO_INTERLEAVED PCM formats.
>
> ❑ The current implementation of AAC-LC Decoder supports 16 and 24 output PCM width.

4.2.1.6 IAUDDEC1_DynamicParams

‖ **Description**

This structure defines the run-time parameters for an algorithm instance object. Set this data structure to NULL, if you are not sure of the values to be specified for these parameters.

‖ **Fields**

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
downSampleSb rFlag	XDAS_Int32	Input	Flag to indicate down sampling for SBR. This flag is off by default.

> **Note:**
>
> AAC-LC decoder does not support downSampleSbrFlag and will ignore this flag.

4.2.1.7 IAUDDEC1_InArgs

‖ **Description**

This structure defines the run-time input arguments for an algorithm instance object.

‖ **Fields**

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
numBytes	XDAS_Int32	Input	Number of valid input data (in bytes) in input buffer. For example, if number of valid input data in input buffer is 128 bytes, set this field to 128.
desiredChan nelMode	XDAS_Int32	Input	Desired Channel Configuration. Refer IAUDIO_ChannelMode
lfeFlag	XDAS_Int32	Input	Flag indicating whether LFE channel data is desired in the output.

Note:

❑ MPEG4AAC-LC decoder supports mono, stereo and dual mono channel modes.

For a mono stream if the desired channel mode is stereo or dual mono, decoder does 3dB attenuation on PCM output.

❑ AAC-LC Decoder does support LFE channel provided total number of channels is one or two.

4.2.1.8 IAUDDEC1_Status

‖ **Description**

This structure defines parameters that describe the status of the algorithm instance object.

‖ **Fields**

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
extendedError	XDAS_Int32	Output	Extended error enumeration for XDM compliant encoders and decoders. See XDM_ErrorBit enumeration for details.

Field	Datatype	Input/ Output	Description
data	XDM1_SingleBuf Desc	Output	Buffer descriptor for data passing
validFlag	XDAS_Int32	Output	Flag indicating the validity of the status structure
lfeFlag	XDAS_Int32	Output	Flag indicating whether LFE channel data is present or not in the input.
bitRate	XDAS_Int32	Output	Bit-rate in bits per second. For example, if the value of this field is 128000, it indicates that bit-rate is 128 kbps.
sampleRate	XDAS_Int32	Output	Sampling frequency in Hertz (Hz). For example, if the value of this field is 44100, it indicates that the sample rate is 44.1kHz.
channelMode	XDAS_Int32	Output	Output channel mode. See IAUDIO_ChannelMode for details.
pcmFormat	XDAS_Int32	Output	Output PCM Format Block/Interleaved.
numSamples	XDAS_Int32	Output	Number of samples in the output
bufInfo	XDM_AlgBufInfo	Output	Input and output buffer information. See XDM_AlgBufInfo data structure for details.
dualMonoMode	XDAS_Int32	Output	Mode to indicate type of dual mono. Only used in case of dual mono output

Note:

❑ MPEG4 AAC-LC decoder supports only IAUDIO_DUALMONO_LR type of dual mono mode.

❑ Current implementation of AAC-LC Decoder does not update data field.

4.2.1.9 IAUDDEC1_OutArgs

‖ **Description**

This structure defines the run-time output arguments for the algorithm instance object.

‖ **Fields**

Field	Datatype	Input/ Output	Description
size	XDAS_Int32	Input	Size of the basic or extended (if being used) data structure in bytes.
extendedError	XDAS_Int32	Output	Extended error enumeration for XDM compliant encoders and decoders. See XDM_ErrorBit data structure for details.
bytesConsumed	XDAS_Int32	Output	Bytes consumed during the process call
numSamples	XDAS_Int32	Output	Number of output samples per channel
channelMode	XDAS_Int32	Output	Output channel configuration
lfeFlag	XDAS_Int32	Output	Flag indicating the presence of LFE channel in the output
dualMonoMode	XDAS_Int32	Output	Mode to indicate type of dual mono. Only used in case of dual mono output
sampleRate	XDAS_Int32	Output	Sampling frequency, in Hz

Note:

MPEG4-AAC-LC decoder supports only IAUDIO_DUALMONO_LR type of dual mono mode.

4.2.2 MPEG4AAC LC Decoder Data Structures

This section includes the following MPEG4AAC LC Decoder specific extended data structures:

❑ IAACDEC_Params

❑ IAACDEC_DynamicParams

❑ IAACDEC_InArgs

❑ IAACDEC_Status

❑ IAACDEC_OutArgs

4.2.2.1 IAACDEC_Params

‖ **Description**

This structure defines the creation parameters and any other implementation specific parameters for the MPEG4AAC LC Decoder instance object. The creation parameters are defined in the XDM data structure, IAUDDEC1_Params.

‖ **Fields**

Field	Datatype	Input/ Output	Description
auddec_params	IAUDDEC1_Params	Input	See IAUDDEC1_Params data structure for details.
sixChannelMode	XDAS_Int32	Input	Multi-channel mode: ❑ 0 - off ❑ 1 - on

---

Note:

sixChannelMode is not applicable to MPEG4 AAC-LC decoder and this field is ignored.

4.2.2.2 IAACDEC_DynamicParams

‖ **Description**

This structure defines the run-time parameters and any other implementation specific parameters for the MPEG4AAC LC Decoder instance object. The run-time parameters are defined in the XDM data structure, `IAUDDEC1_DynamicParams`.

‖ **Fields**

Field	Datatype	Input/ Output	Description
auddec_dynamicparams	IAUDDEC1_DynamicParams	Input	See `IAUDDEC1_DynamicParams` data structure for details.
enablePS	XDAS_Int32	Input	Parametric stereo (PS) flag: ❑ 0 - Disables PS ❑ 1 - Enables PS Not supported for MPEG4AAC LC Decoder
ulSamplingRateIdx	XDAS_Int32	Input	Sampling rate required for raw input data format: ❑ 0 - 96.000 khz ❑ 1 - 88.200 khz ❑ 2 - 64.000 khz ❑ 3 - 48.000 khz ❑ 4 - 44.100 khz ❑ 5 - 32.000 khz ❑ 6 - 24.000 khz ❑ 7 - 22.050 khz ❑ 8 - 16.000 khz ❑ 9 - 12.000 khz ❑ 10 - 11.025 khz ❑ 11 - 8.000 khz
nProfile	XDAS_Int32	Input	Decoder profile required for raw input data format: ❑ 0 - MAIN ❑ 1 - LC ❑ 2 - SSR ❑ 3 - LTP
bRawFormat	XDAS_Int32	Input	Raw input format indicator flag: ❑ 0 – Indicates input format is not raw (that is, ADIF or ADTS formats) ❑ 1 – Indicates input format is raw

Field	Datatype	Input/ Output	Description
pseudoSurroundEna bleFlag	XDAS_Int32	Input	Pseudo surround enable flag to use Set2 type of down mixing. ❑ 0 - Disables Pseudo surround enable flag ❑ 1 - Enables Pseudo surround enable flag Ignored if EnableARIBDownmix is off.
enableARIBDownmix	XDAS_Int32	Input	ARIB downmix enable for 3/1, 3/2 and 3/2+LFE channel streams. ❑ 0 – ARIB downmix off ❑ 1 – ARIB downmix on

---

Note:

❑ For decoding ADIF and ADTS streams, set the field bRawFormat to 0 and ignore the ulSamplingRateIdx and nProfile fields.

❑ For decoding raw streams, set the field bRawFormat to 1 and the fields ulSamplingRateIdx and nProfile to one of the values given above.

❑ nProfile will be 1 as the decoder supports only LC profile

❑ enablePS, pseudoSurroundEnableFlag and enableARIBDownmix are not applicable MPEG4-AAC–LC Decoder and these fields will be ignored.

4.2.2.3   IAACDEC_InArgs

‖ **Description**

This structure defines the run-time input arguments for the MPEG4AAC LC Decoder instance object.

‖ **Fields**

Field	Datatype	Input/ Output	Description
auddec_inArgs	IAUDDEC1_InArg s	Input	See IAUDDEC1_InArgs data structure for details.

4.2.2.4 IAACDEC_Status

‖ Description

This structure defines parameters that describe the status of the MPEG4AAC LC Decoder and any other implementation specific parameters. The status parameters are defined in the XDM data structure, `IAUDDEC1_Status`.

‖ Fields

Field	Datatype	Input/ Output	Description
auddec_status	IAUDDEC1_Status	Output	See `IAUDDEC1_Status` data structure for details.
mpegID	XDAS_Int16	Output	Indicates the version of MPEG : ❑ 0 – Indicates MPEG4 ❑ 1 – Indicates MPEG2
original_copy	XDAS_Int16	Output	Indicates if the stream is original or copy ❑ 0 – Copy ❑ 1 - Original
copyright	XDAS_Int16	Output	Indicates if the stream is copyright protected or not ❑ 0 – not copyright protected ❑ 1 – copyright protected
toolsUsed	XDAS_Int16	Output	5 bit field which indicates the tools used to encode the stream Bit4 Bit3 Bit2 Bit1 Bit0 PULSE PNS TNS IS MS ❑ 0 – tool not present ❑ 1 – tool present
pcePresent	XDAS_Int16	Output	Indicates if program configuration element is present in the stream or not ❑ 0 – pce Not present ❑ 1 – pce present
sixChannelMode	XDAS_Int32	Output	Indicates if the decoder is working in 6 channel mode or stereo mode ❑ 0 – 2 channel mode ❑ 1 – 6 channel mode For MPEG4-LC decoder it is always 0
sbrPresent	XDAS_Int16	Output	Indicates if SBR is present in the bit-stream or not : ❑ 0 – Indicates SBR is not present in the bit-stream ❑ 1 – Indicates SBR is present in the bit-stream
psPresent	XDAS_Int16	Output	Indicates if PS is present in bit-stream or not : ❑ 0 – Indicates PS is not present in the bitstream ❑ 1 – Indicates PS is present in the bit-stream

Field	Datatype	Input/ Output	Description
`isARIBDownMixed`	XDAS_Int32	Output	Flag to indicate type of downmixing ❑ 1 – Indicates ARIB downmixed. ❑ 0 – Indicates ISO/IEC 13818-8 standard downmixed. `isARIBDownMixed` should be interpreted only for the streams with more than two channels.
`pseudoSurroundE nable`	XDAS_Int32	Output	Flag to indicate if `pseudo_surround_enable` field in the bit-stream is set or not. ❑ 1 – Indicates `pseudo_surround_enable` is not set. ❑ 0 – Indicates `pseudo_surround_enable` is set.

Note:

`sbrPresent`, `psPresent`, `sixChannelMode`, `pseudoSurroundEnable` and `isARIBDownMixed`, are not applicable to MPEG4 AAC-LC decoder and these fields will always be set to 0.

4.2.2.5   IAACDEC_OutArgs

‖ Description

This structure defines the run-time output arguments for the MPEG4AAC LC Decoder instance object.

‖ Fields

Field	Datatype	Input/ Output	Description
`auddec_outArgs`	IAUDDEC1_OutArg s	Output	See `IAUDDEC1_OutArgs` data structure for details.

4.3 Interface Functions

This section describes the Application Programming Interfaces (APIs) used in the MPEG4AAC LC Decoder. The APIs are logically grouped into the following categories:

❑ **Creation** – `algNumAlloc()`, `algAlloc()`

❑ **Initialization –** `algInit()`

❑ **Control** – `control()`

❑ **Data processing** – `algActivate()`, `process()`, `algDeactivate()`

❑ **Termination** – `algFree()`

You must call these APIs in the following sequence:

1) `algNumAlloc()`

2) `algAlloc()`

3) `algInit()`

4) `algActivate()`

5) `process()`

6) `algDeactivate()`

7) `algFree()`

`control()` can be called any time after calling the `algInit()` API.

`algNumAlloc()`, `algAlloc()`, `algInit()`, `algActivate()`, `algDeactivate()`, and `algFree()` are standard XDAIS APIs. This document includes only a brief description for the standard XDAIS APIs. For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

Note:

Audio codecs do not use `algActivate()` and `algDeactivate()` APIs.

4.3.1 Creation APIs

Creation APIs are used to create an instance of the component. The term creation could mean allocating system resources, typically memory.

|| **Name**

algNumAlloc() – determine the number of buffers that an algorithm requires

|| **Synopsis**

```
XDAS_Int32 algNumAlloc(Void);
```

|| **Arguments**

```
Void
```

|| **Return Value**

```
XDAS_Int32; /* number of buffers required */
```

|| **Description**

algNumAlloc() returns the number of buffers that the algAlloc() method requires. This operation allows you to allocate sufficient space to call the algAlloc() method.

algNumAlloc() may be called at any time and can be called repeatedly without any side effects. It always returns the same result. The algNumAlloc() API is optional.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

|| **See Also**

```
algAlloc()
```

‖ Name

algAlloc() – determine the attributes of all buffers that an algorithm requires

‖ Synopsis

```
XDAS_Int32 algAlloc(const IALG_Params *params, IALG_Fxns
**parentFxns, IALG_MemRec memTab[]);
```

‖ Arguments

```
IALG_Params *params; /* algorithm specific attributes */
```

```
IALG_Fxns **parentFxns;/* output parent algorithm
functions */
```

```
IALG_MemRec memTab[]; /* output array of memory records */
```

‖ Return Value

```
XDAS_Int32 /* number of buffers required */
```

‖ Description

algAlloc() returns a table of memory records that describe the size, alignment, type, and memory space of all buffers required by an algorithm. If successful, this function returns a positive non-zero value indicating the number of records initialized.

The first argument to algAlloc() is a pointer to a structure that defines the creation parameters. This pointer may be NULL; however, in this case, algAlloc() must assume default creation parameters and must not fail.

The second argument to algAlloc() is an output parameter. algAlloc() may return a pointer to its parent's IALG functions. If an algorithm does not require a parent object to be created, this pointer must be set to NULL.

The third argument is a pointer to a memory space of size nbufs * sizeof(IALG_MemRec) where, nbufs is the number of buffers returned by algNumAlloc() and IALG_MemRec is the buffer-descriptor structure defined in ialg.h.

After calling this function, memTab[] is filled up with the memory requirements of an algorithm.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

‖ See Also

```
algNumAlloc(), algFree()
```

4.3.2   Initialization API

Initialization API is used to initialize an instance of the algorithm. The initialization parameters are defined in the `Params` structure (see Data Structures section for details).

‖ Name

`algInit()` – initialize an algorithm instance

‖ Synopsis

`XDAS_Int32 algInit(IALG_Handle handle, IALG_MemRec memTab[], IALG_Handle parent, IALG_Params *params);`

‖ Arguments

`IALG_Handle handle; /* algorithm instance handle*/`

`IALG_memRec memTab[]; /* array of allocated buffers */`

`IALG_Handle parent; /* handle to the parent instance */`

`IALG_Params *params; /* algorithm initialization parameters */`

‖ Return Value

`IALG_EOK; /* status indicating success */`

`IALG_EFAIL; /* status indicating failure */`

‖ Description

`algInit()` performs all initialization necessary to complete the run-time creation of an algorithm instance object. After a successful return from `algInit(),` the instance object is ready to be used to process data.

The first argument to `algInit()` is a handle to an algorithm instance. This value is initialized to the base field of `memTab[0].`

The second argument is a table of memory records that describe the base address, size, alignment, type, and memory space of all buffers allocated for an algorithm instance. The number of initialized records is identical to the number returned by a prior call to `algAlloc().`

The third argument is a handle to the parent instance object. If there is no parent object, this parameter must be set to `NULL.`

The last argument is a pointer to a structure that defines the algorithm initialization parameters.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

‖ See Also

`algAlloc(), algMoved()`

4.3.3 Control API

Control API is used for controlling the functioning of the algorithm instance during run-time. This is done by changing the status of the controllable parameters of the algorithm during run-time. These controllable parameters are defined in the `Status` data structure (see Data Structures section for details).

‖ Name

`control()` – change run-time parameters and query the status

‖ Synopsis

```
XDAS_Int32 (*control) (IAUDDEC1_Handle handle,
IAUDDEC1_Cmd id, IAUDDEC1_DynamicParams *params,
IAUDDEC1_Status *status);
```

‖ Arguments

```
IAUDDEC1_Handle handle; /* algorithm instance handle */
```

```
IAUDDEC1_Cmd id; /* algorithm specific control commands*/
```

```
IAUDDEC1_DynamicParams *params /* algorithm run-time
parameters */
```

```
IAUDDEC1_Status *status /* algorithm instance status
parameters */
```

‖ Return Value

```
IALG_EOK; /* status indicating success */
```

```
IALG_EFAIL; /* status indicating failure */
```

‖ Description

This function changes the run-time parameters of an algorithm instance and queries the algorithm's status. `control()` must only be called after a successful call to `algInit()` and must never be called after a call to `algFree()`.

The first argument to `control()` is a handle to an algorithm instance.

The second argument is an algorithm specific control command. See `XDM_CmdId` enumeration for details.

The third and fourth arguments are pointers to the `IAUDDEC1_DynamicParams` and `IAUDDEC1_Status` data structures respectively.

Note:

If you are using extended data structures, the third and fourth arguments must be pointers to the extended `DynamicParams` and `Status` data structures respectively. Also, ensure that the `size` field is set to the size of the extended data structure. Depending on the value set for the `size` field, the algorithm uses either basic or extended parameters.

‖ Preconditions

The following conditions must be true prior to calling this function; otherwise, its operation is undefined.

❑ `control()` can only be called after a successful return from `algInit()` and `algActivate()`.

❑ If algorithm uses DMA resources, `control()` can only be called after a successful return from `DMAN3_init()`.

❑ `handle` must be a valid handle for the algorithm's instance object.

‖ Postconditions

The following conditions are true immediately after returning from this function.

❑ If the control operation is successful, the return value from this operation is equal to `IALG_EOK`; otherwise it is equal to either `IALG_EFAIL` or an algorithm specific return value.

❑ If the control command is not recognized, the return value from this operation is not equal to `IALG_EOK`.

‖ Example

See test application file, TestAppDecoder.c available in the \Client\Test\Src sub-directory.

‖ See Also

`algInit()`, `algActivate()`, `process()`

Note:

Audio codecs do not use `algActivate()`, `algDeactivate()`, and `DMAN3_init()` APIs.

4.3.4   Data Processing API

Data processing API is used for processing the input data.

|| **Name**

        `process()` – basic encoding/decoding call

|| **Synopsis**

        ```
XDAS_Int32 (*process)(IAUDDEC1_Handle handle, XDM1_BufDesc
*inBufs, XDM1_BufDesc *outBufs, IAUDDEC1_InArgs *inargs,
IAUDDEC1_OutArgs *outargs);
```

|| **Arguments**

        ```
IAUDDEC1_Handle handle; /* algorithm instance handle */
```

        ```
XDM1_BufDesc *inBufs; /* algorithm input buffer descriptor
*/
```

        ```
XDM1_BufDesc *outBufs; /* algorithm output buffer
descriptor */
```

        ```
IAUDDEC1_InArgs *inargs /* algorithm runtime input
arguments */
```

        ```
IAUDDEC1_OutArgs *outargs /* algorithm runtime output
arguments */
```

|| **Return Value**

        ```
IALG_EOK; /* status indicating success */
```

        ```
IALG_EFAIL; /* status indicating failure */
```

|| **Description**

This function does the basic encoding/decoding. The first argument to `process()` is a handle to an algorithm instance.

The second and third arguments are pointers to the input and output buffer descriptor data structures respectively (see `XDM_BufDesc` data structure for details).

The fourth argument is a pointer to the `IAUDDEC1_InArgs` data structure that defines the run-time input arguments for an algorithm instance object.

The last argument is a pointer to the `IAUDDEC1_OutArgs` data structure that defines the run-time output arguments for an algorithm instance object.

Note:

If you are using extended data structures, the fourth and fifth arguments must be pointers to the extended `InArgs` and `OutArgs` data structures respectively. Also, ensure that the `size` field is set to the size of the extended data structure. Depending on the value set for the `size` field, the algorithm uses either basic or extended parameters.

|| **Preconditions**

The following conditions must be true prior to calling this function; otherwise, its operation is undefined.

❑ `process()` can only be called after a successful return from `algInit()` and `algActivate()`.

❑ If algorithm uses DMA resources, `process()` can only be called after a successful return from `DMAN3_init()`.

❑ `handle` must be a valid handle for the algorithm's instance object.

❑ Buffer descriptor for input and output buffers must be valid.

❑ Input buffers must have valid input data.

‖ Postconditions

The following conditions are true immediately after returning from this function.

❑ If the process operation is successful, the return value from this operation is equal to `IALG_EOK`; otherwise it is equal to either `IALG_EFAIL` or an algorithm specific return value.

❑ After successful return from `process()` function, `algDeactivate()` can be called.

‖ Example

See test application file, TestAppDecoder.c available in the \Client\Test\Src sub-directory.

‖ See Also

`algInit(), algDeactivate(), control()`

Note:

❑ Audio codecs do not use `algActivate(), algDeactivate()`, and `DMAN3_init()` APIs.

❑ The input data for MPEG4AAC LC Decoder is in byte format. The decoder outputs 16-bit or 24-bit (not supported) raw PCM samples in the little-endian format. The output data is either in block or interleaved format. In the block format, samples of the left channels are stored contiguously first, followed by right channel samples (that is, LLLLRRRR). In the interleaved format, left channel samples are stored followed by the right channel samples (that is, LRLRLRLR).

4.3.5 Termination API

Termination API is used to terminate the MPEG4AAC LC Decoder and free up the memory space that it uses.

‖ Name

algFree() – determine the addresses of all memory buffers used by the algorithm

‖ Synopsis

```
XDAS_Int32 algFree(IALG_Handle handle, IALG_MemRec
memTab[]);
```

‖ Arguments

```
IALG_Handle handle; /* handle to the algorithm instance */
```

```
IALG_MemRec memTab[]; /* output array of memory records */
```

‖ Return Value

```
XDAS_Int32; /* Number of buffers used by the algorithm */
```

‖ Description

algFree() determines the addresses of all memory buffers used by the algorithm. The primary aim of doing so is to free up these memory regions after closing an instance of the algorithm.

The first argument to algFree() is a handle to the algorithm instance.

The second argument is a table of memory records that describe the base address, size, alignment, type, and memory space of all buffers previously allocated for the algorithm instance.

For more details, see *TMS320 DSP Algorithm Standard API Reference* (literature number SPRU360).

‖ See Also

algAlloc()

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Frequently Asked Questions

This chapter provides answers to few frequently asked questions related to using this decoder.

Table 5-1. FAQs for MPEG4 AAC-LC Decoder on C64x+ /C674x

Questions	Answers
Can this decode HE/PS streams?	The MPEG4-AAC LC decoder can decode HE/PS streams but decodes only LC part.
Can this decoder support streams with more than 2 channels?	Decoder does not support the streams with more than two channels.
Does this decoder support RAW streams?	Decoder needs to be informed that the stream is RAW by setting `dynamicParams.bRawFormat` to 1 and `dynamicParams.uSamplingRateIdx` to proper index. And calling setParams API.
Does this decoder support 24 bit PCM output?	Yes, Decoder supports both 16 and 24 -bit PCM output.
Will the decoder recover from error for RAW and ADIF streams?	Decoder will not recover from error in case of ADIF and RAW streams. Decoder can recover only in case of ADTS streams.
Does this decoder supports ELF and COFF libraries?	Yes this decoder supports both COFF and ELF libraries with separate library and application projects for COFF and ELF.

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Optimal Function Placement Order for Minimizing Instruction Cache Penalty

This appendix contains the optimal function placement order for minimizing cache penalty due to instruction cache misses. The program memory and MCPS performance numbers quoted in the datasheet are based on this placement. The user's application linker command file should contain the following:

```
GROUP : {
/* API functions */
.text:algAlloc:_AACDEC_TII_algAlloc
.text:algNumAlloc:_AACDEC_TII_algNumAlloc
.text:algInit:_AACDEC_TII_algInit
.text:algFree:_AACDEC_TII_algFree

.text:_AACDEC_TII_control
.text:_AACDEC_TII_getStatus
.text:_AACDEC_TII_reset
.text:_AACDEC_TII_decode

/* Decoder functions */

.text:_AACDEC_TII_Decoder
.text:_AACDEC_TII_SyncCheck
.text:_AACDEC_TII_SuperSyncSeek
.text:_AACDEC_TII_SyncSeek
.text:_AACDEC_TII_GetAdifHeader
.text:_AACDEC_TII_DecodeBlock
.text:_AACDEC_TII_Set_Pointers
.text:_AACDEC_TII_ParseBlock
.text:_AACDEC_TII_DQCoef
.text:_AACDEC_TII_ParseBlock_Adts_Header
.text:_AACDEC_TII_Parse_Element
.text:_AACDEC_TII_GetAdtsHeader
.text:_AACDEC_TII_GetBits
.text:_AACDEC_TII_ParseChannelElement
.text:_AACDEC_TII_EnterChannel
.text:_AACDEC_TII_GetIcsInfo
.text:_AACDEC_TII_Infoinit
.text:_AACDEC_TII_GetGroup
.text:_AACDEC_TII_CalcGsfbTable
.text:_AACDEC_TII_GetMask
.text:_AACDEC_TII_GetIcs
.text:_AACDEC_TII_Huffcb
.text:_AACDEC_TII_Hufffac
.text:_AACDEC_TII_GetPrefix
.text:_AACDEC_TII_HuffDecodeLoop0
.text:_AACDEC_TII_HuffDecodeLoop1
.text:_AACDEC_TII_HuffDecodeLoop2
.text:_AACDEC_TII_HuffDecodeLoop3
```

```
.text:_AACDEC_TII_HuffDecodeLoop4
.text:_AACDEC_TII_HuffDecodeLoop5
.text:_AACDEC_TII_CB_11_Handler
.text:_AACDEC_TII_get_pulse_nc
.text:_AACDEC_TII_pulse_nc
.text:_AACDEC_TII_GetTns
.text:_AACDEC_TII_Huffspec
.text:_AACDEC_TII_GetProgConfig
.text:_AACDEC_TII_GetFill
.text:_AACDEC_TII_GetData
.text:_AACDEC_TII_BitstreamSetBookmark
.text:_AACDEC_TII_BitstreamDistanceFromBookmark
.text:_AACDEC_TII_BitstreamGotoBookmark
.text:_AACDEC_TII_AccumulateCRCFromStream
.text:_AACDEC_TII_AccumulateCRCWord
.text:_AACDEC_TII_IntensityStereo
.text:_AACDEC_TII_MapMask
.text:_AACDEC_TII_Intensity
.text:_AACDEC_TII_InverseQuantizer
.text:_AACDEC_TII_InverseQuantize
.text:_AACDEC_TII_Rescale
.text:_AACDEC_TII_MS_Stereo
.text:_AACDEC_TII_MSTool
.text:_AACDEC_TII_ScalefactorToQpt
.text:_AACDEC_TII_FindMaxQptInCh
.text:_AACDEC_TII_AdjustQptInCh
.text:_AACDEC_TII_deinterleave
.text:_AACDEC_TII_ApplyPNS
.text:_AACDEC_TII_GenRandVector
.text:_AACDEC_TII_Random
.text:_AACDEC_TII_TnsDecodeSubblock2
.text:_AACDEC_TII_TnsDecodeCoef
.text:_AACDEC_TII_TnsPostscale
.text:_AACDEC_TII_DetermineQpt
.text:_AACDEC_TII_TnsArFilter
.text:_AACDEC_TII_IFilterBank
.text:_AACDEC_TII_Post_Processing
.text:_AACDEC_TII_IMDCTWrapper
.text:_AACDEC_TII_PreTwiddle
.text:_AACDEC_TII_BitReverse
.text:_AACDEC_TII_IFFT
.text:_AACDEC_TII_IFFTStage1and2
.text:_AACDEC_TII_IFFTStage3
.text:_AACDEC_TII_IFFTStagex
.text:_AACDEC_TII_PostTwiddle
.text:_AACDEC_TII_StateBookkeeping
.text:_AACDEC_TII_WinOvaddLong
.text:_AACDEC_TII_ShortWindowing
.text:_AACDEC_TII_WinOvaddShort
.text:_AACDEC_TII_WinLFT
.text:_AACDEC_TII_WinLFF
.text:_AACDEC_TII_WinEFT
.text:_AACDEC_TII_WinEFF
.text:_AACDEC_TII_WinMFT
.text:_AACDEC_TII_WinMFF
.text:_AACDEC_TII_Rounding
.text:_AACDEC_TII_Rounding_16bit
.text:_AACDEC_TII_Rounding_24bit
.text:_AACDEC_TII_MonoToStereo
.text:_AACDEC_TII_StereoToMono
.text:_AACDEC_TII_DecoderErr
.text:_AACDEC_TII_init
.text:_AACDEC_TII_Restarttio
```

```
.text:_AACDEC_TII_exit

        } > ROM_MEMORY
```