

MIPS16 Instructions

R-Type

- ADD add \$d, \$s, \$t 000_rs_rt_rd_0_000 $\$d \leftarrow \$s + \$t$
- SUB sub \$d, \$s, \$t 000_rs_rt_rd_0_001 $\$d \leftarrow \$s - \$t$
- SLL sll \$d \$t, h 000_rs_rt_rd_h_010 $\$d \leftarrow \$t \ll h$
- SRL srl \$d \$t, h 000_rs_rt_rd_h_011 $\$d \leftarrow \$t \gg h$
- AND and \$d, \$s, \$t 000_rs_rt_rd_0_100 $\$d \leftarrow \$s \& \$t$
- OR or \$d, \$s, \$t 000_rs_rt_rd_0_101 $\$d \leftarrow \$s | \$t$
- XOR xor \$d, \$s, \$t 000_rs_rt_rd_0_110 $\$d \leftarrow \$s \wedge \$t$
- NOR nor \$d, \$s, \$t 000_rs_rt_rd_0_111 $\$d \leftarrow \sim(\$s | \$t)$

I-Type

- ADDI addi \$t, \$s, imm 001_rs_rt_i_i_i_i_i_i $\$d \leftarrow \$s + \text{imm}$
- LW lw \$t, offset(\$s) 010_rs_rt_t_t_t_t_t_t $\$t \leftarrow \text{MEM}[\$s + \text{offset}]$
- SW sw \$t, offset(\$s) 011_rs_rt_t_t_t_t_t_t $\text{MEM}[\$s + \text{offset}] \leftarrow \t
- BEQ beq \$s, \$t, offset(\$s) 100_rs_rt_t_t_t_t_t_t if $\$s == \t advance_pc (offset)
else advance_pc (1)
- ANDI andi \$t, \$s, imm 101_rs_rt_i_i_i_i_i_i $\$d \leftarrow \$s \& \text{imm}$
- ORI ori \$t, \$s, imm 110_rs_rt_i_i_i_i_i_i $\$d \leftarrow \$s | \text{imm}$

J-Type

- J j target 111_j_j_j_j_j_j_j_j_j_j $\text{PC} \leftarrow \text{nPC}; \text{nPC} = (\text{PC} \& 0\text{xf000}) | (\text{target})$