

LCPI-T113 Specification

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1. Overview

The LCPI-T113 is powered by AllWinner's T113 chip, an intelligent control and display processor for automotive and industrial products. Dual-core ARM CortexTM-A7 CPU

HiFi4 DSP

Memories

- Embedded with 128 MB DDR3, clock frequency up to 800 MHz
- Three SD/MMC host controller (SMHC) interfaces: SD3.0/SDIO3.0/eMMC5.0

Video Engine

- $\rm H.265/H.264/MPEG-1/MPEG-2/MPEG-4/JPEG/VC1/Xvid/Sorenson$ Spark decoding, up to 1080p@60fps
 - JPEG/MJPEG encoding, up to 1080p@60fps

Video and Graphics

- Allwinner SmartColor2.0 post processing for an excellent display experience
- Supports de-interlacer (DI) up to 1080p@60fps
- Supports Graphic 2D (G2D) hardware accelerator including rotate, mixer, LBC decompression functions

Video Output

- RGB interface up to 1920 x 1080@60fps
- Dual link LVDS interface up to 1920 x 1080@60fps
- 4-lane MIPI DSI up to 1920 x 1200@60fps
- CVBS OUT interface, supporting NTSC and PAL format

Video Input

- 8-bit digital camera interface
- CVBS IN interface, supporting NTSC and PAL format

Analog Audio Codec

- 2 DACs and 3 ADCs
- Analog audio interfaces: HPOUTL/R, MICIN3P/N, LINEINL/R, FMINL/R

Two I2S/PCM external interfaces (I2S1, I2S2)

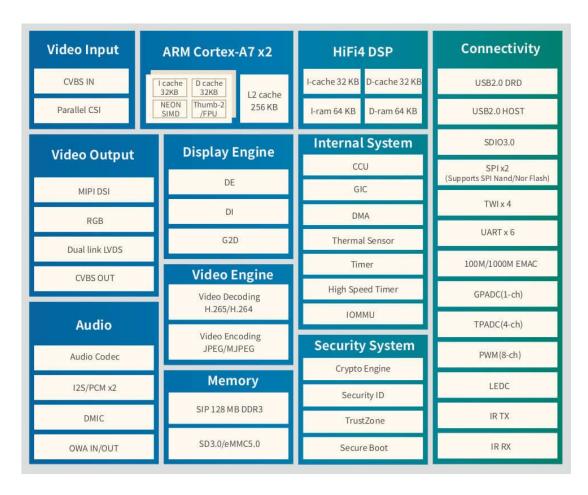
Maximum 8 digital PDM microphones (DMIC)



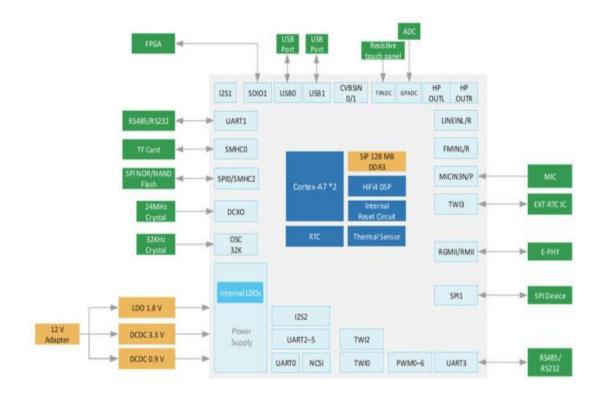
2. Introduced the hardware and instructions

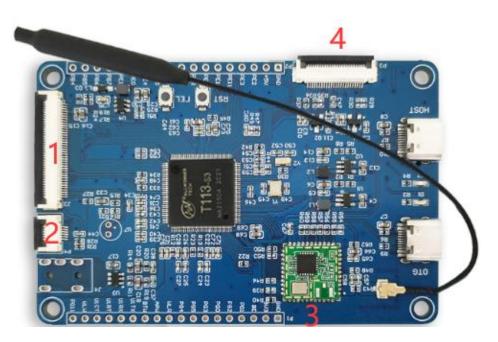
Board size: 85*55mm

Weight: 20g









- 1, 40pin 2.54mm 800*480 LCD interface
- 2, 6pin 2.54mm touch screen interface
- 3, RTL8189 WIFI module
- 4, 24pin 2.54mm camera interface