

1. Description

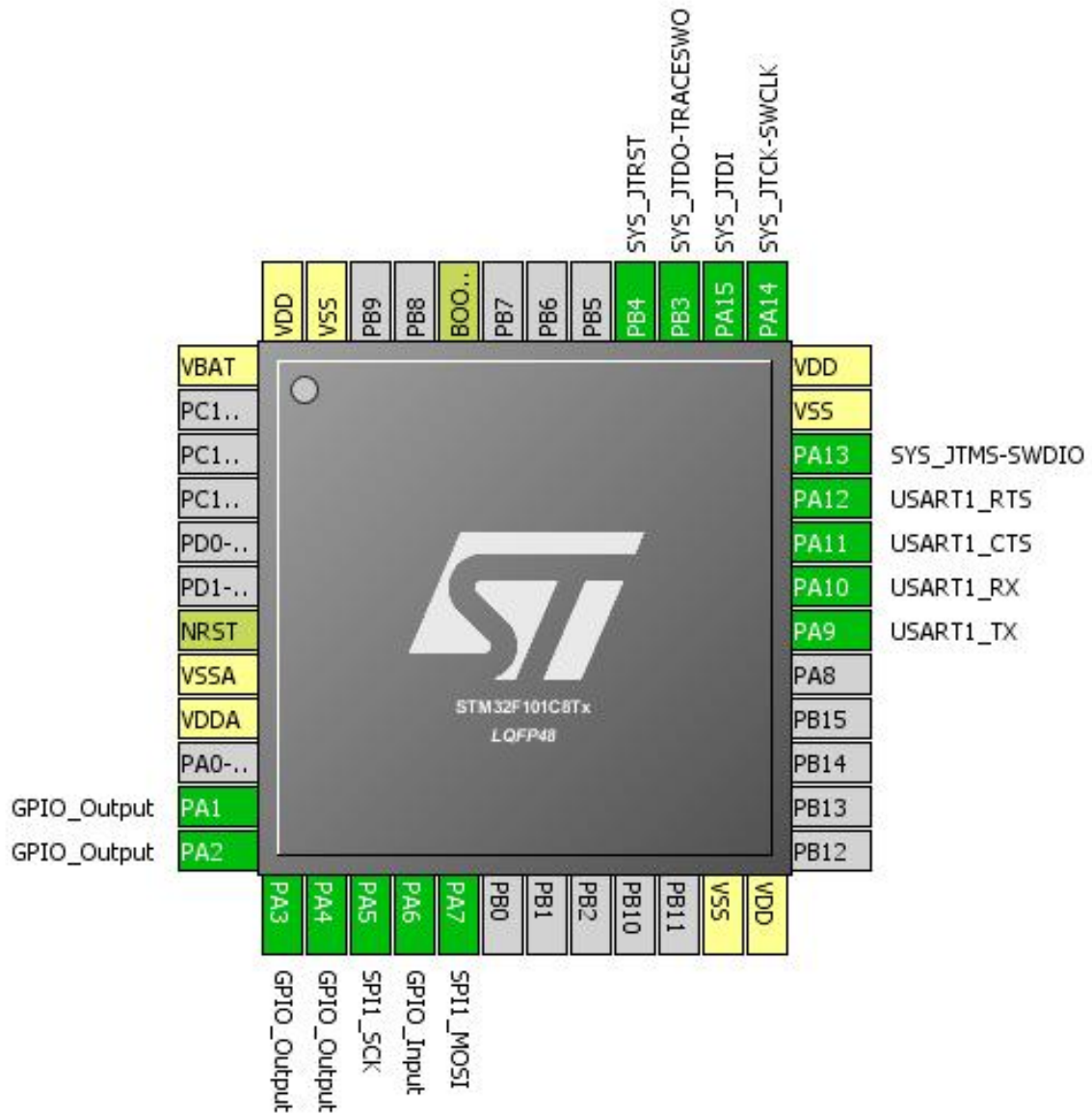
1.1. Project

Project Name	stm32f101c8t6
Generated with:	STM32CubeMX 4.3.0
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1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F101
MCU name	STM32F101C8Tx
MCU Package	LQFP48
MCU Pin number	48

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
SPI1	Mode: TX-Only-Simplex-Unidirect-Master	SPI1_MOSI	PA7
		SPI1_SCK	PA5
SYS	Debug: JTAG(5-pin)	SYS_JTCK-SWCLK	PA14
		SYS_JTDI	PA15
		SYS_JTDO-TRACESWO	PB3
		SYS_JTMS-SWDIO	PA13
		SYS_JTRST	PB4
USART1	Mode: Hardware-flow-control	USART1_CTS	PA11
		USART1_RTS	PA12
		USART1_RX	PA10
		USART1_TX	PA9

4. Pins Configuration

Pin	Pos	Function(s)	Label
PA1 *	11	GPIO_Output	
PA2 *	12	GPIO_Output	
PA3 *	13	GPIO_Output	
PA4 *	14	GPIO_Output	
PA5	15	SPI1_SCK	
PA6 *	16	GPIO_Input	
PA7	17	SPI1_MOSI	
PA9	30	USART1_TX	
PA10	31	USART1_RX	
PA11	32	USART1_CTS	
PA12	33	USART1_RTS	
PA13	34	SYS_JTMS-SWDIO	
PA14	37	SYS_JTCK-SWCLK	
PA15	38	SYS_JTDI	
PB3	39	SYS_JTDO-TRACESWO	
PB4	40	SYS_JTRST	

* The pin is affected with an I/O function