

1. Description

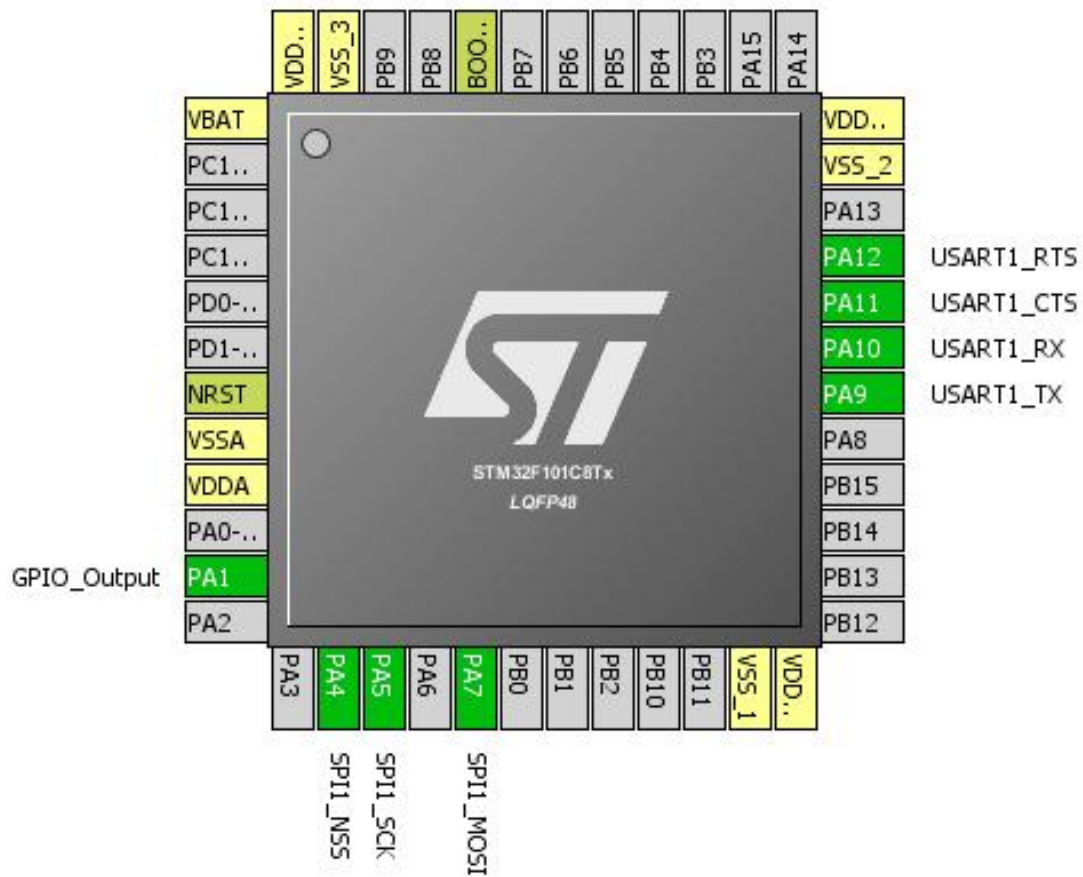
1.1. Project

| | |
|-----------------|-------------------|
| Project Name | stm32f101c8t6 |
| Generated with: | STM32CubeMX 4.2.0 |
| Date | 06/13/2014 |

1.2. MCU

| | |
|----------------|---------------|
| MCU Serie | STM32F1 |
| MCU Line | STM32F101 |
| MCU name | STM32F101C8Tx |
| MCU Package | LQFP48 |
| MCU Pin number | 48 |

2. Pinout Configuration



3. IPs and Middlewares Configuration

| IP | Mode | Fonction | Pin |
|--------|--|------------|------|
| SPI1 | Mode: TX-Only-Simplex-Unidirect-Master_With-NSS | SPI1_MOSI | PA7 |
| | | SPI1_NSS | PA4 |
| | | SPI1_SCK | PA5 |
| USART1 | Mode: Hardware-flow-control | USART1_CTS | PA11 |
| | | USART1_RTS | PA12 |
| | | USART1_RX | PA10 |
| | | USART1_TX | PA9 |

4. Pins Configuration

| Pin | Pos | Function(s) | Label |
|-------|-----|-------------|-------|
| PA1 * | 11 | GPIO_Output | |
| PA4 | 14 | SPI1_NSS | |
| PA5 | 15 | SPI1_SCK | |
| PA7 | 17 | SPI1_MOSI | |
| PA9 | 30 | USART1_TX | |
| PA10 | 31 | USART1_RX | |
| PA11 | 32 | USART1_CTS | |
| PA12 | 33 | USART1_RTS | |

* The pin is affected with an I/O function