

PCB:  
FABRICATE TO IPC 6012 CLASS 2  
VENDOR MARKINGS ON BOTTOM SIDE ONLY!  
VENDOR MARKINGS IN SILKSCREEN ONLY!

FIDUCIAL MARKS:  
FID1, FID2, FID3

DESIGNED BY:  
ENG. ANDRÉ A. M. ARAÚJO

<p>         FID1, FID2, FID3          DESIGNED BY:          ENG. ANDRÉ A. M. ARAÚJO       </p>	<p>A</p>
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Copper Layer Count:	4	Board Thickness:	1.5888 mm
Board overall dimensions:	100.0000 mm x 82.0000 mm		
Min track/spacing:	0.1270 mm / 0.1270 mm	Min hole diameter:	0.3000 mm
Copper Finish:	HAL SnPb	Impedance Control:	Yes
Castellated pads:	No	Plated Board Edge:	No
Edge card connectors:	No		

D	F.Paste	Top Solder Paste	0 mm		1	0	<div>Revision Log: Rev: 00 – Initial Release Rev: 01 – Improved images on silkscreen Rev: 02 – Corrected minor issues on D2 and U6 symbols/footprints</div> <div>Eng. André A. M. Araújo <b>Mechatronics Lab</b></div> <div>Sheet: File: EMBARCADOS – CURSO PCB.kicad_pcb</div> <div><b>Title: EMBARCADOS – CURSO PCB</b></div> <div>Size: A4Date: 2025–02–09</div> <div>KiCad E.D.A. 8.0.6</div> <div>Rev: 02</div> <div>Id: 1/1</div>	
	F.Mask	Top Solder Mask	Not specified	0.01 mm	Green	3.3		0
	L1 – TOP (SIG–PWR)	copper		0.035 mm		1		0
	Dielectric 1	prepreg	FR4	0.0994 mm	FR4 natural	4.1		0
	L2 – GND	copper		0.0175 mm		1		0
	Dielectric 2	core	FR4	1.265 mm	FR4 natural	4.6		0
	L3 – GND	copper		0.0175 mm		1		0
	Dielectric 3	prepreg	FR4	0.0994 mm	FR4 natural	4.1		0
	L4 – BOTTOM (SIG–PWR)	copper		0.035 mm		1		0
	B.Mask	Bottom Solder Mask	Not specified	0.01 mm	Green	3.3		0
B.Paste	Bottom Solder Paste		0 mm		1	0		
B.Silkscreen	Bottom Silk Screen	Not specified	0 mm	White	1	0		

Eng. André A. M. Araújo	
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