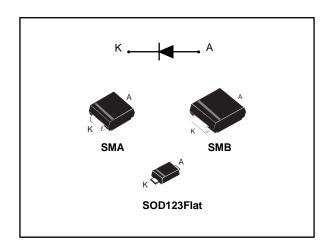
STPS1L40-Y



Automotive low drop power Schottky rectifier

Datasheet - production data



Features



- AEC-Q101 qualified
- Very small conduction losses
- Negligible switching losses
- Low forward voltage drop
- Avalanche capability specified

Surface mount miniature packages

PPAP capable

Description

Single chip Schottky rectifiers suited to switched mode power supplies and high frequency DC to DC converters.

Packaged in SOD123Flat, SMA and SMB, this device is especially intended for surface mounting and used in low voltage, high frequency inverters, free-wheeling and polarity protection in automotive applications.

Table 1: Device summary

Symbol	Value
I _{F(AV)}	1 A
V _{RRM}	40 V
V _F (typ.)	0.37 V
T _j (max.)	175 °C

Characteristics STPS1L40-Y

1 Characteristics

Table 2: Absolute ratings (limiting values at 25 °C, unless otherwise specified)

Symbol	Parameter	Value	Unit	
V _{RRM}	Repetitive peak reverse voltage	$T_j = -40 ^{\circ}\text{C} \text{ to } +175 ^{\circ}\text{C}$	40	V
1	Average forward current	SMA/SMB: T _L = 155 °C	4	
IF(AV)	δ = 0.5, square wave	SOD123Flat: T _L = 160 °C	1 1	Α
	Surge non repetitive forward current, $t_p = 10$ ms sinusoidal	SMA/SMB	60	
IFSM		SOD123Flat	50	Α
Parm	Repetitive peak avalanche power $t_p = 10 \mu s$, $T_j = 125 \degree C$		65	W
T _{stg}	Storage temperature range	-65 to +175	°C	
Tj	Operating junction temperature range ⁽¹⁾	-40 to +175		

Notes:

Table 3: Thermal parameters

Symbol	Parameter		Max. value	Unit
		SMA	30	
R _{th(j-l)}	R _{th(j-l)} Junction to lead	SMB	25	°C/W
		SOD123Flat	20	

Table 4: Static electrical characteristics

	Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
Ī	ı (1)	. (1)	T _j = 25 °C	., .,	-		35	μΑ
	I _R ⁽¹⁾ Reverse leakage current	T _j = 125 °C	$V_R = V_{RRM}$	-	6	10	mΑ	
Ī	V _F ⁽²⁾ Forward voltage drop	T _j = 25 °C	Ι 4 Λ	-		0.50	\ /	
		Forward voltage drop	T _j = 125 °C	I _F = 1 A	-	0.37	0.42	V

Notes:

 $^{(1)}\text{Pulse}$ test: t_p = 5 ms, δ < 2%

(2) Pulse test: t_p = 380 μs, δ < 2%

To evaluate the conduction losses, use the following equation:

 $P = 0.23 \text{ x } I_{F(AV)} + 0.19 \text{ x } I_{F^2(RMS)}$

For more information, please refer to the following application notes related to the power losses.

- AN604 (Calculation of conduction losses in a power rectifier)
- AN4021 (Calculation of reverse losses in a power diode)

 $^{^{(1)}(}dP_{tot}/dT_j) < (1/R_{th(j-a)}) \ condition \ to \ avoid \ thermal \ runaway \ for \ a \ diode \ on \ its \ own \ heatsink.$

STPS1L40-Y Characteristics

Characteristics (curves) 1.1

Figure 1: Average forward power dissipation versus average forward current

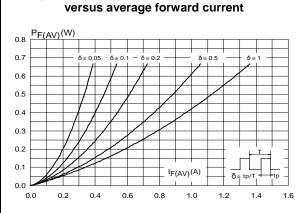
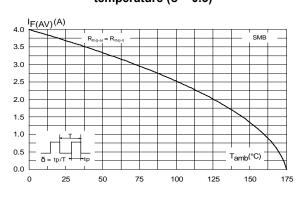


Figure 2: Average forward current versus ambient temperature ($\delta = 0.5$) 4.0 3.5 2.5 2.0 1.0 0.5 $\delta = tp/T$ 0 25 50 75 100 125 150 175

Figure 3: Average forward current versus ambient temperature ($\delta = 0.5$)



temperature ($\delta = 0.5$) $I_{F(AV)}(A)$ SOD123 flat

 $\delta = tp/T$

25

50

0 0

0.4

0.3 0.2 0.1

0.0

1.E-02

1.E-01

Figure 4: Average forward current versus ambient

Figure 5: Normalized avalanche power derating versus pulse duration $(T_i = 125 °C)$

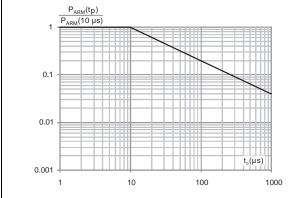


Figure 6: Relative variation of thermal impedance junction to ambient versus pulse duration $Z_{th(j-a)}/R_{th(j-a)}$ 1.0 SMA 0.9 0.8 0.7 0.6 0.5

1.E+00

1.E+01

75

100

1.E+02

1.E+03

 $T_{amb}(^{\circ}C)$

175

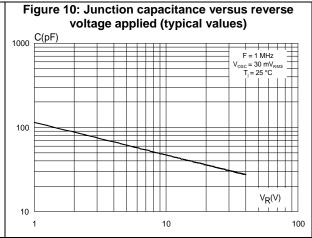
125

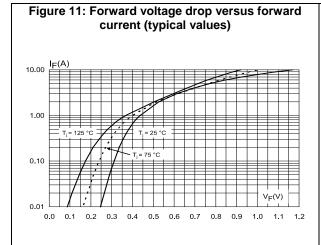
Characteristics STPS1L40-Y

Figure 7: Relative variation of thermal impedance junction to ambient versus pulse duration Z_{th(j-a)}/R_{th(j-a)} 1.0 SMB 0.9 0.8 0.6 0.5 0.4 0.3 0.2 0.1 0.0 1.E-02 1 F-01 1 F+00 1.E+01 1.E+02 1 F+03

Figure 8: Relative variation of thermal impedance junction to lead versus pulse duration $Z_{th(j-l)}/R_{th(j-l)}$ 1.0 1 11111 SOD123Flat 0.9 0.8 0.7 0.6 0.5 0.4 0.3 Single pulse 0.1 tp(s) للسب 0.0 1.E-04 1.E+01

Figure 9: Reverse leakage current versus reverse voltage applied (typical values) $I_R(mA)$ 1.E+02 T_i = 150 °C 1.E+01 T; = 125 °C T_i = 100 °C 1.E+00 T_j = 75 °C 1.E-01 T, = 50 °C 1.E-02 T_i = 25 °C $V_{R}(V) =$ 1.E-03 20 25 0 5 10 15 30 40 35





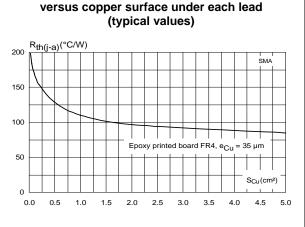


Figure 12: Thermal resistance junction to ambient

STPS1L40-Y Characteristics

S_{Cu} (cm²)

4.5

3.5

Figure 13: Thermal resistance junction to ambient versus copper surface under each lead (typical values)

2.0 2.5 3.0

100

50

1.0

versus copper surface under each lead (typical values)

Rth(j-a)(C/W)

250

Rth(j-a)(C/W)

150

Epoxy printed board FR4, e_{Cu} = 35 µm

50

0.0 0.5 1.0 1.5 2.0 2.5 3.0 3.5 4.0 4.5 5.0

Figure 14: Thermal resistance junction to ambient

Package information STPS1L40-Y

2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: **www.st.com**. ECOPACK® is an ST trademark.

Epoxy meets UL94, V0

Cooling method: by conduction (C)

2.1 SMA package information

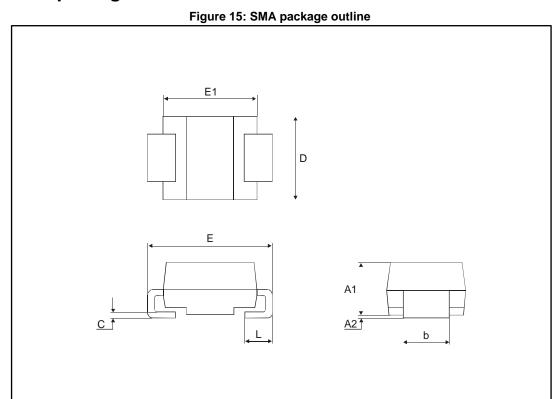


Table 5: SMA package mechanical data

	Dimensions			
Ref.	Millir	Millimeters		hes
	Min.	Max.	Min.	Max.
A1	1.90	2.45	0.075	0.097
A2	0.05	0.20	0.002	0.008
b	1.25	1.65	0.049	0.065
С	0.15	0.40	0.006	0.016
D	2.25	2.90	0.089	0.114
E	4.80	5.35	0.189	0.211
E1	3.95	4.60	0.156	0.181
L	0.75	1.50	0.030	0.059

STPS1L40-Y Package information

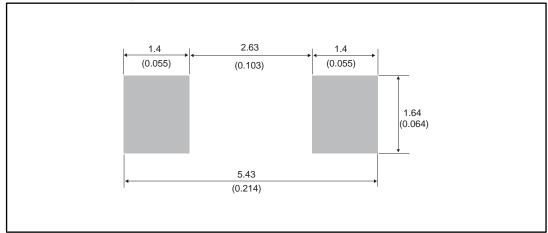


Figure 16: SMA recommended footprint in mm (inches)

Package information STPS1L40-Y

2.2 SMB package information

Figure 17: SMB package outline

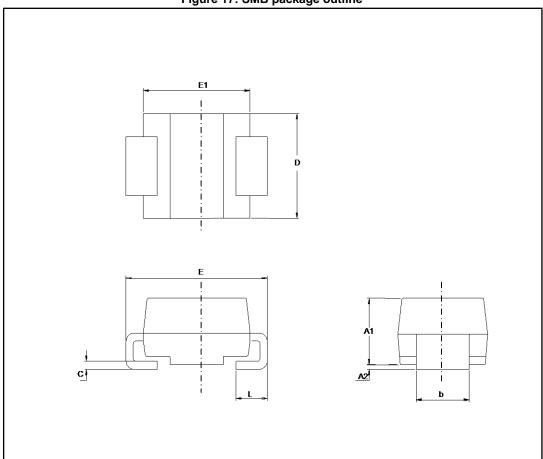
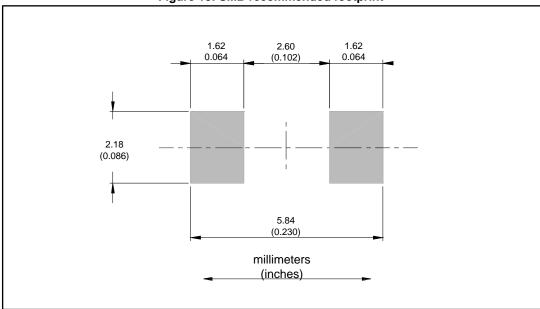


Table 6: SMB package mechanical data

	Dimensions			
Ref.	Millimeters		Inc	hes
	Min.	Max.	Min.	Max.
A1	1.90	2.45	0.0748	0.0965
A2	0.05	0.20	0.0020	0.0079
b	1.95	2.20	0.0768	0.0867
С	0.15	0.40	0.0059	0.0157
D	3.30	3.95	0.1299	0.1556
Е	5.10	5.60	0.2008	0.2205
E1	4.05	4.60	0.1594	0.1811
L	0.75	1.50	0.0295	0.0591

Figure 18: SMB recommended footprint



Package information STPS1L40-Y

2.3 SOD123Flat package information

Figure 19: SOD123Flat package outline

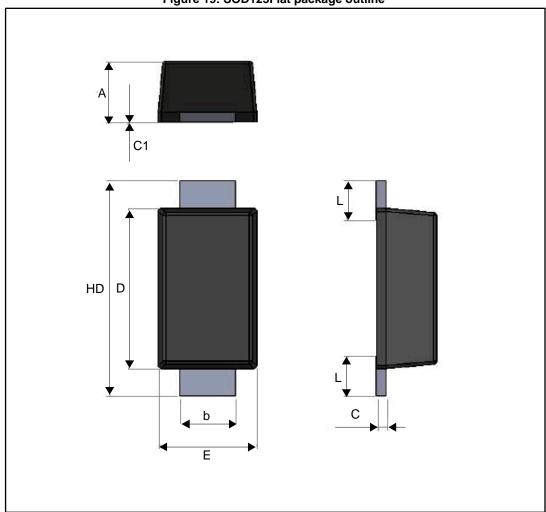
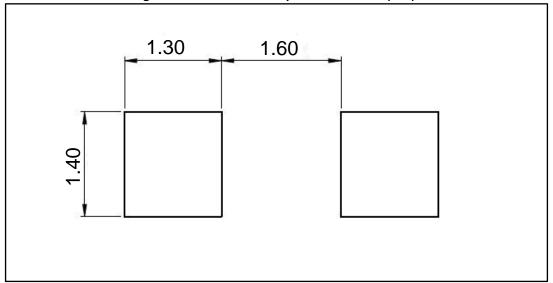


Table 7: SOD123Flat package mechanical data

		Dimensions			
Ref.	Millimeters				
	Min.	Тур.	Max.		
А	0.86	0.98	1.10		
b	0.80	0.90	1.00		
С	0.08	0.15	0.25		
c1	0.00		0.10		
D	2.50	2.60	2.70		
Е	1.50	1.60	1.80		
HD	3.30	3.50	3.70		
L	0.45	0.65	0.85		

Figure 20: SOD123Flat footprint dimensions (mm)



Ordering information STPS1L40-Y

3 Ordering information

Table 8: Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
STPS1L40AY	GB4Y	SMA	68 mg	5000	Tape and reel
STPS1L40UY	GC4Y	SMB	107 mg	2500	Tape and reel
STPS1L40ZFY	1Y4	SOD123Flat	12.5 mg	3000	Tape and reel

4 Revision history

Table 9: Document revision history

Date	Revision	Changes
21-Oct-2011	1	First issue.
01-Oct-2016	2	Added SOD123Flat package.

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