```
1
     library IEEE;
     use IEEE.STD LOGIC 1164.ALL;
 2
 3
     use IEEE.STD LOGIC ARITH.ALL;
     use IEEE.STD LOGIC UNSIGNED.all;
 4
 5
     entity lcd moduleC is
 6
 7
         Port ( clk : in STD LOGIC;
 8
                reset : in STD LOGIC;
 9
                 -- Señales para escribir
                DECENASMIL, UNIDADESMIL, CENTENASUN, DECENASUN, UNIDADESUN, DECENASDEC,
10
     UNIDADESDEC: in std logic vector ( 3 downto 0);
11
                db : out STD LOGIC VECTOR (7 downto 0);
12
                rs : out STD LOGIC;
13
                 rw : out STD LOGIC;
                 ena : out STD LOGIC);
14
15
     end lcd moduleC;
16
17
     architecture Behavioral of lcd moduleC is
     type state is (se1,se2,se3,se4,se5,si1,si2,si3,slini2,slini3,slini4,slini5,sllcur,
     s1write, s2write, sfin, s3write, s4write, s5write, s6write, s7write, s8write);
     signal mq : state;
19
20
21
     type nom is (ini1,ini2,ini3,ini4,loccur,writx,writx1,writx2,writx3,writx4,writx5,
     writx6, writx7);
22
     signal var: nom;
23
24
     signal tc : std logic vector ( 20 downto 0);
25
26
     signal dbi: std logic vector ( 7 downto 0);
27
     -- num es la columna que queremos en este caso numero de 0 a 9 (3)
28
29
     signal num : std logic vector ( 3 downto 0);
30
31
     begin
32
33
     rw <='0';
34
35
     process(clk, reset)
36
        begin
37
            if reset ='1' then
38
              mq <= si1;
39
              rs <= '0';
40
              ena <= '0';
41
              tc <= ( others => '0');
42
              var <= ini1;</pre>
              db <= "00000000";
43
              dbi <= "00100000";
44
              num <= "0011";
45
            elsif clk'event and clk ='1' then
46
47
48
              case mq is
49
     --- rutina de env?o de datos
50
                  when se1 =>
                       ena <= '1';
51
52
                       mq \le se2;
53
54
                  when se2 \Rightarrow
```

```
55
                         if tc < 101 then
 56
                             tc <= tc + 1;
 57
                             mq \le se2;
 58
 59
                              tc <= ( others =>'0');
 60
                             mq \le se3;
 61
                       end if;
 62
 63
                    when se3 =>
 64
                          ena <= '0';
                          mq <= se4;
 65
 66
 67
                    when se4 =>
 68
                          if tc < 2501 then
 69
                              tc <= tc + 1;
 70
                             mq \le se4;
 71
 72
                             tc <= ( others =>'0');
 73
                             mq \le se5;
 74
                          end if;
 75
 76
                    when se5 \Rightarrow
 77
                          case var is
 78
                            when ini1 => mq <= slini2;</pre>
 79
                            when ini2 => mq <= slini3;</pre>
 80
                            when ini3 => mg <= s1ini4;
 81
                            when ini4 => mq <= s1ini5;
82
                            when loccur => mq <= s1write;</pre>
 83
                            when writx => mq <= s2write ;</pre>
 84
                            when writx1 => mq <= s3write;</pre>
 85
                            when writx2 => mq <= s4write;</pre>
 86
                            when writx3 => mg <= s5write;</pre>
 87
                            when writx4 => mq <= s6write;</pre>
 88
                            when writx5 => mq <= s7write;</pre>
 89
                            when writx6 => mq <= s8write;</pre>
 90
                            when writx7 => mq <= sfin;</pre>
 91
 92
                            when others => null;
                          end case;
 93
 94
 95
          --inicializaci?n
 96
 97
98
                    when si1 = - se configura LCD a 8 bits, 2 l?neas y caracter de 5 x 7
 99
100
                   ena <= '0';
101
                   mq <= si2;
102
103
                    when si2 \Rightarrow
104
                       if tc < 1000001 then
105
                           tc <= tc + 1;
106
                           mq <= si2;
107
                       else
108
                           tc <= (others =>'0');
109
                           mq \le si3;
110
                       end if;
111
```

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112
                   when si3 =>
                         db <= x"38";
113
                         rs <= '0';
114
115
                          mq \le se1;
116
117
                   when slini2 => -- entry mode -- incremente apuntador y mueva cursor a
      la derecha
118
                         var <= ini2;</pre>
119
                         db \le x"06";
120
                        rs <= '0';
121
                         mq \le sel;
122
123
                   when slini3 => -- Display on, cursor off y est?tico.
124
                       var <= ini3;</pre>
125
                        db <= x"0C";
126
                       rs <= '0';
127
                       mq \le se1;
128
129
                   when slini4 => -- se borra display
130
                      var <= ini4;</pre>
131
                      db <= x"01";
                      rs <='0';
132
133
                      mq \le se1;
134
135
                   when slini5 => -- se espera ejecuci?n del borrado
                       if tc < 1000001 then
136
137
                           tc <= tc + 1;
138
                           mq \le slini5;
139
                       else
140
                           tc <= (others =>'0');
141
                           mq <= s1lcur;
142
                       end if:
143
144
                   when sllcur => -- ubica cursor en la posici?n 00
145
                   var <= loccur;</pre>
146
                   db <= x"80";
                   rs <= '0';
147
148
                   mq \le se1;
149
                   -- Aqui ponemos que se escribe con cada selectror
150
                   --x"numnum" para escribir un simbolo em particular
151
                   --var&var para un simbolo desede una señal
152
                   -- el var, se configura al siguiente en la lista
153
                   when slwrite =>
154
                          var <= writx;</pre>
155
                          rs <= '1';
                          db <= "0011"&DECENASMIL;
156
157
                         mq \le se1;
158
                   when s2write =>
159
                         var <= writx1;</pre>
160
                          rs <= '1';
                          db <= "0011"&UNIDADESMIL;</pre>
161
162
                          mq \le se1;
163
                   when s3write =>
                         var <= writx2;</pre>
164
                          rs <= '1';
165
                         db <= "0011"&CENTENASUN;
166
167
                         mq \le se1;
```

```
168
169
                    when s4write =>
                          var <= writx3;</pre>
170
171
                          rs <= '1';
172
                          db <= "0011"&DECENASUN;
173
                          mq \le se1;
174
175
                    when s5write =>
176
                          var <= writx4;</pre>
177
                          rs <= '1';
                          db <= "0011"&UNIDADESUN;
178
179
                          mq \le se1;
180
181
                   when s6write =>
182
                          var <= writx5;</pre>
183
                          rs <= '1';
                          db <= x"2E"; --punto
184
185
                          mq <= se1;
186
187
                    when s7write =>
188
                          var <= writx6;</pre>
                          rs <= '1';
189
190
                          db <= "0011"&DECENASDEC;</pre>
191
                          mq <= se1;
192
193
                   when s8write =>
194
                          var <= writx7;</pre>
195
                          rs <= '1';
196
                          db <= "0011"&UNIDADESDEC;</pre>
197
                          mq <= se1;
198
199
                  when sfin => mq <= s1lcur;</pre>
200
201
202
203
                   when others => null;
204
                end case;
205
            end if;
      end process;
206
207
208
209
210
    end Behavioral;
211
```