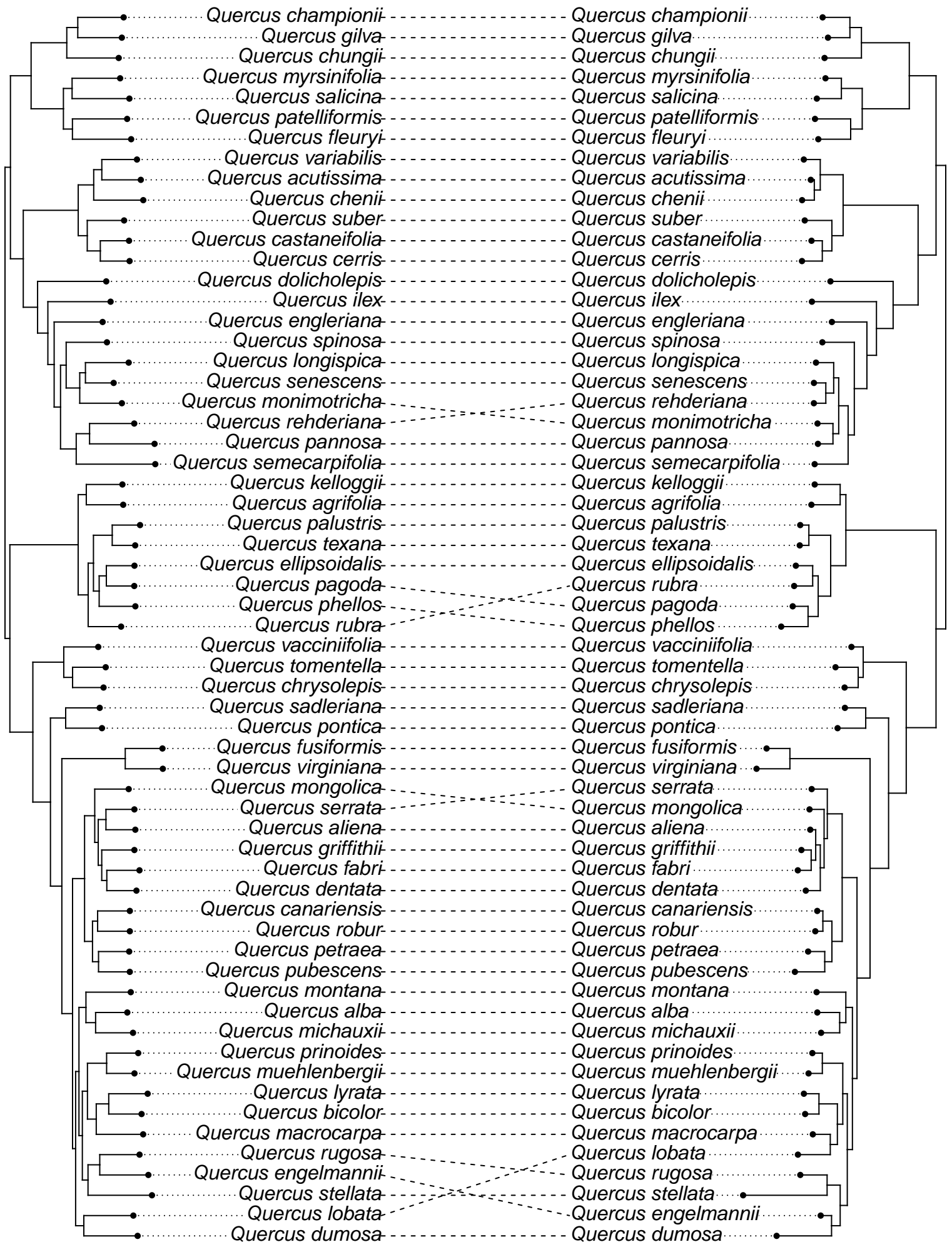
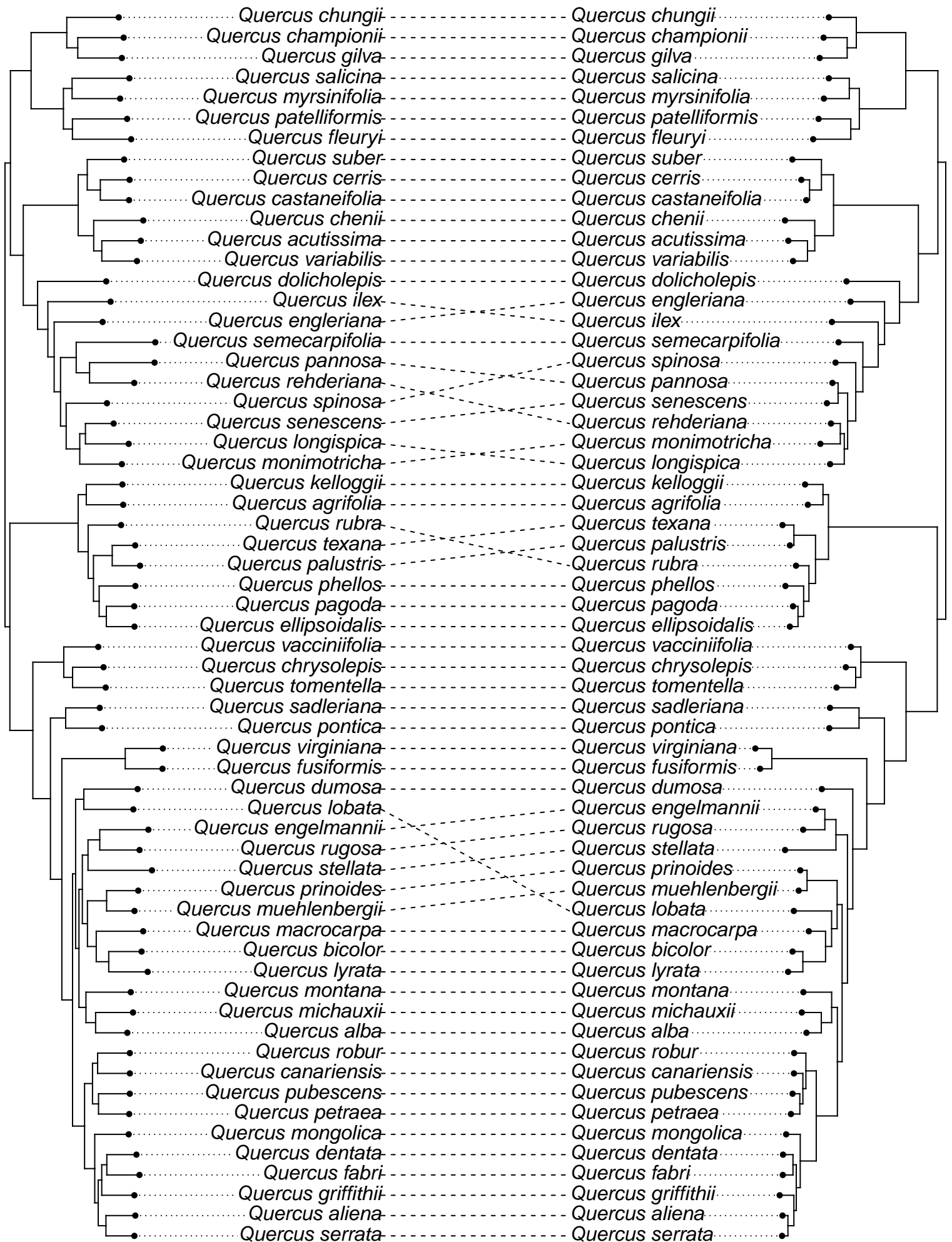


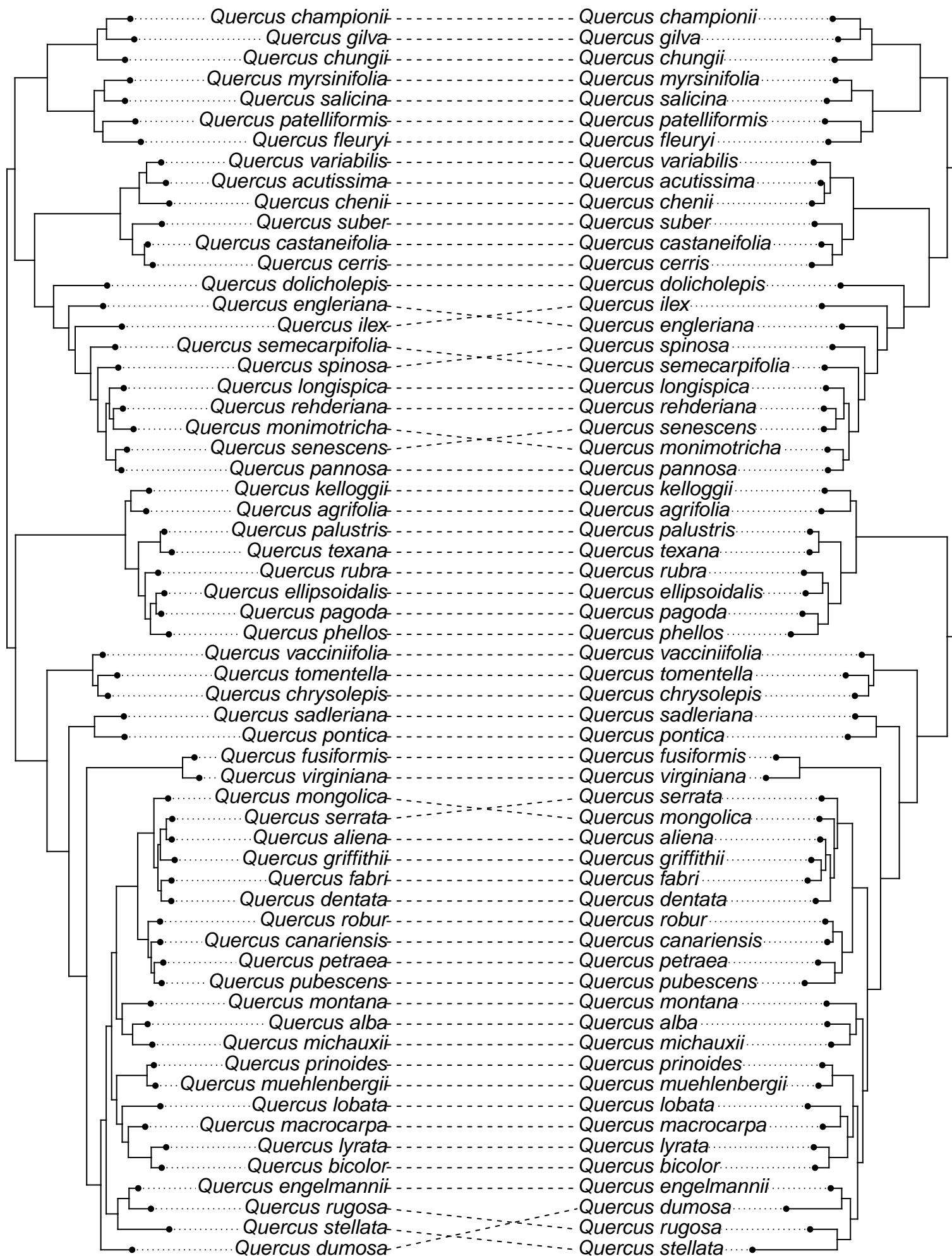
# simRADalb\_refalb



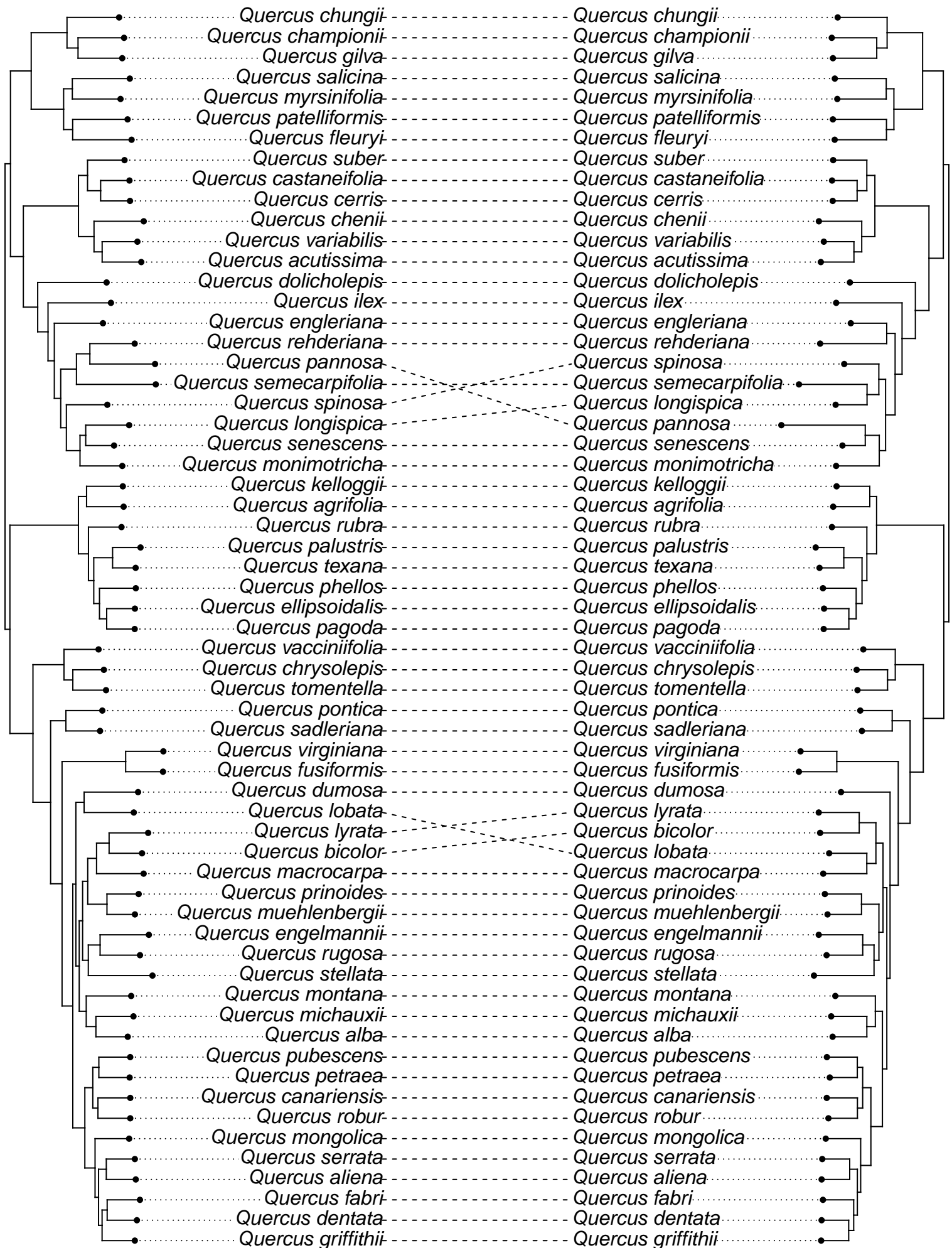
# simRADalb\_reseqalb



# reseqalb\_refalb



# simRADalb\_simRADvar



# reseqalb\_reseqvar

