Programming Assignment 5 Report

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Problem Description

For this programming assignment we are implementing a page table and different page replacement algorithms. Were instructed to assume a single running process and that the physical memory is fixed size, using the replacement algorithms FIFO, LIFO, and LRU.

Program Design

The basic program design is separate class files for each replacement algorithm (FIFO, LIFO, LRU) that get called and executed in the main class. The main class takes user input for page size in bytes (256-8192), and the physical memory size in megabytes (4-64) that's used for the simulated memory management. The program then runs all three algorithms with two tests, a small-sample test, and a large-same test. This allows us to see how the algorithms scale. The program outputs the number of references, page faults, and total page replacements.

System Implementation

The FIFO and LIFO algorithms are built pretty similarly with just the order of withdrawal being reversed. Both have the same functions: load_page() and replace_page() which do as they say. The LRU algorithm still includes load_page() and replace_page() but adds touch_page() to update its position on how recently it was used.

@ lifo_replacement.cpp

```
Jsers > mitchellcurtis > Downloads > 253325317 > 😉 Iru_replacement.cpp
      void LRUReplacement::touch_page(int page_num)
29
30
          // page is in the page table need to update its position in the list
31
          // search the list for the page number
32
          // erase that from the list and push it to the the front
33
          auto it = find(buffer.begin(), buffer.end(), page_num);
34
          buffer.erase(it);
35
          buffer.push_front(page_num);
36
37
38
      // Access an invalid page, but free frames are available
39
      void LRUReplacement::load_page(int page_num) {
40
          // page table has free frames so we just load a page
41
          // create new PageEntry and insert into table
42
          PageEntry temp;
43
          temp.frame_num = num_frames;
44
          temp.valid = true;
45
          temp.dirty = false;
46
          page_table[page_num] = temp; // insert into page table
47
48
          //edit the map and list
49
          indexes[page_num] = indx; // insert pagenum and used index of the page
50
51
          buffer.push_front(page_num); // push the new page to the front and push everything behind it
52
53
54
55
56
      // Access an invalid page and no free frames are available
57
      int LRUReplacement::replace_page(int page_num) {
58
          // page table has no free frames now we replace
59
          int replaced_page_num = buffer.back();
60
61
          // remove top of buffer
62
          buffer.pop_back();
63
64
          // update the page table entry for the replaced page
65
          page_table[replaced_page_num].valid = false; // make as invalid
66
67
          // create new page entry for the page table
68
          PageEntry new_page;
69
          new_page.frame_num = page_table[replaced_page_num].frame_num; // reuse the frame
70
          new_page.valid = true; // mark new page as valid
71
          new_page.dirty = false;
72
73
          // update the page table with the new entry
74
          page_table[page_num] = new_page;
75
76
          // Add the index of the new page to the top of the buffer
77
          buffer.push_front(page_num);
78
79
          // Update the current page index
80
          indexes[page_num] = indx;
81
          //[replaced_page_num] = NULL;
```

The pagetable file contains two classes: PageEntry and PageTable that store the actual

pages and entries used during the simulation.

```
class PageEntry
24
25
26
     public:
27
         // Physical frame number for a given page
28
         int frame_num;
29
         // valid bit represents whether a page is in the physical memory
30
         bool valid = false;
31
         // dirty bit represents whether a page is changed
32
         bool dirty = false;
33
     };
34
35
36
37
      * @brief A page table is like an array of page entries.
38
      * The size of the page table should equal to the number of pages in logical mem
39
     class PageTable
40
41
42
     private:
43
          // A page table is like an array of page entries.
44
         vector<PageEntry> pages;
45
46
     public:
47
         // Constructor
48
         PageTable(int num_pages);
49
         // Destructor
50
         ~PageTable();
51
52
          // TODO: Add your implementation of the page table here
53
54
55
          * @brief Access a page in the page table.
56
          * @param i
57
          * @return
58
59
          PageEntry& operator [] (int i) {
60
             return pages[i];
61
62
```

Results

```
mitchellcurtis@MacBook-Pro-9 253325317 % ./prog5 256 4
CS 433 Programming assignment 5
Author: xxxxxx and xxxxxxx
Date: xx/xx/20xx
Course: CS433 (Operating Systems)
Description : Program to simulate different page replacement algorithms
Page size = 256 bytes
Physical Memory size = 4194304 bytes
Number of pages = 524288
Number of physical frames = 16384
                           ====Test 1===
Logical address: 101853141,
                                                        frame number = 0,
                               page number: 397863,
                                                                               is page fault? 1
Logical address: 101853027,
                                                                               is page
                                                        frame number = 0,
                               page number: 397863,
                                                                                       fault? 0
                                                        frame number = 1,
Logical address: 72042423,
                               page number: 281415,
                                                                               is page fault?
Logical address: 71971407,
                               page number: 281138,
                                                        frame number = 2,
                                                                               is page fault?
Logical address: 72084158,
Logical address: 101853169,
                               page number: 281578,
                                                        frame number = 3,
                                                                               is page fault?
                                                        frame number = 0,
                                                                               is page fault? 0
                               page number: 397863,
Logical address: 101854129,
                               page number: 397867,
                                                        frame number = 4,
                                                                               is page fault? 1
Logical address: 101853782,
Logical address: 101853138,
                                page number: 397866
                                                        frame number = 14,
                                                                                is page fault? 0
is page fault? 0
                                page number: 397863,
                                                        frame number = 0,
                                                        frame number = 2,
Logical address: 71971390,
                                page number: 281138,
Logical address: 71971405,
                                page number: 281138,
                                                        frame number = 2,
                                                                                is page fault? 0
Number of references:
Number of page faults:
                                100
                                35
Number of page replacements:
                               =Test 2=
Total number of references: 2000000
2000000
340108
Number of references:
2000000
Number of references:
Number of page faults:
                                293297
Number of page replacements:
Elapsed time = 21 seconds
                                276913
mitchellcurtis@MacBook-Pro-9 253325317 %
```

Conclusion

Our program output shows that while LRU is taking more time, its producing considerably less page faults and replacements which implies that there may be a faster way of implementing this so that LRU is the fastest algorithm.