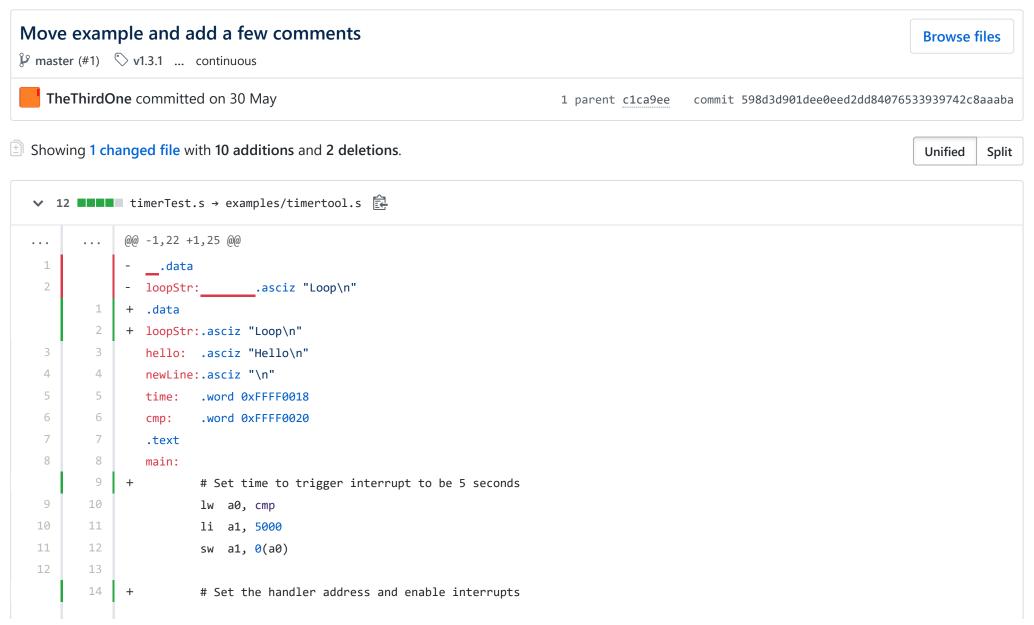
TheThirdOne / rars



```
13
       15
                       la
                                t0, handle
14
                               zero, 5, t0
       16
                        csrrs
15
                       csrrsi zero, 4, 0x10
       17
16
       18
                       csrrsi zero, 0, 0x1
17
       19
18
       20
19
       21
                loop:
       22
                       # Output current time in loop
20
       23
                       li
                                a7, 1
21
       24
                        lw
                                a0, time
22
       25
                        lw
                                a0, 0(a0)
28
       31
29
       32
       33
30
                handle:
                       # Save some space for temporaries
        34
31
                        addi
                                sp, sp, -20
32
       36
                                t0, 16(sp)
                        SW
33
       37
                                t1, 12(sp)
                        SW
34
                               t2, 8(sp)
       38
                        SW
35
                                a0, 4(sp)
       39
                        SW
                                a7, 0(sp)
36
       40
                        SW
37
       41
        42
                       # Print out hello
38
       43
                       li
                                a7, 4
39
       44
                        la
                                a0, hello
40
       45
                        ecall
        46
       47
                       # Set cmp to time + 5000
       48
41
                        lw a0 time
42
       49
                       lw t2 0(a0)
                       li t1 5000
43
       50
                        add t1 t2 t1
44
       51
45
       52
                       lw t0 cmp
```

```
53
46
                       sw t1 0(t0)
47
       54
       55
                       # Reload the saved registers and return
48
       56
                               t0, 16(sp)
                       lw
                              t1, 12(sp)
49
       57
                       lw
                       lw
                              t2, 8(sp)
50
       58
```

0 comments on commit 598d3d9

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