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Tree: 1f06f3751d ▼

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1f06f37 on 7 Jun

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46 lines (36 sloc) 1.85 KB

```
1  package rars.riscv.instructions;
2
3  import rars.ProgramStatement;
4  import rars.riscv.hardware.RegisterFile;
5  import rars.riscv.BasicInstruction;
6  import rars.riscv.BasicInstructionFormat;
7
8  /*
9   Copyright (c) 2017, Benjamin Landers
10
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```
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31
32 (MIT license, http://www.opensource.org/licenses/mit-license.html)
33 */
34
35 public class AUIPC extends BasicInstruction {
36     public AUIPC() {
37         super("auipc t1,100000", "Add upper immediate to pc: set t1 to (pc
38             BasicInstructionFormat.U_FORMAT, "ssssssssssssssssssss fff
39     }
40
41     public void simulate(ProgramStatement statement) {
42         int[] operands = statement.getOperands();
43         RegisterFile.updateRegister(operands[0], RegisterFile.getProgramCo
44     }
45 }
```