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% Andrew Branicki 100973961
% Elec 4700 Assignment 4
```

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clear
clc
```

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MNPA
Question2
Question3
Question4
```

Question 2 a) We can see from the frequency response plot in the MNPA that this is a low pass filter circuit.

As the frequency increases, the inductor will resist the change in current (it will start to block current) and the capacitor will create a parallel resistance with the resistor.

Question 2 b) The expected frequency response would be a constant gain at low frequencies, and then a -3 dB cutoff at the specific cutoff frequency ($f_c = 1/(2\pi RC)$) with a drop in gain after that.

QUESTION 2 G and C matrices:

Columns 1 through 7

1.0000	-1.0000	0	0	0	0	0
-1.0000	1.5000	0	0	0	1.0000	0
0	0	0.1000	0	0	-1.0000	0
0	0	0	10.0000	-10.0000	0	1.0000
0	0	0	-10.0000	10.0010	0	0
0	1.0000	-1.0000	0	0	0	0
0	0	-10.0000	1.0000	0	0	0
1.0000	0	0	0	0	0	0

Column 8

1.0000
0
0
0
0
0
0
0
0

Columns 1 through 7

0.2500	-0.2500	0	0	0	0	0
-0.2500	0.2500	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	-0.2000	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0

Column 8

0
0
0
0
0
0
0
0
0

Question 2: SINE WAVE INPUT: We can see that at slower frequencies the gain of the circuit increases. We are able to get a larger voltage output.

Question 2: Changing the time step changes how accurate the simulation is. With a larger time step we have less accurate simulations since we are getting less detail overall.

QUESTION 3 G and C matrices:

Columns 1 through 7

1.0000	-1.0000	0	0	0	0	0
-1.0000	1.5000	0	0	0	1.0000	0
0	0	0.1000	0	0	-1.0000	0
0	0	0	10.0000	-10.0000	0	1.0000
0	0	0	-10.0000	10.0010	0	0
0	1.0000	-1.0000	0	0	0	0
0	0	-10.0000	1.0000	0	0	0
1.0000	0	0	0	0	0	0

Column 8

1.0000
0
0
0
0
0
0
0
0

Columns 1 through 7

0.2500	-0.2500	0	0	0	0	0
-0.2500	0.2500	0	0	0	0	0
0	0	0.0000	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	-0.2000	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0

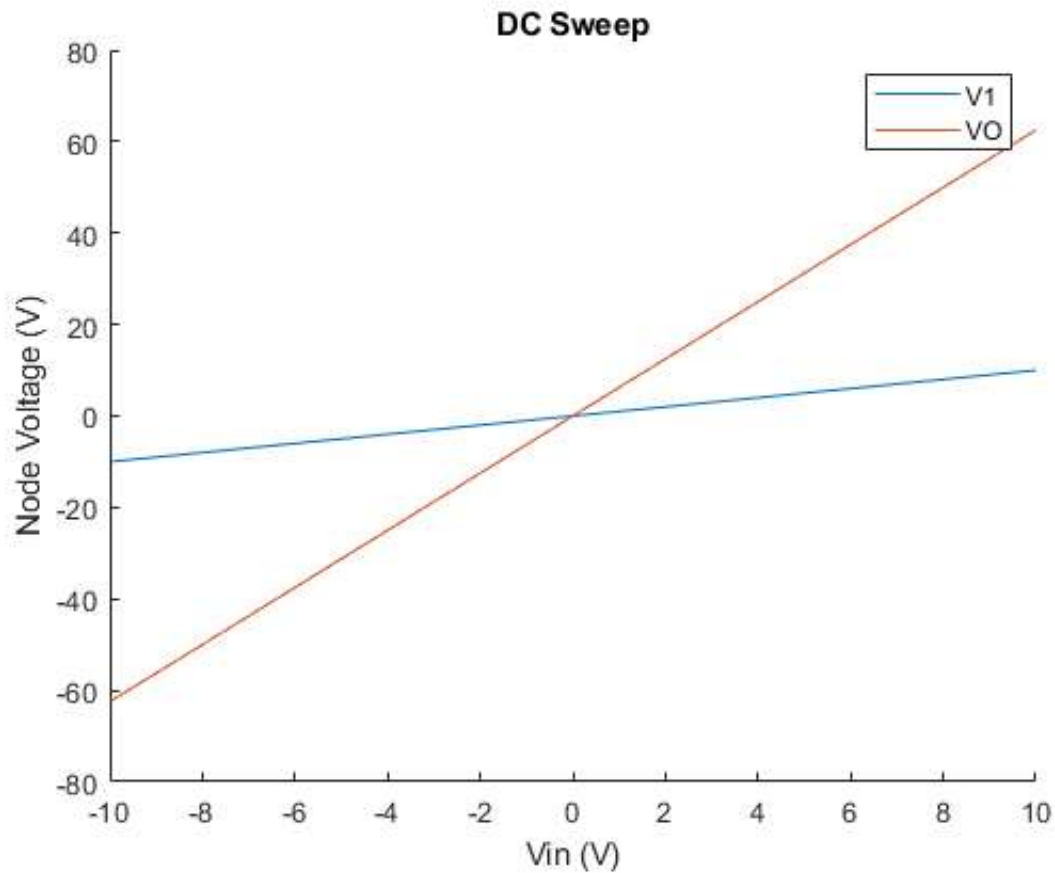
Column 8

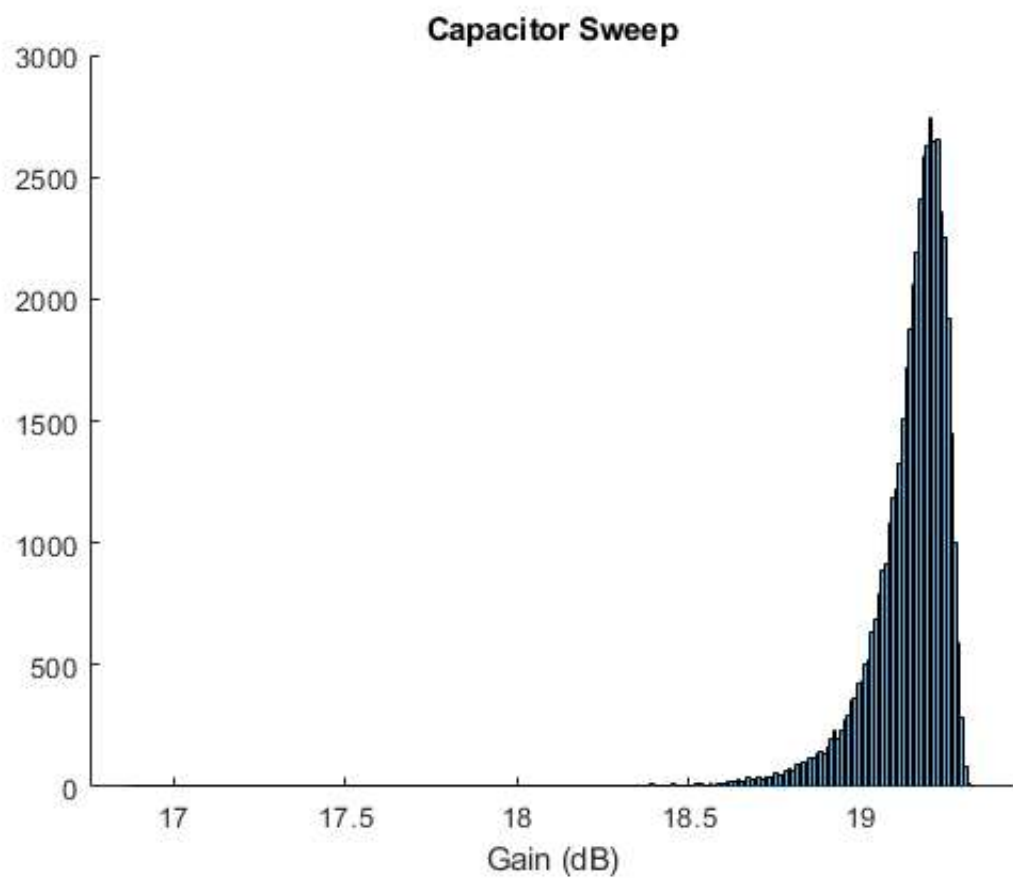
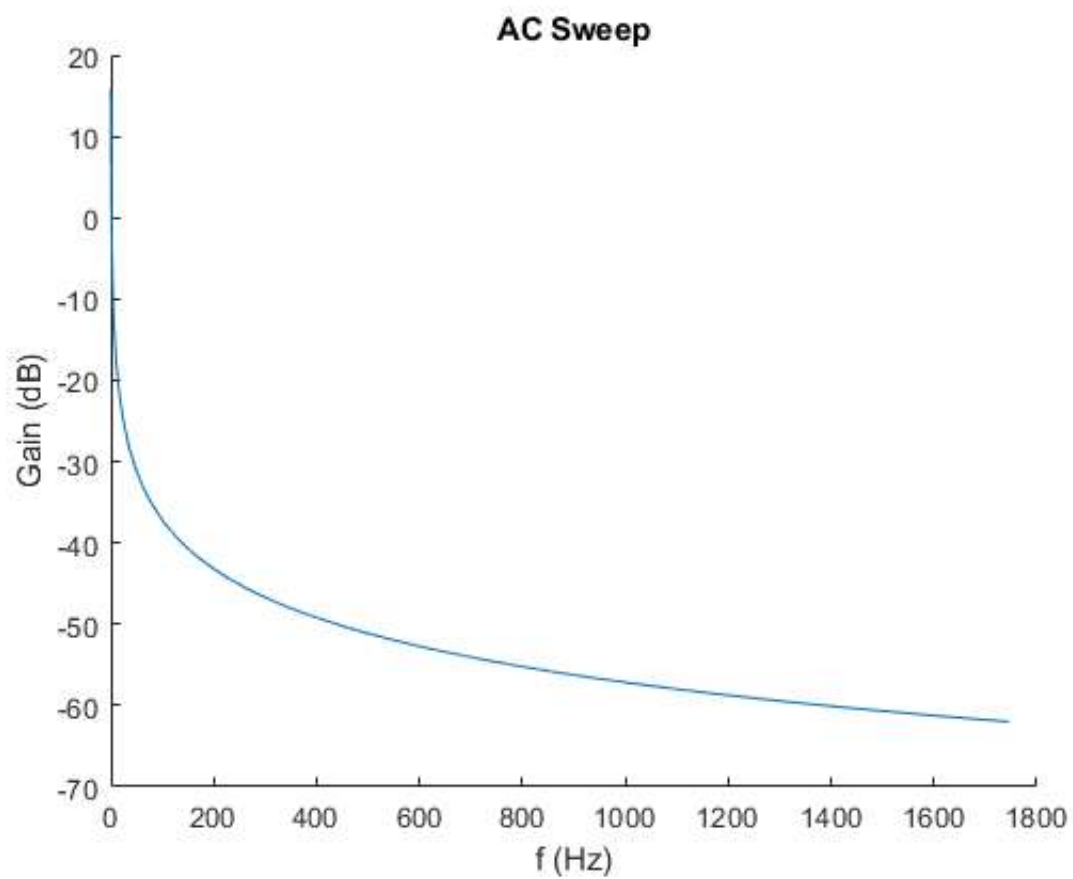
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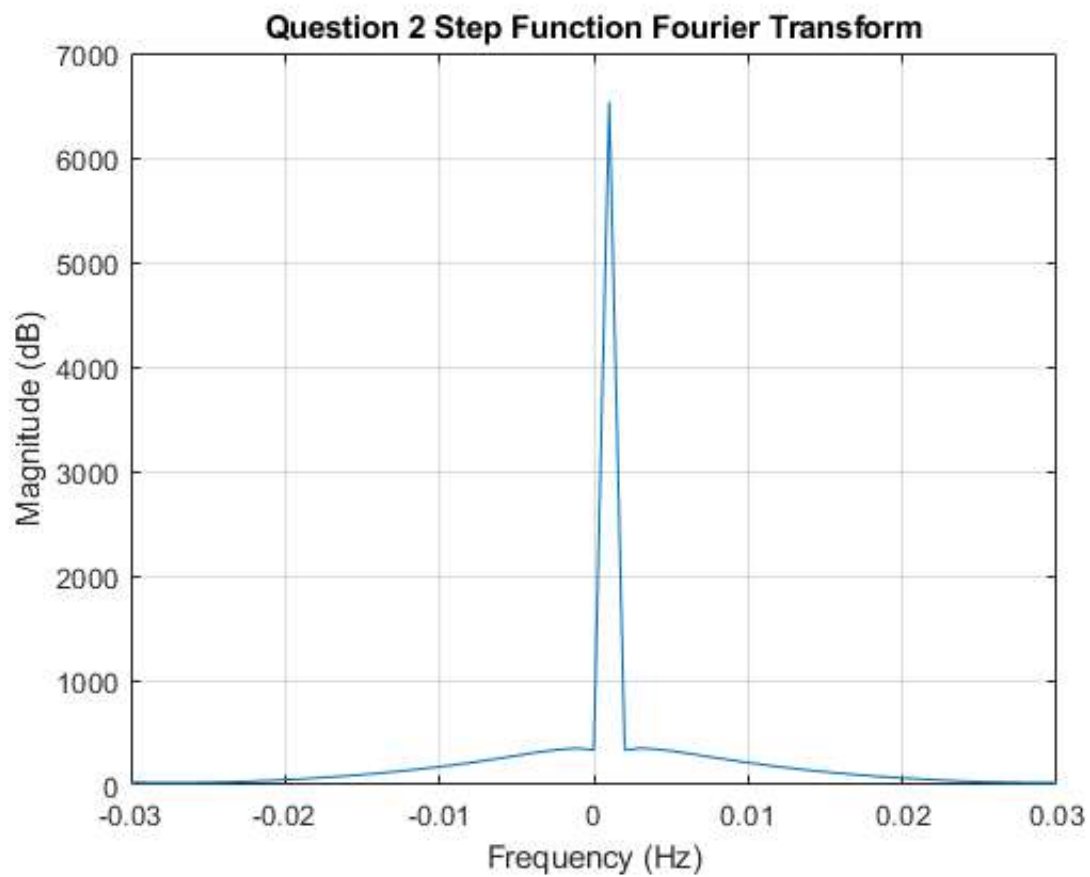
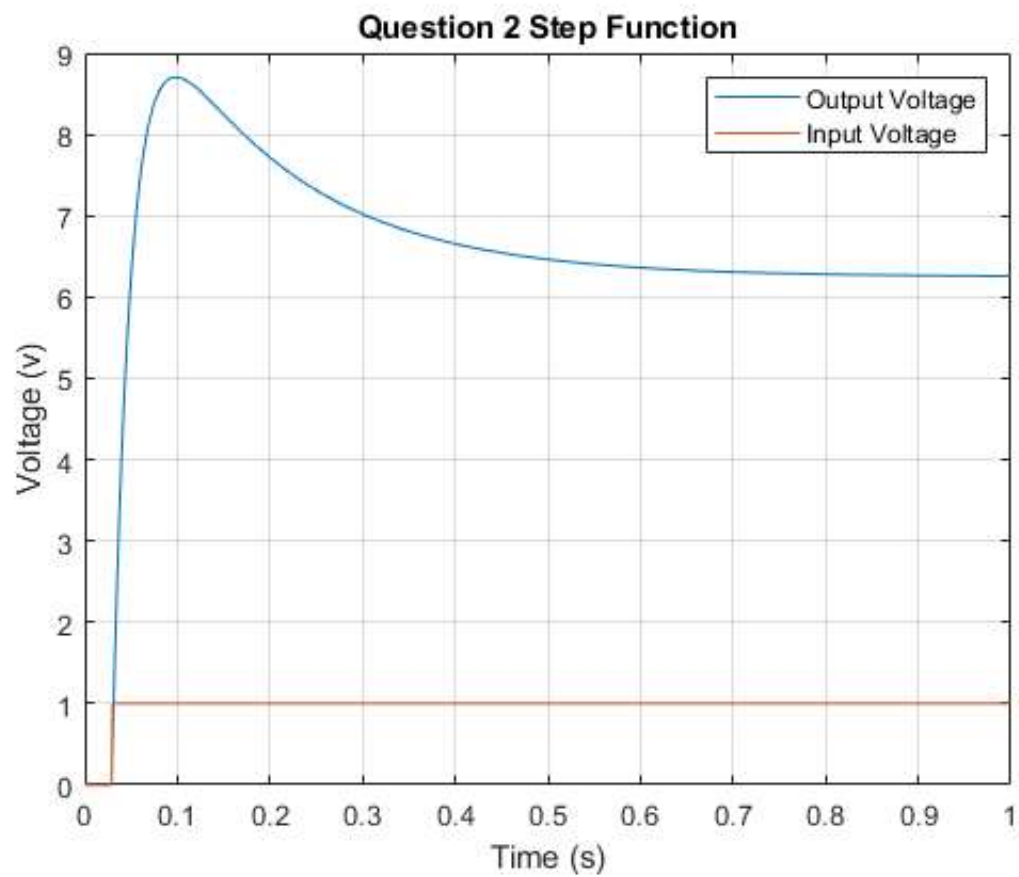
QUESTION 3: We can see how increasing the value of C_n reduces the overall output of the circuit. With $C_3 = 0.01$, the output is actually lower than the input.

QUESTION 3: As before, changing the timestep will have an effect on the accuracy of the simulation.

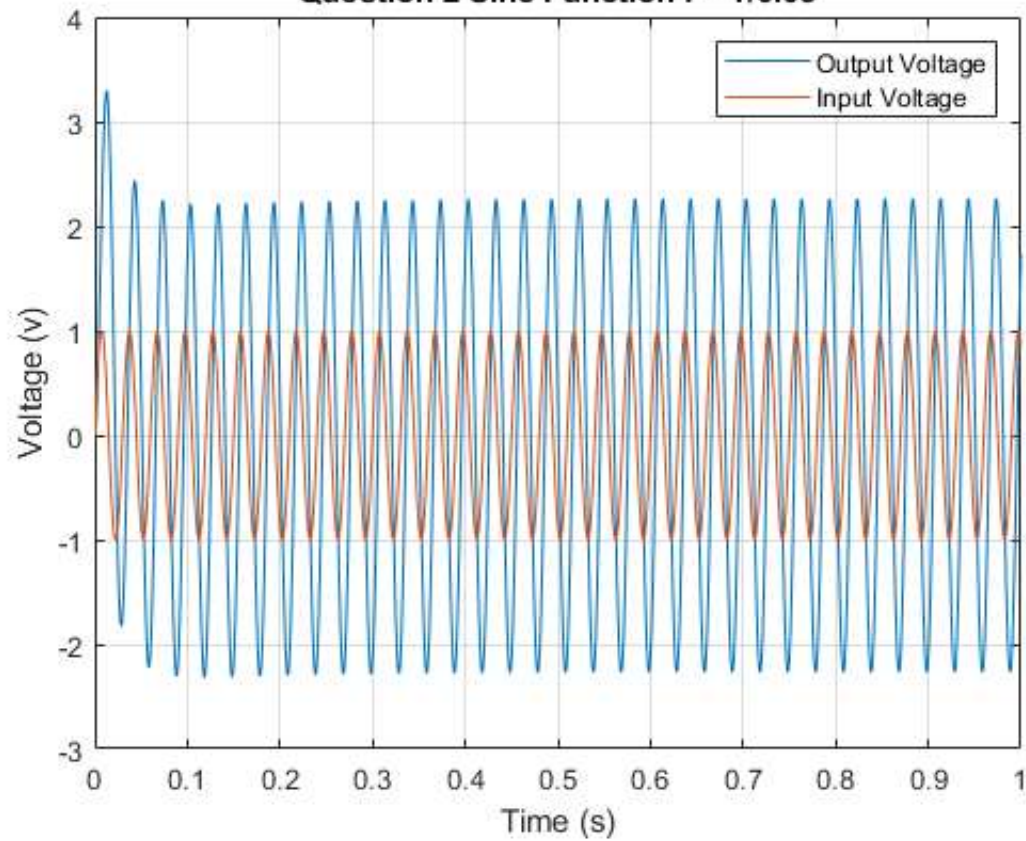
QUESTION 4: In this case we can see that V is modelled by a cubic equation. We would need the values for β and γ , and then we can take the roots of this function and then use the values of the roots in our MNA matrices.



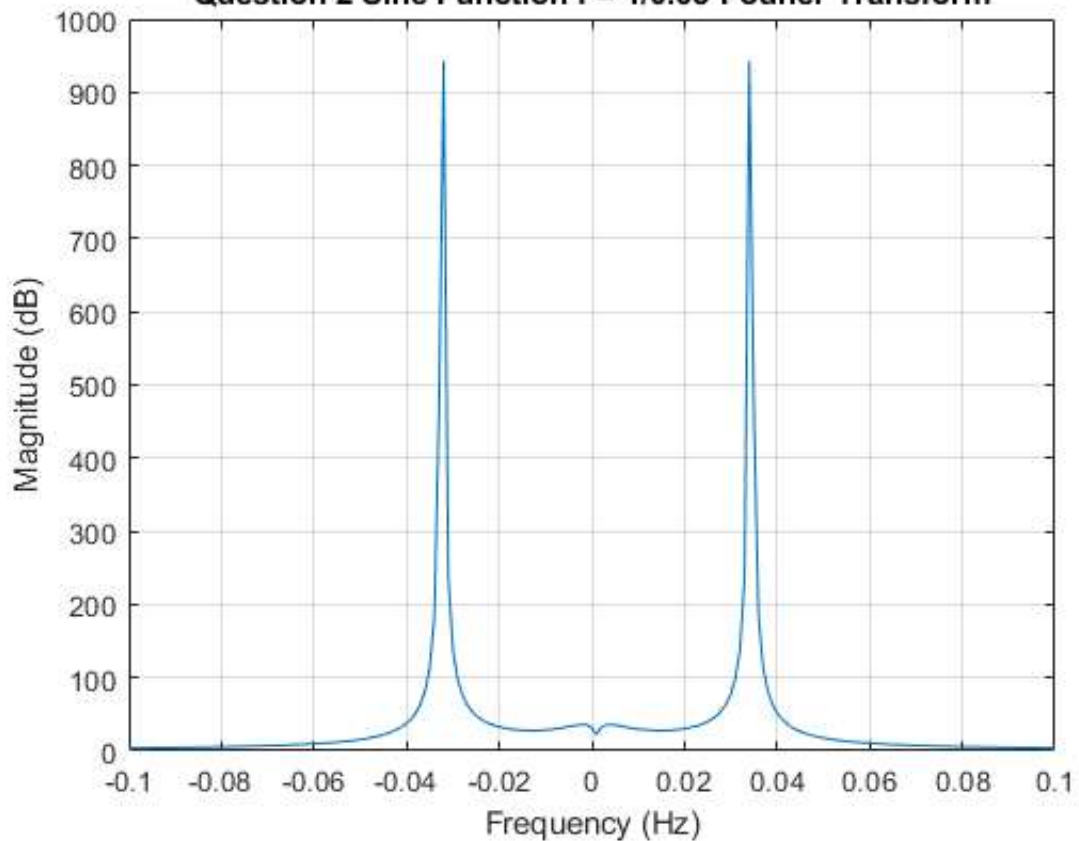




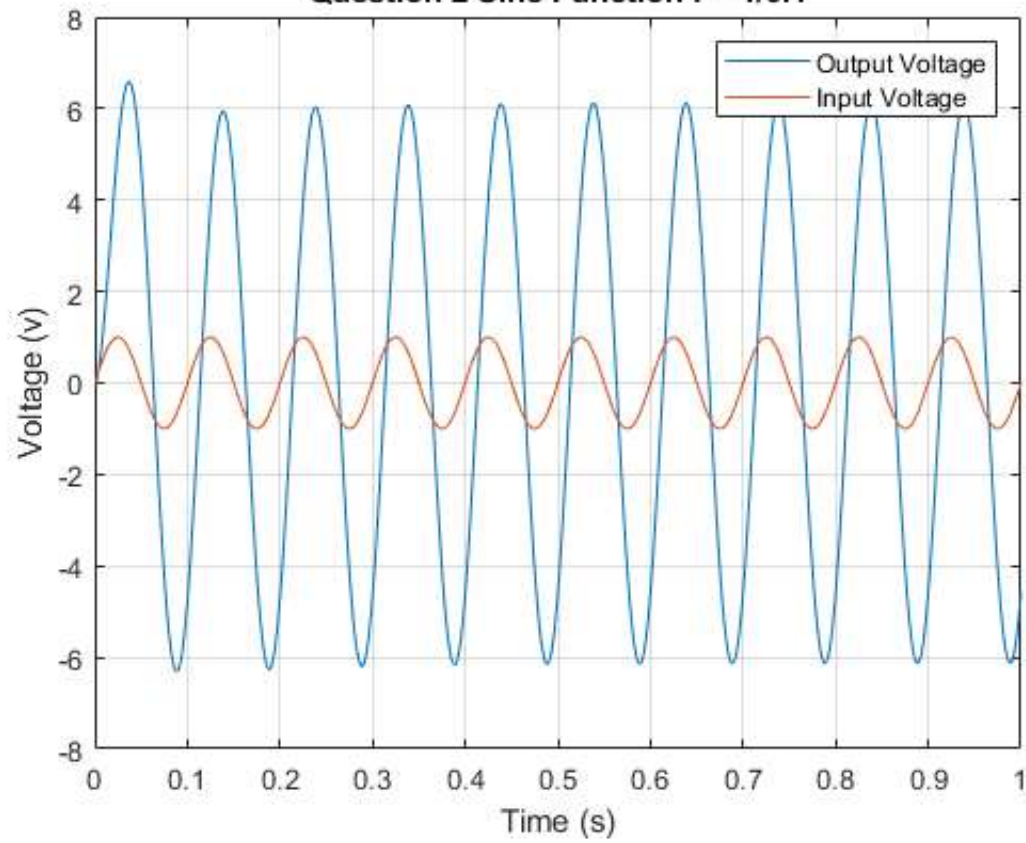
Question 2 Sine Function $f = 1/0.03$



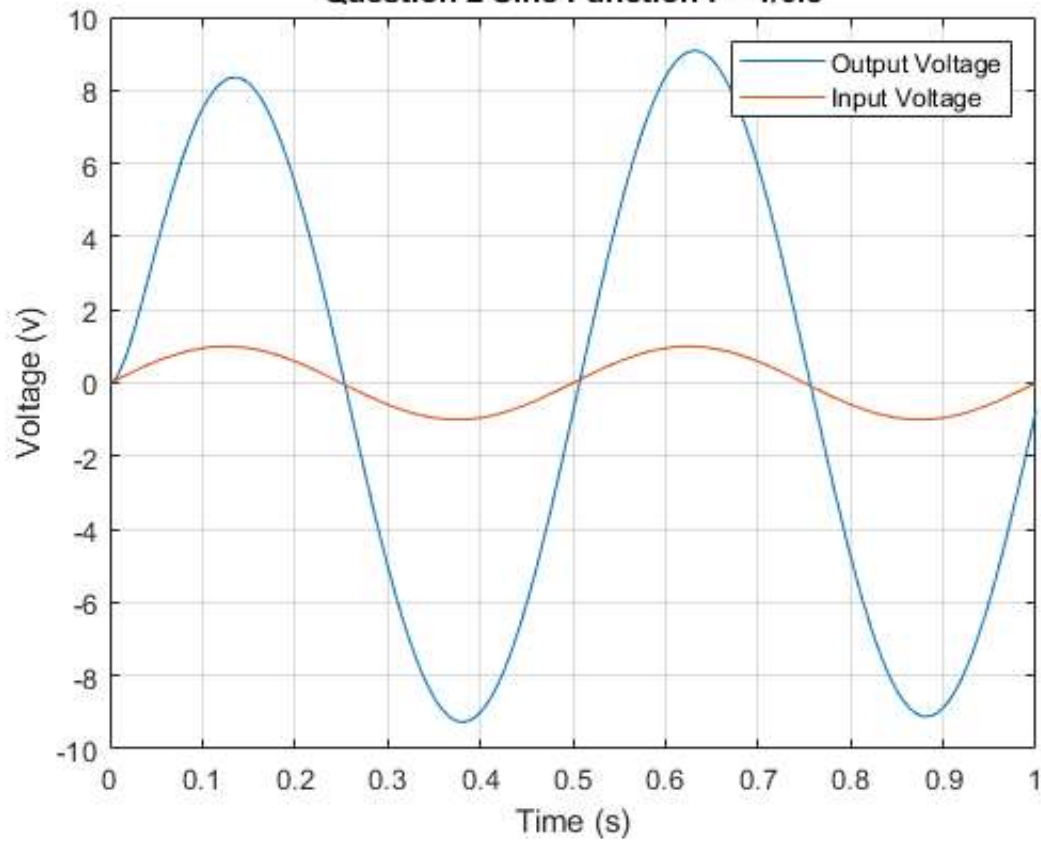
Question 2 Sine Function $f = 1/0.03$ Fourier Transform



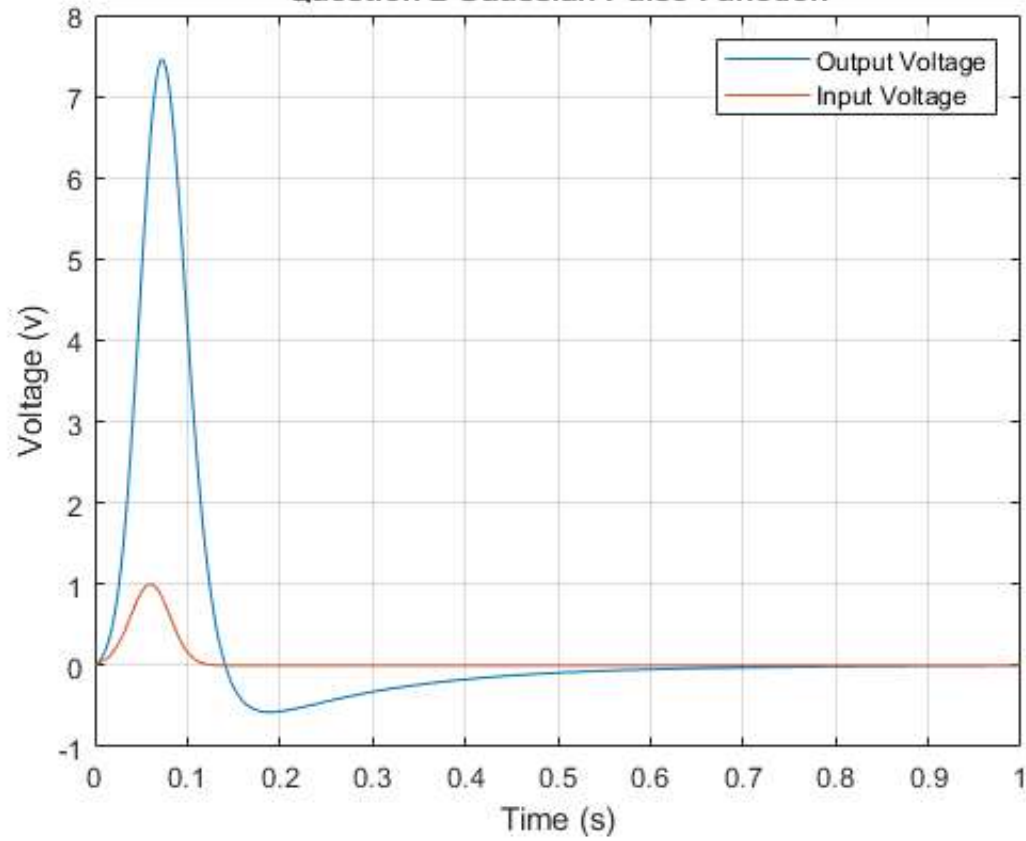
Question 2 Sine Function $f = 1/0.1$



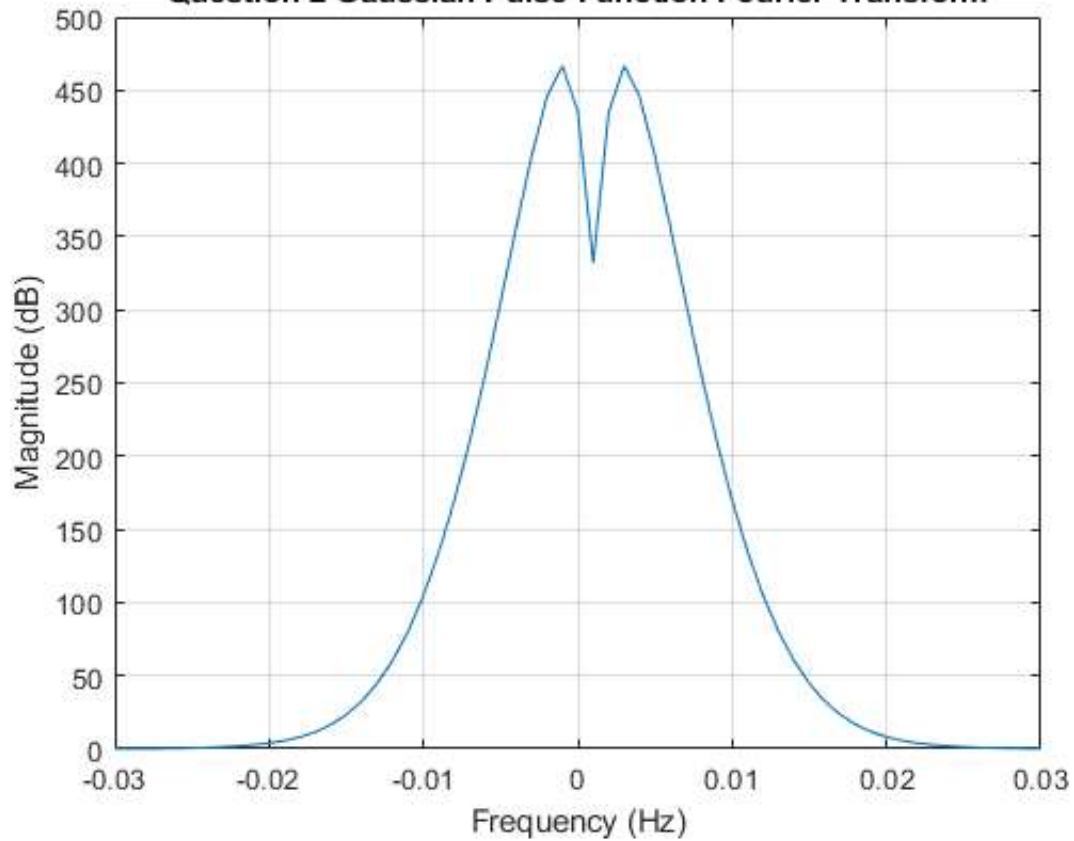
Question 2 Sine Function $f = 1/0.5$



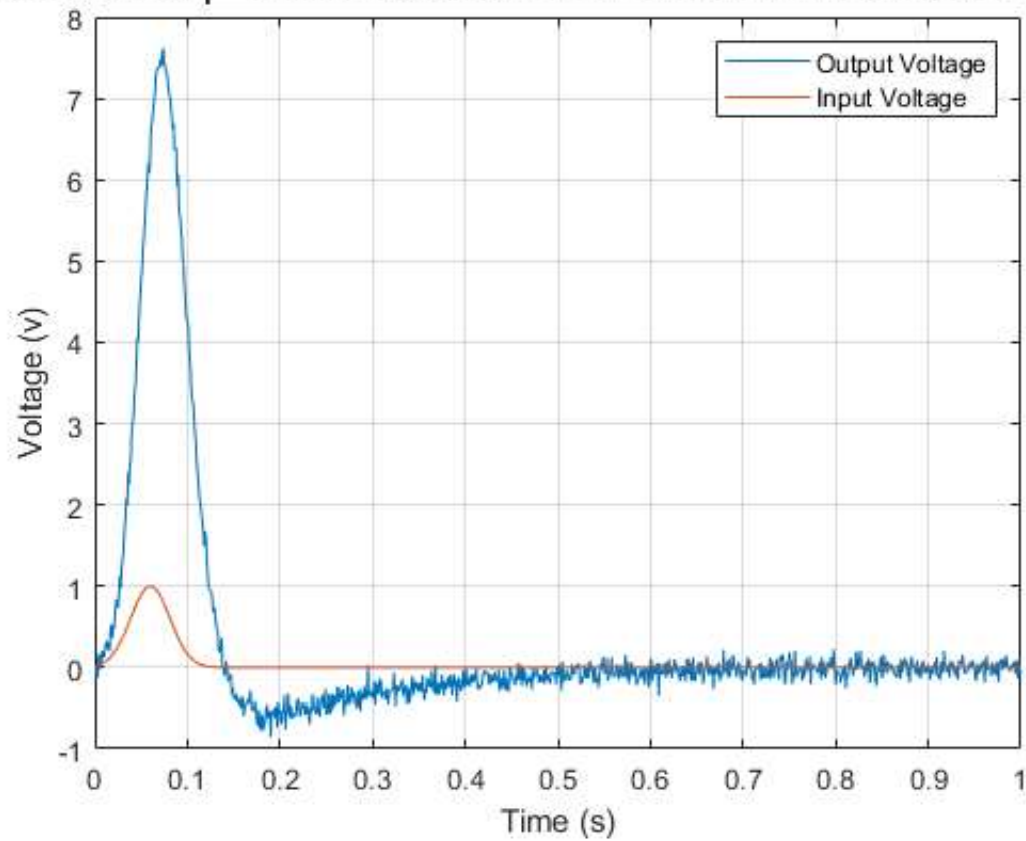
Question 2 Gaussian Pulse Function



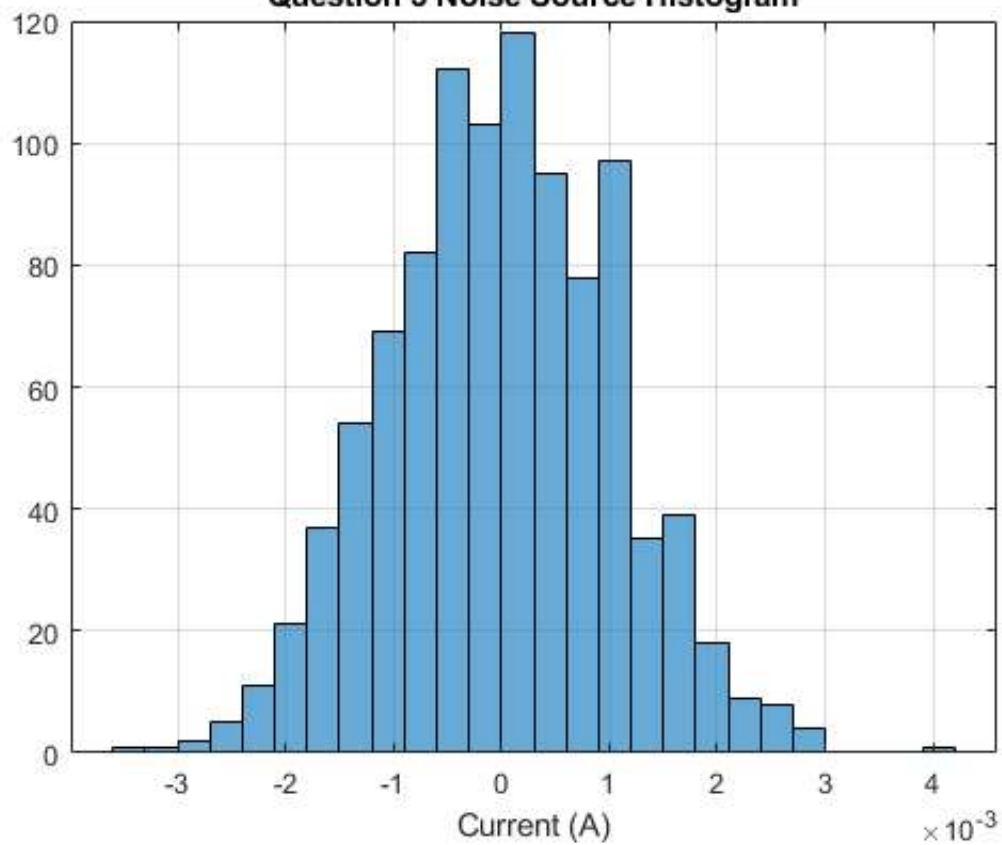
Question 2 Gaussian Pulse Function Fourier Transform



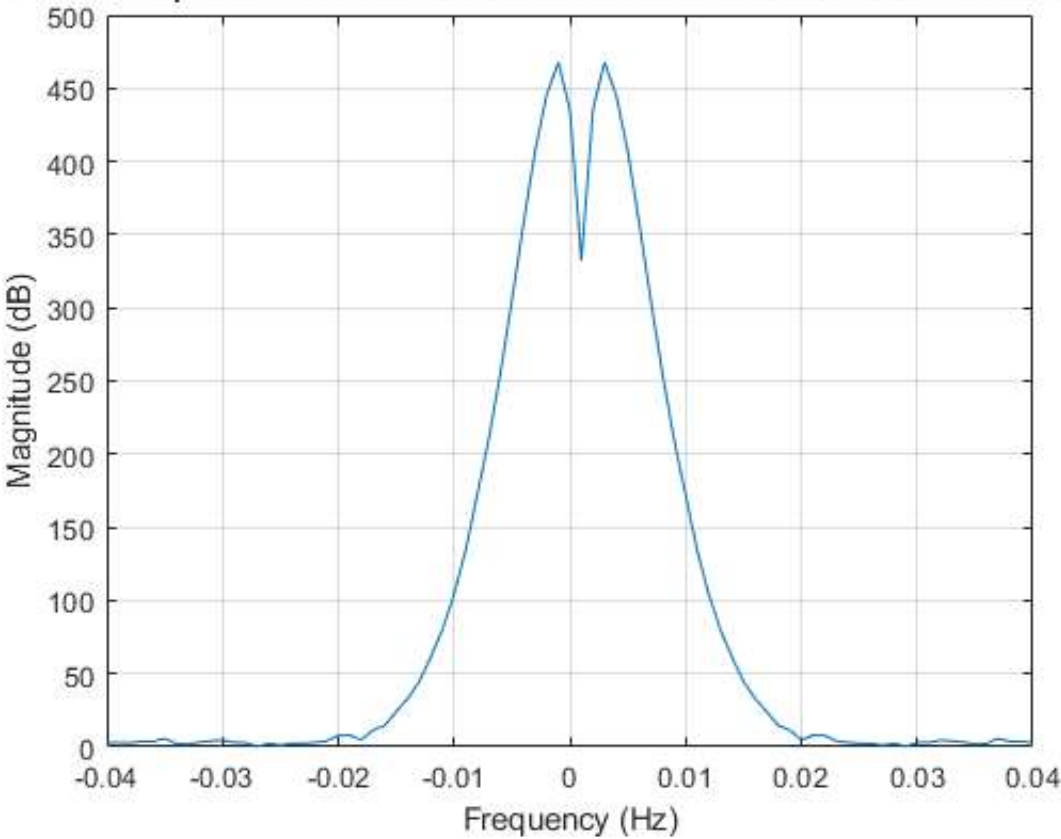
Question 3 $CN_1 = 0.00001$ Gaussian Pulse Function with Added Noise Source



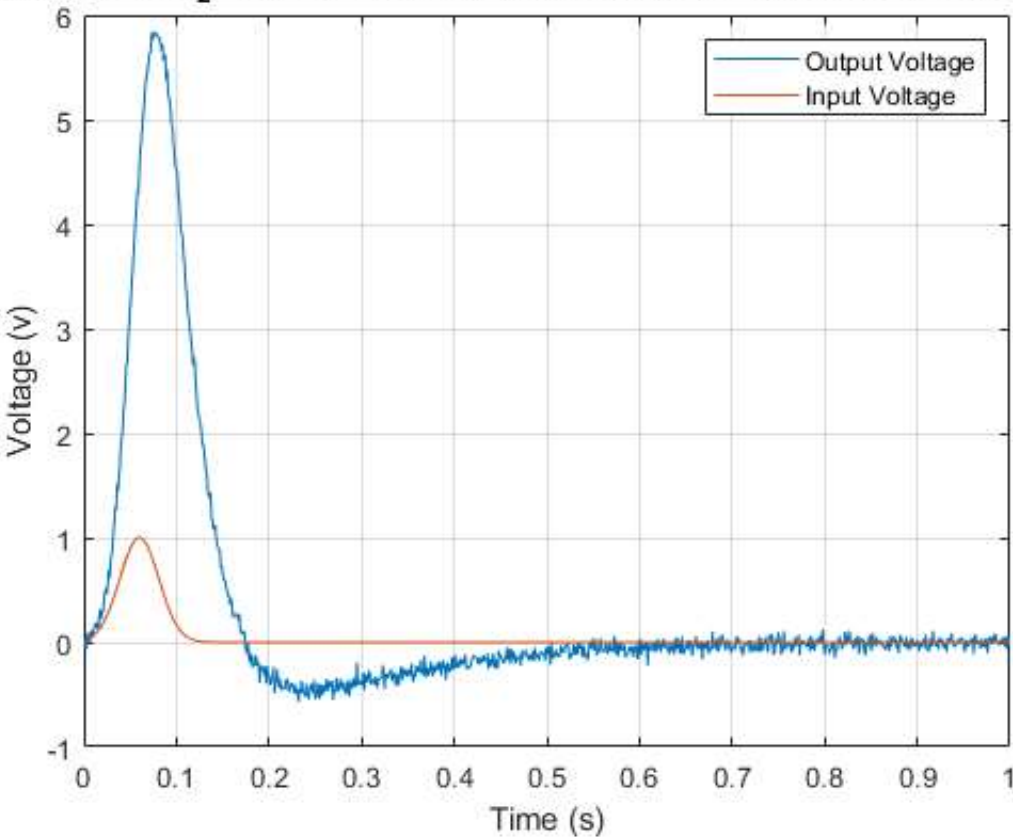
Question 3 Noise Source Histogram



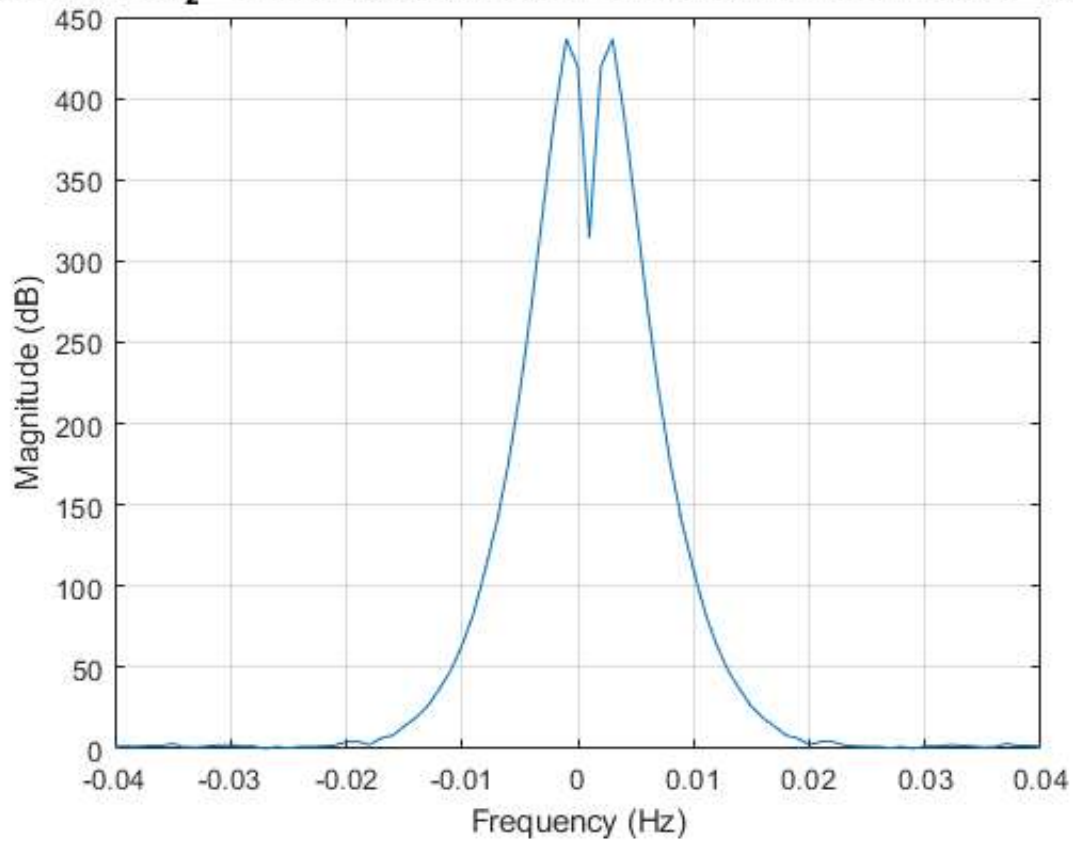
Question 3 $CN_1 = 0.00001$ Gaussian Pulse Function (With Noise) Fourier Transfo



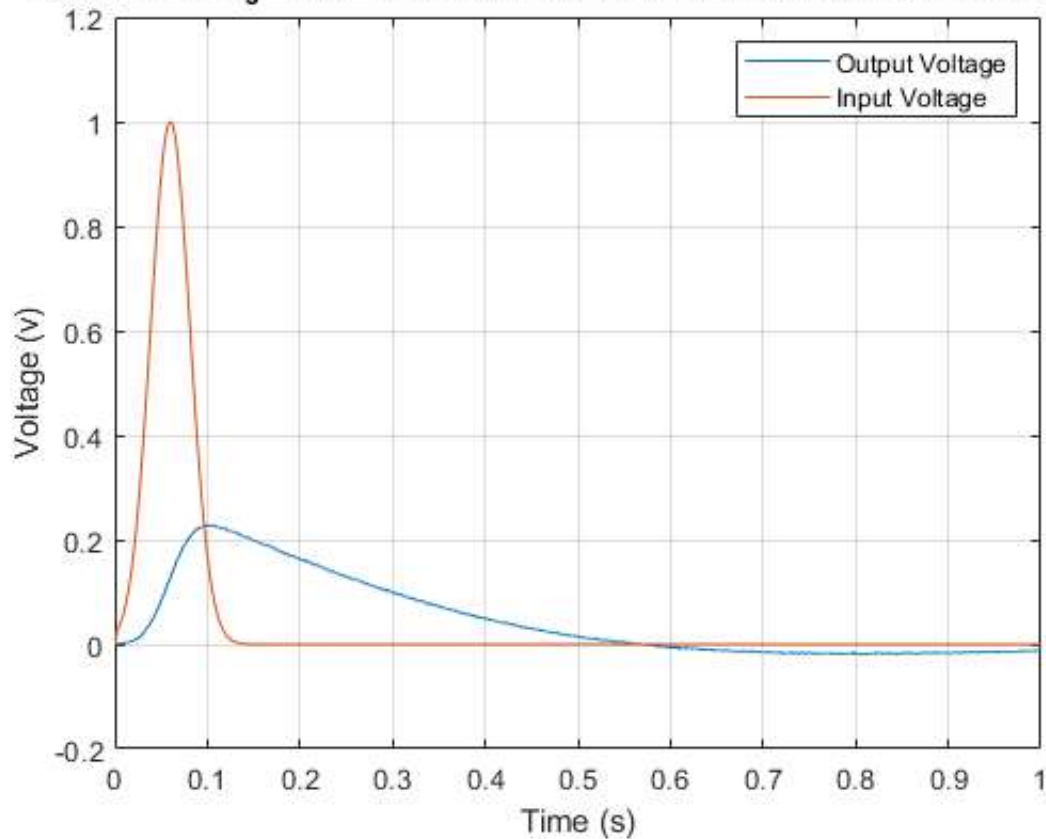
Question 3 $CN_2 = 0.0001$ Gaussian Pulse Function with Added Noise Source



Question 3 $CN_2 = 0.0001$ Gaussian Pulse Function (With Noise) Fourier Transfor



Question 3 $CN_3 = 0.01$ Gaussian Pulse Function with Added Noise Source



Question 3 $CN_3 = 0.01$ Gaussian Pulse Function (With Noise) Fourier Transform

