



CSE 2312: Computer Organization & Assembly Language Programming Fall 2017

Homework #2

Student Name: _____

Student ID: _____

Directions: Answer the questions on the following pages. Show all applicable steps for any problems requiring the use of formulas or calculations. Submit your completed assignment electronically as a single PDF document with this completed coversheet as the first page and your name written at the top of all additional pages. You may also submit the document in person before the deadline, in which case this coversheet must be completed and stapled to your solution pages.

NOTE: Refer to the MIPS Reference Data Card for all questions on this assignment.

1. Consider the following C statement:

$$f = (g - 7) + (h + 79);$$

- a) Assuming g and h are stored in registers $\$t0$ and $\$t1$, and f is to be stored in register $\$s0$, assemble the statement into a sequence of MIPS instructions.
- b) Give the machine code representation (binary) of your answer to part a.

2. Assume the following register contents (hex value):

$\$t0 = 0x00FF00FF$, $\$t1 = 0xABCD1234$

- a) What is the value of $\$t2$ after the following statements are executed?

```
sll $t2, $t0, 7
or  $t2, $t2, $t1
```

- b) What is the value of $\$t2$ after the following statements are executed?

```
sll  $t2, $t0, 3
andi $t2, $t2, -9
```

- c) What is the value of $\$t2$ after the following statements are executed?

```
srl  $t2, $t0, 6
andi $t2, $t2, 0x0000EEAD
```

3. Translate the following MIPS code into C. Let integers x , y , and z be stored in $\$a0$, $\$a1$, and $\$a2$, respectively.

```
fun:  add $t0, $a0, $a1
      sub $t0, $t0, $a2
      add $v0, $t0, $zero
      jr  $ra
```

4. Consider the following MIPS code:

```
LOOP:    slt  $t2, $zero, $t1
         beq  $t2, $zero, DONE
         subi $t1, $t1, 1
         addi $s2, $s2, 3
         j    LOOP
DONE:
```

- a) Assume that register \$t1 is initialized to the value 8 (in base 10), and \$s2 is initialized to zero. What is the value of \$s2 after executing the code segment above?
- b) Convert the MIPS instructions above to C code. Assume that registers \$s2 and \$t1 contain the variables *B* and *i*, respectively.
- c) How many MIPS instructions would be executed if the register \$t1 was initialized to the value *N*?

5. Translate the following C code to MIPS assembly. Assume that the values of *a*, *b*, *i*, and *j* are in registers \$s0, \$s1, \$t0, and \$t1, respectively. Also assume that \$s2 holds the base address of the array D.

```
for(i = 0; i < a; i++)
    for(j = 0; j < b; j++)
        D[2 * j] = i + j;
```

6. Write a series of MIPS assembly instructions to implement the following: “If the 8th bit of register \$s0 is 1, then add 2 to register \$s1; else set the 8th bit of register \$s0 to 1.”