Left connector panel (top to bottom):

PL 10
PL 2 — SKT 9
SKT 7
PL 6
SKT 5 — SKT 12
PL 4 — SKT 11
PL 3 — PL 16
PL 15 — SKT 8
PL 1 — SKT 17

Main wiring table (left block):

No.	Description	No.
33 34 35	—	32 32
36 37 38	— B – S1 COMPUTER START	32 32 32
39	—	32
40	— A – HO PRIORITY SELECT	32 32
41	— A – HN CHANNEL SELECT	32
42	— A – HQ SELECT CHANNEL DRIVERS + J14–16 SELECTION	32
43	— A – HP TR REGENERATOR + SELECT CHANNEL MULTIPLEXING	32
44	— A – JF DATA TRANSMITTER (UNGATED)	32
45	— A – JF	32
46	— A – JD DATA TRANSMITTER (GATED)	32
47	— A – HU DATA RECEIVERS	32 32
48	— A – HU	32
49	— A – JC DATA AND ADDRESS INVERTER	32
50	— A – JH CONTROL SIGNAL INTERFACE	32
51	— A – HU DATA RECEIVERS	32
52	— A – JC DATA & ADDRESS INVERTERS	32
53	— A – JG CONTROL SIGNAL INTERFACE	32
54	— A – JB OUTPUT DATA INVERTERS AND TRANSMITTERS	32
55	— A – HU DATA RECEIVERS	32 32
56	— A – HU	32
57	— A – JK STORE SELECT	32
58	— A – EB3 STORE CONTROL	40
59	— A – EC3 GATE STROBE. MASTER DRIVERS	40
60	— A – ED3 WRITE GATES. READ DRIVERS. DECODE	40
61	— A – ED3 WRITE GATES. READ DRIVERS. DECODE	40
62	— A – ED3 WRITE GATES READ DRIVERS. DECODE	40
63	— A – ED3 WRITE GATES READ DRIVERS. DECODE	40
64	— A – ED3 WRITE GATES READ DRIVERS DECODE	40

Main wiring table (right block):

No.	Description	No.
65	— A – GZ TEMP. SENSING & CONTROL	32
66	— A – EA3 INHIBIT DRIVER & SENSE AMP.	40
67	— A – EA3 AMPLIFIERS & INHIBITS	40 40
68	— A – EA3 AMPLIFIERS & INHIBITS	40
69	— A – EA3 AMPLIFIERS & INHIBITS	40 40
70	— A – EA3 AMPLIFIERS & INHIBITS	40
71	— A – EA3 AMPLIFIERS & INHIBITS	40

No.	Description	No.
72	— A – AE SELECTION DIODES	40 40 40
73	— A – AE SELECTION DIODES	40
74	— A – AE SELECTION DIODES	40 40
75	— A – AE SELECTION DIODES	40
76	— A – AE SELECTION DIODES	40 40
77	— A – AE SELECTION DIODES	40

Unit layout (center right):

X1 — READ OUT
X2
X3 — Y1
X4 — Y2
X5 — Y3
X6 — INHIBIT

VIEW ON BACK WIRING SIDE

B16

Right column:

SKT5 POWER TO NEXT ADDITIONAL STORE PL6

SKT7 DISPLAY UNIT PL2

SKT8 DATA TO NEXT ADDITIONAL STORE PL1

SKT9 DATA TO NEXT ADDITIONAL STORE PL2

SKT11 STORE SELECTION CONTROL TO FURTHER STORE PL4

SKT17 RETURN DATA FROM NEXT STORE PL15

PL1 DATA IN FROM COMPUTER OR PREVIOUS STORE SKT2

PL2 DATA IN FROM COMPUTER OR PREVIOUS STORE SKT9

PL3 DATA IN FROM COMPUTER PERIPHERAL OR PREVIOUS STORE SKT8

PL4 STORE SELECTION CONTROL FROM COMPUTER OR PREVIOUS STORE —

PL6 POWER IN FROM POWER UNIT OR PREVIOUS STORE SKT3

PL10 DATA IN FROM PERIPHERAL UNIT SKT5

SKT12 MARGINAL TEST UNIT SKT1

PL15 RETURN DATA & CONTROL SIGNALS TO COMPUTER OR PREVIOUS STORE SKT14

PL16 RETURN DATA & CONTROL SIGNALS TO COMPUTER OR PREVIOUS STORE SKT16