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33		c	22	-		A C	7 70	MD ccalera	IG & CONTRO	L S
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35 3	_	B-SI POWER ON/OFF	9 29						IER & SENSE AN	
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46	-	A-JC DIPLEXING & INVERTING S A-JJ LINK BOARD	3		Na-	Х3				)
20	-	A-JJ LINK BOARD	3				7		Y2	
3	_	A-HXLINK BOARD S			_		J			.
25	-	A-HT DIPLEXING & INVERTING				x4				/
23	_	A-HY RECEIVERS & TRANSMITTERS					7		Y3	
54	-	A-HS OUTPUT DATA TRANSMITTER		1	_	X5	,			
55	_	A-HR INPUT RECEIVER			× (-	1000	,			, 1
99	-	A-HR OUTPUT RECEIVER			1.1		/			/
25	-	A - HK STORE SELECT LOGIC				×6			INHIBIT	- 1
28	_	A - EB3 STORE CONTROL								
69	<b>→</b>	A - EC3 GATE STORE MASTER DRIVERS	_	-		A - A1	c c	ELECTION	L DIODES	0
909	_	A - EDB WRITE GATES READ DRIVERS DECODE	-			A - A		ELECTION		9
9 19		A - ED3 WRITE CATES READ DRIVERS DECODES	-	-		- 1.4			****	40 40
62 b	-	TACTOR TO SERVICE STATE OF THE	4	-		A - A			DIODES	4
63 6	=	A - ED3 WRITE GATES READ DRIVERS DECODES	-	F		A - A		ELECTION		40
64 6		A - ED3 WRITE GATES READ BRIVERS DECODES	-	-		A - A	**	ATTEM ALA	DIODES	40 40
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EXTRA STORE 8192 WD. 18 BIT WITHOUT STORE ACCESS CONTROL CONNECTOR & UNIT LAYOUT