673 SKT 7 هم) S<sub>T</sub> 직소 같은

-	35	54	-	A- GZ TEMP SENSING & CONTR	
-	26	38	_	A - EAS INHIBIT DRIVER & SENSE	-
-	B - SI COMPUTER START %	1.0	-	A - EAS AMPLIFIERS & INHIBIT	
_	8	3		A - EAS AMPLIFIERS INHIBIT	is 8
÷	* 8	39	-	A - EAS AMPLIFIERS & INHIBIT	is 9
-	32	뭐	-	A - EASAMPLIFIERS & INHIBI	TS &
-	32	72		A - EA3 AMPLIFIERS & INHIB	175 ₹
_	A - HO PRIDRITY SELECT S				
-	A - HN CHANNEL SELECT #	1			
-	A - HA SELECT CHANNEL DRIVERS N			( Z	7
_	A - HP TR REGENERATOR + SELECT OF			READ OU	T
-	A - JF DATA TRANSMITTER(UNGATED)			×ı	
-	A - JF %				
_	A - JD DATA TRANSMITTER (GATED) &			XZ	
_	A - HU BATA RECEIVERS 8			~~	7
-	A - HU %	6			
	A - JC DATA AND ADDRESS INVERTER S				7
-	A - JH CONTROL SIGNAL INTERFACE %			TS.	J
+	A - HU DATA RECEIVERS S				_
-	A - IC DATA & ADDRESS LAVERTERS SI		1	360	J
Ļ.	A - JG CONTROL SIGNAL INTERFACE S			73	
_	A _ TR CUTPUT DATA INVERTERS OF			<del>×5</del>	
_	A - HU DATA RECEIVERS %				$\overline{}$
_	A - HU - A		l		J
_	A - JK STORE SELECT %			X6 INHIBIT	
_	A - EB3 STORE CONTROL 4		-		2
	A - EC3 GATE STROBE, MASTER DRIVERS	22	-	A - AE SELECTION DIODES	00.00
_	A - ED3 WRITE CATES, READ DRIVERS, DECODES	55	_	A - AE SELECTION DIDDES	
<del>-</del>	A - ED3 WRITE GATES, READ BRIVERS, DECODES	7	-	A - AE SELECTION DIODES	5
	A - ED3 WRITE GATES READ DRIVERS DECORES	75.7	_	A - AE SELECTION DIDDES	
	A - EDB WRITE GATES READ DRIVERS DEODES	76		A - AE SELECTION DIODES	
	A - EDS WRITE GATES READ DRIVERS DECOMES	2	-	A - AE SELECTION DIODES	70

EXTRA STORE, 8192 WD, 18 BIT WITH STORE ACCESS CONTROL CONECTOR & UNIT LAYOUT.

SKTS POWER TO NEXT ADDITIONAL STORE PLG

SKT7 DISPLAY UNIT PL2

SKT8 DATA TO NEXT ADDITIONAL STORE PLI

SKTS DATA TO NEXT ADDITIONAL STORE PL2

SKTI STORE SELECTION CONTROL TO FURTHER STORE PL4

SKTIT RETURN DATA FROM NEXT STORE PLIS

DATA IN FROM COMPUTER OR PREVIOUS STORE SKT2

PL2 DATA N FROM COMPUTER OR PREVIOUS STORE SKT9

PL3 DATA IN FROM COMPUTER PERIPHERAL OF PREVIOUS STORE SKT8

STORE SELECTION CONTROL FROM COMPUTER OR PREVIOUS STORE -

POWER IN FROM POWER UNIT OR PREVIOUS STORE SKT 3

DATA IN FROM PERIPHERAL UNIT SKT9

SKTIZ MARCINAL TEST UNIT SKT1

RETURN DATA & CONTROL SIGNALS TO COMPUTER OR PREVIOUS STORE SKT14

RETURN DATA & CONTROL SIGNALS TO PLI6 COMPUTER OR PREVIOUS STORE SKTIG