

-4	A - FT CONTROL KEY INPUT \$	E -	SPARE	52	A- GZ TEMP SENSING CONTROL
J -	A - FL MANUAL CONTROL LOGIC #	25 -	A- FB WAVEFORM AMPLIFIERS	\$ 00 ±	A - EAS INHIBIT BRIVER & SENSE AMP.
n	A - FD PROGRAM INTERRUPT	35	A - GL INITIAL INSTRUCTIONS &	- 0.7	A - EAB INHIBIT DRIVER & SENSE AMP
r{	A - FC TIMING	36	A - GK INITIAL INSTRUCTIONS 1	25 42	A - E.A. INHIBIT DRIVER & SENSE AMP.
n [A - FR TIMING WAVEFORM DRIVERS	37	A - FA REGISTERS (BIT 18)	25 %	A - EAS INHIBIT DRIVER & SENSE AMP.
0 -	A - FI CONTROL FUNCTION DRIVE #	88 -	A - FA RECISTERS (BIT 17)	2 2 -	A - EAS INHIBIT DRIVER & SENSE AMP.
-	A - FH CONTROL MATRIX A &C. P	8	A - FA REGISTERS (BIT 16)	28 -	A - EASINHIBIT DRIVER & SENSE AMP.
D	A - FE CONTROL MATRIX AMPLIFIERS !	8-	A -FA REGISTERS (BIT 15)	25	one Terms to the
=	A - FB CONTROL MATRIX AMPLIFIERS 2 %	7 -	A FA RECISTERS (BIT 14)	24	
2 -	A - GJ CONTROL MATRIX (FN 15) %	4	A FA REGISTERS (BIT 13)	56	
3/-	A - GI CONTROL MATRIX (FN 14A) %	£ -	A- FA REGISTERS (BIT 12)	35	READOUT
-	A - GH.CONTROL MATRIX(FN 14) #	4 -	A- FA RECISTERS (BIT II)	32	×I
74-	A - GG.CONTROL MATRIX (FN 13) %	- 45	A-FA RECISTERS (BIT 10)	26	
-	A - GE CONTROL MATRIX (FN 12) &	40	A FK WAVEFORM DRIVERS Z	8	ײ
2 -	A - G.E. CONTROL MATRIX (FN 5.1.8) #	- 4	A - FJ WAVEFORM DRIVERS I	21	
9 -	A - GD.CONTROL MATRIX (FN 56.9) %	20	A FA REGISTERS (BIT 9)	22	\
-	A - GC.CONTROL MATRIX (FNS.2 10.1)	04 -	A - FA REGISTERS (BIT 8)	33	× 5
9 -	A - GB CONTROL MATRIX(FNS 0,13,4) %	2 -	A . FA RECISTERS (BIT 7)	24	(YZ
<u>-</u>	A - GA MATRIX (CONTROL CHAIN)	3 -	A - FA RECISTERS (BIT 6)	ရူ	×4
3 -	A - FG. CONTROL MATRIX ADDRESS DRIVER #	21 -	4 - FA REGISTERS (BITS)	22	
ú -	A - FF CONTROL MATRIX ADDRESS BRIVER	£2 1-	A FA REGISTERS (BIT4)	8	Y3
J -	A - FE FUNCTION DECODE TES! #	20 -	A FA REGISTERS (BIT 3)	ed ed	×5
3 -	A- FP PROCESS COUNTER (BD. 1) %	3	A FA REGISTERS, (BIT 2)	133	
5 -	A - FP PROCESS COUNTER (BU 2) %	3 -	A-FA REGISTERS (BITI)	8	Xb
0	A - FP PROCESS COUNTER (BD 3) %	15	A - FM STORE CONTROL COVERFLOW LOGI	C M	
6 -	* A - FV CABLE RECEIVERS 1 %	82	A - CB3 STORE CONTROL CHAIN UNIT	4	
N -	* A- FU CABLE TRANSMITTERS \$	55	A- EC3 MASTER DRIVERS	1 27	A - AE SELECTION DIODES
e -	A - FO CABLE RECEIVERS I	6	A - ED3 DRIVER & GATES	73	A - AE SELECTION DIODES
5 -	A FS CABLE RECEIVERS 2 %	- 42	A - EDB DRIVERS & CATES	4 × ~	A - AE SELECTION DIODES
Z -	A - FQ IMPUT, OUTPUT, CONTROL 3	29	A-EDS DRIVERS & CATES	140	A - AE SELECTION BIODES
ñ ~	A- FN CABLE TRANSMITTERS 1 %	40	A EDS DRIVERS & CATES	2 40	A - AE SELECTION DIODES
X -	A - FN CABLE TRANSMITTERS 2 %	7 -	A ED3 DRIVERS & CATES	3 1 -	A - AE SELECTION BIODES

VIEW ON BACK WIRING SIDE

* DNLY SUPPLIED WITH EXTRA STORE UNITS

MSC 920B COMPUTER
CONNECTOR AND UNIT LAYOUT