

Figure 1 (ISSUE 2)

900  
4.1.2.

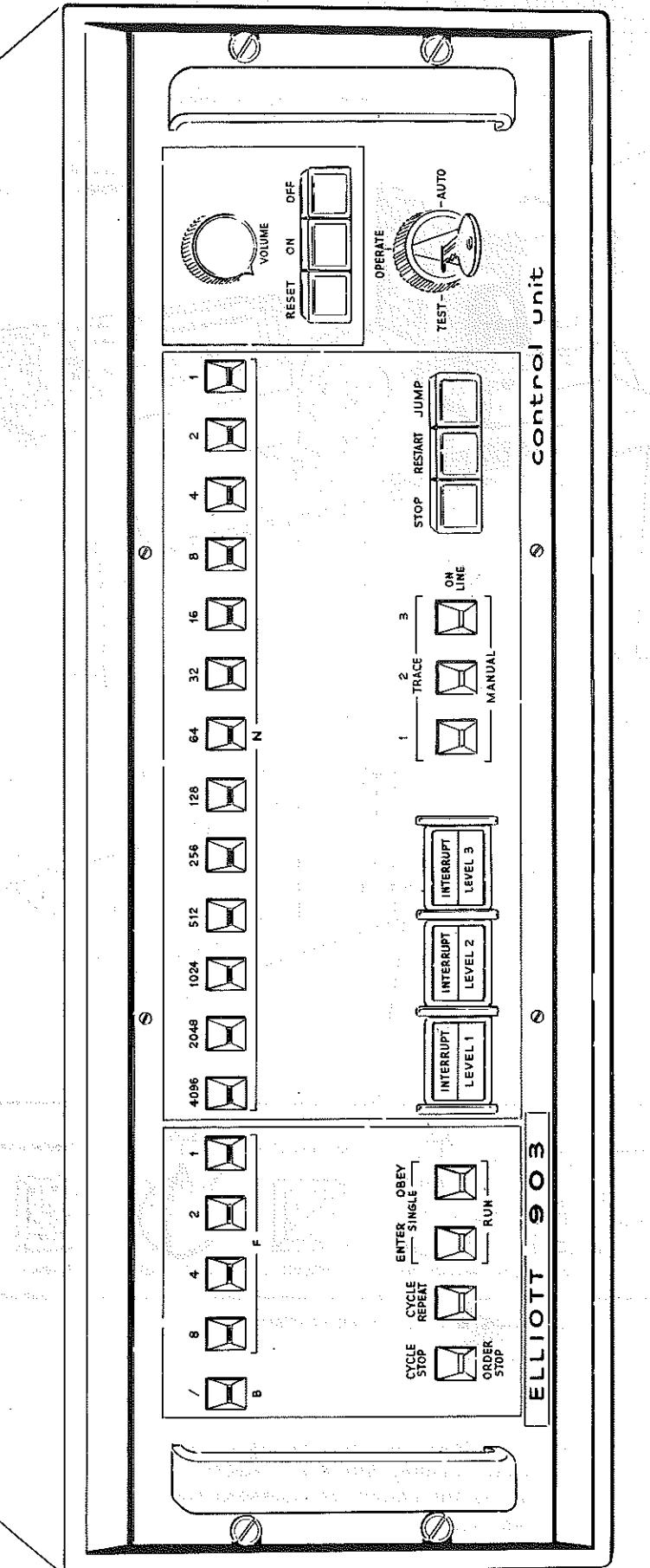
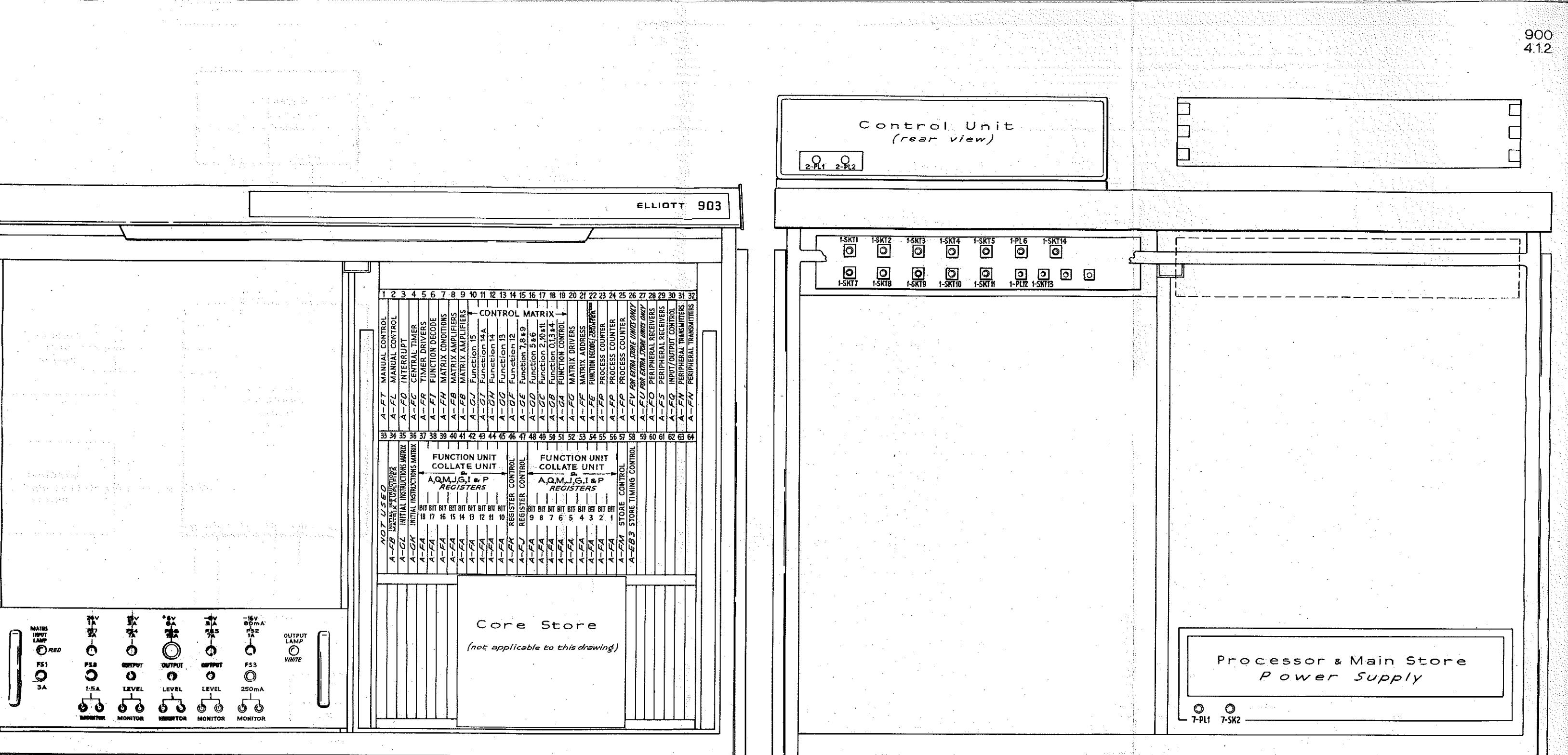


Figure 2

(Issue 2)

903 Control Unit



900  
4.1.2.

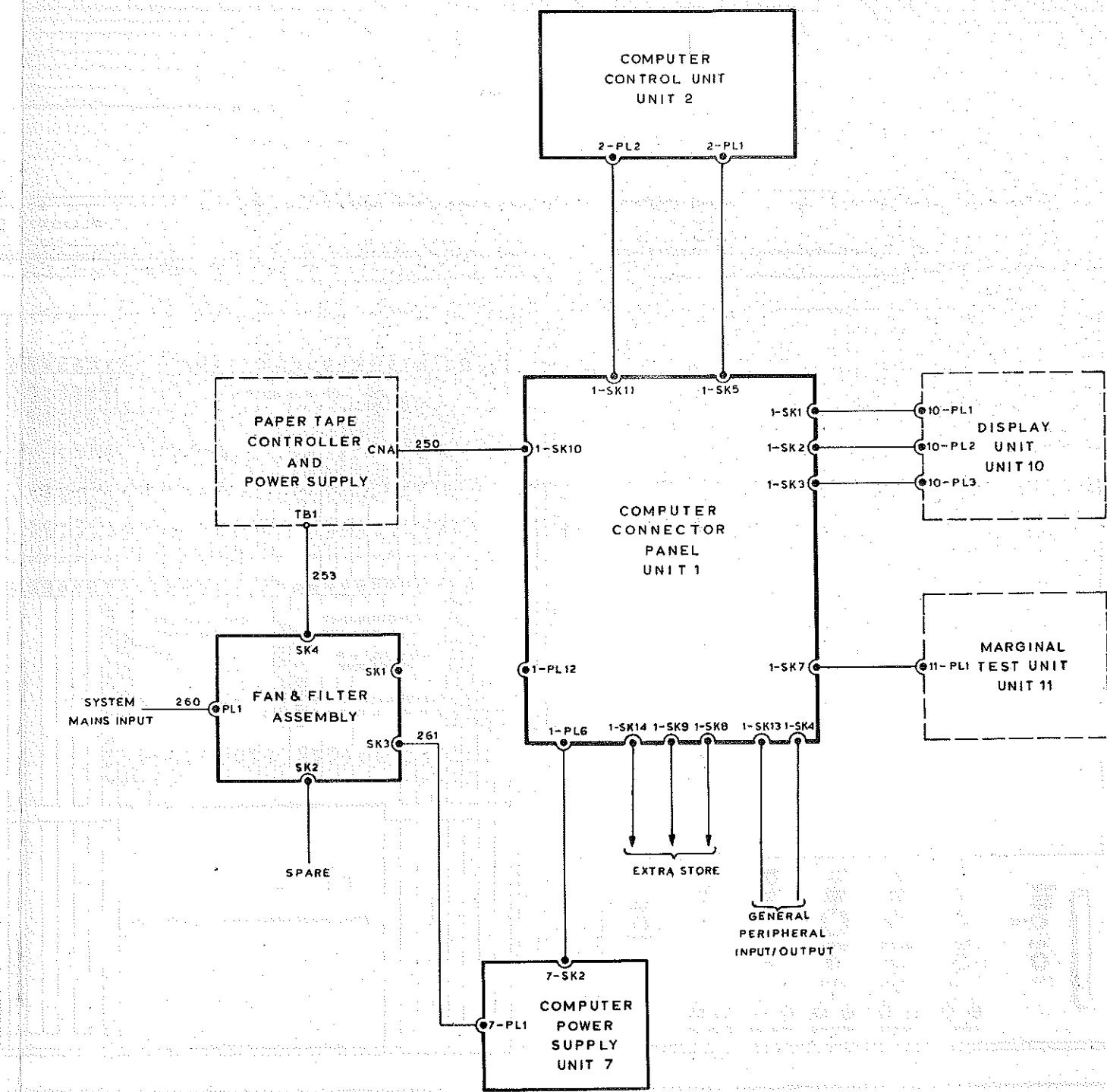
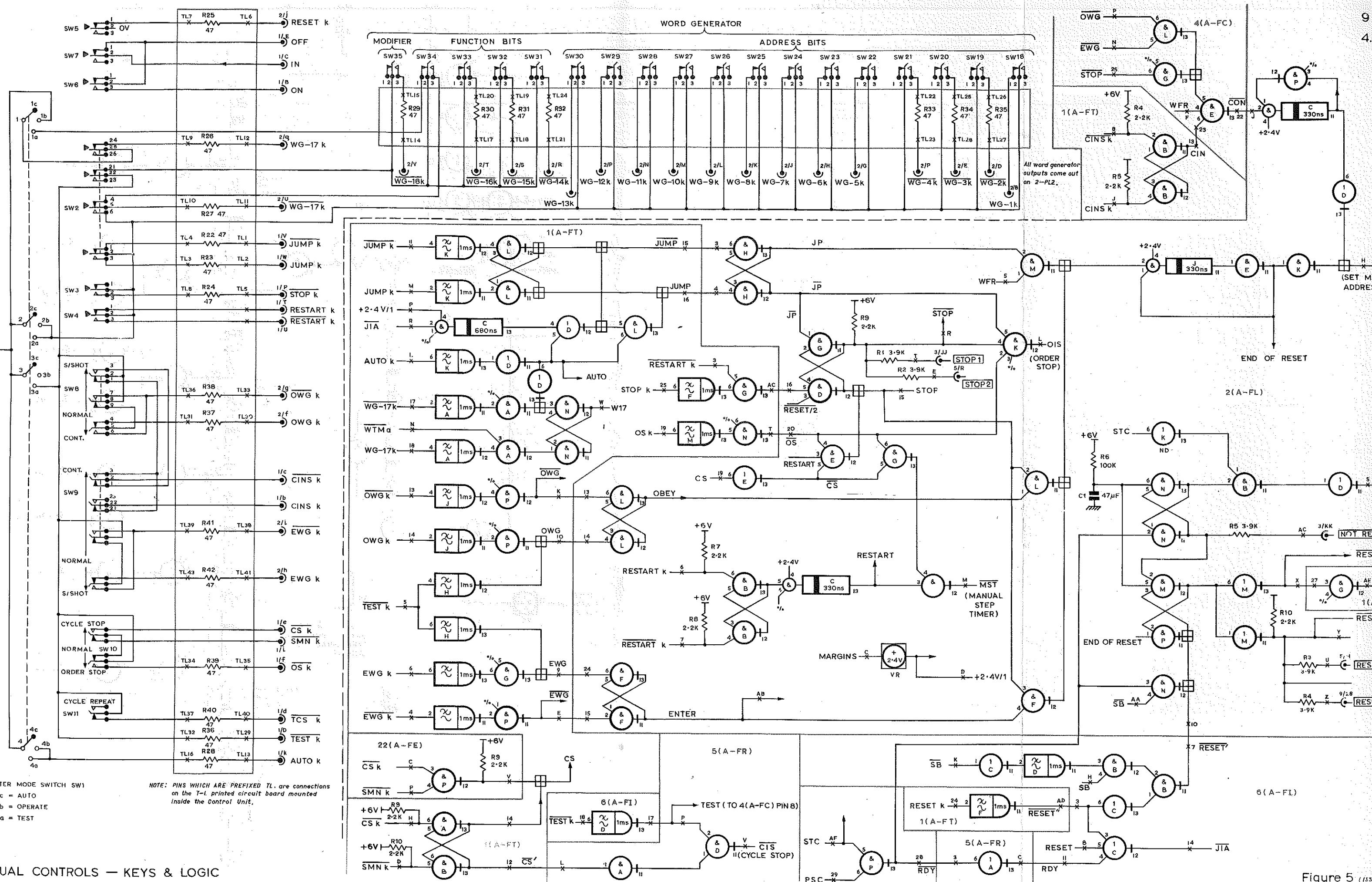
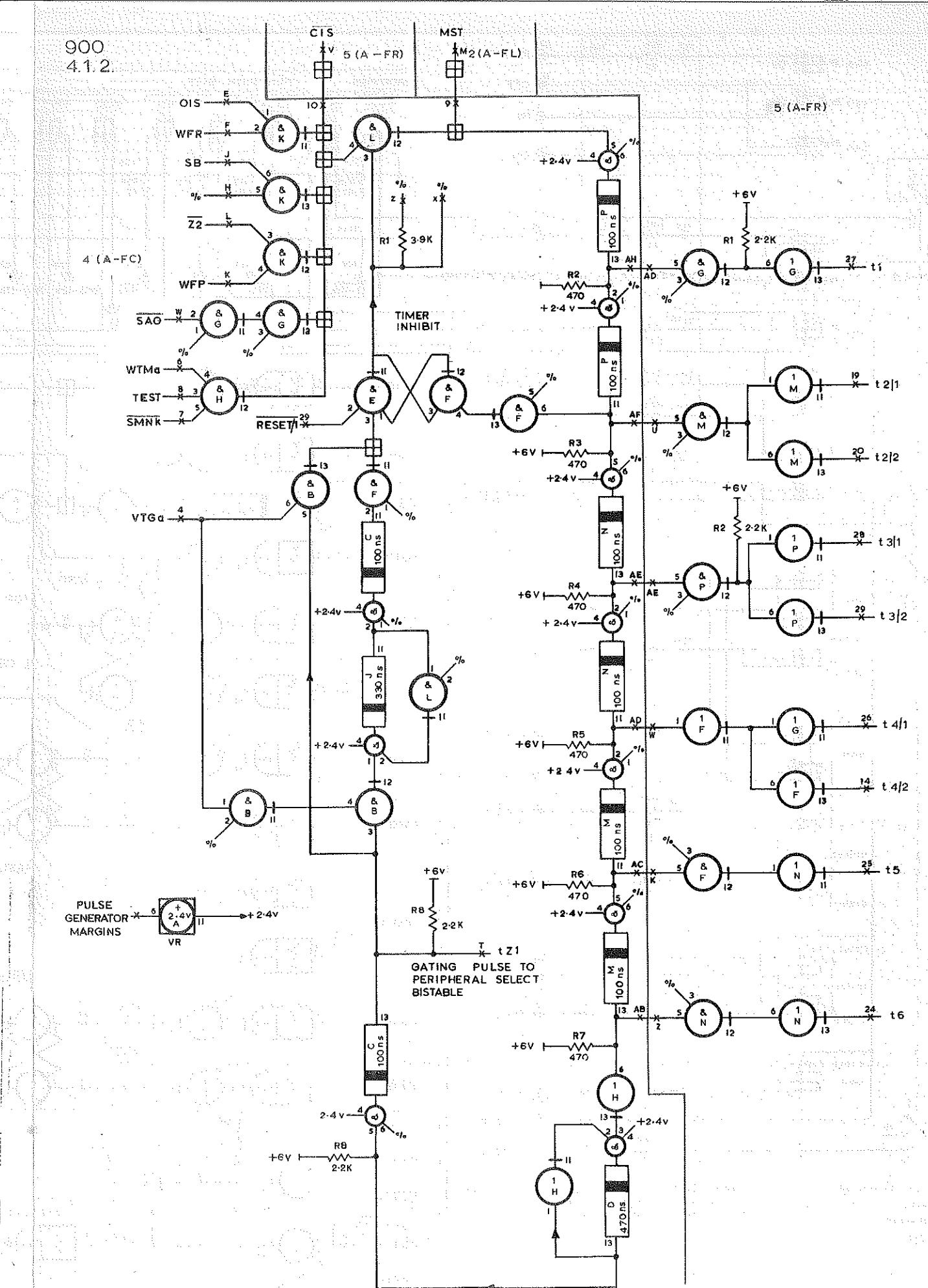
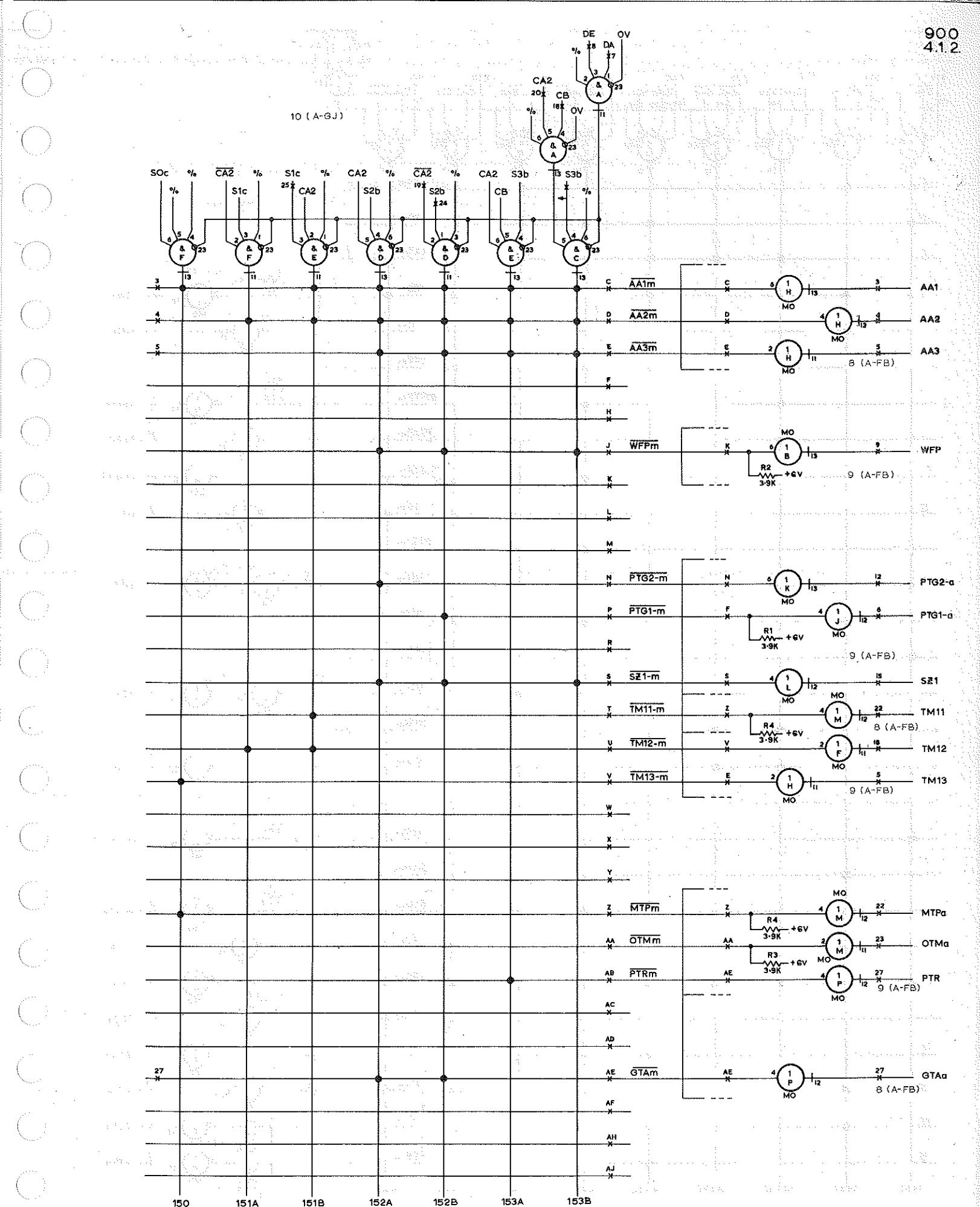


Figure 4 (ISSUE 2)



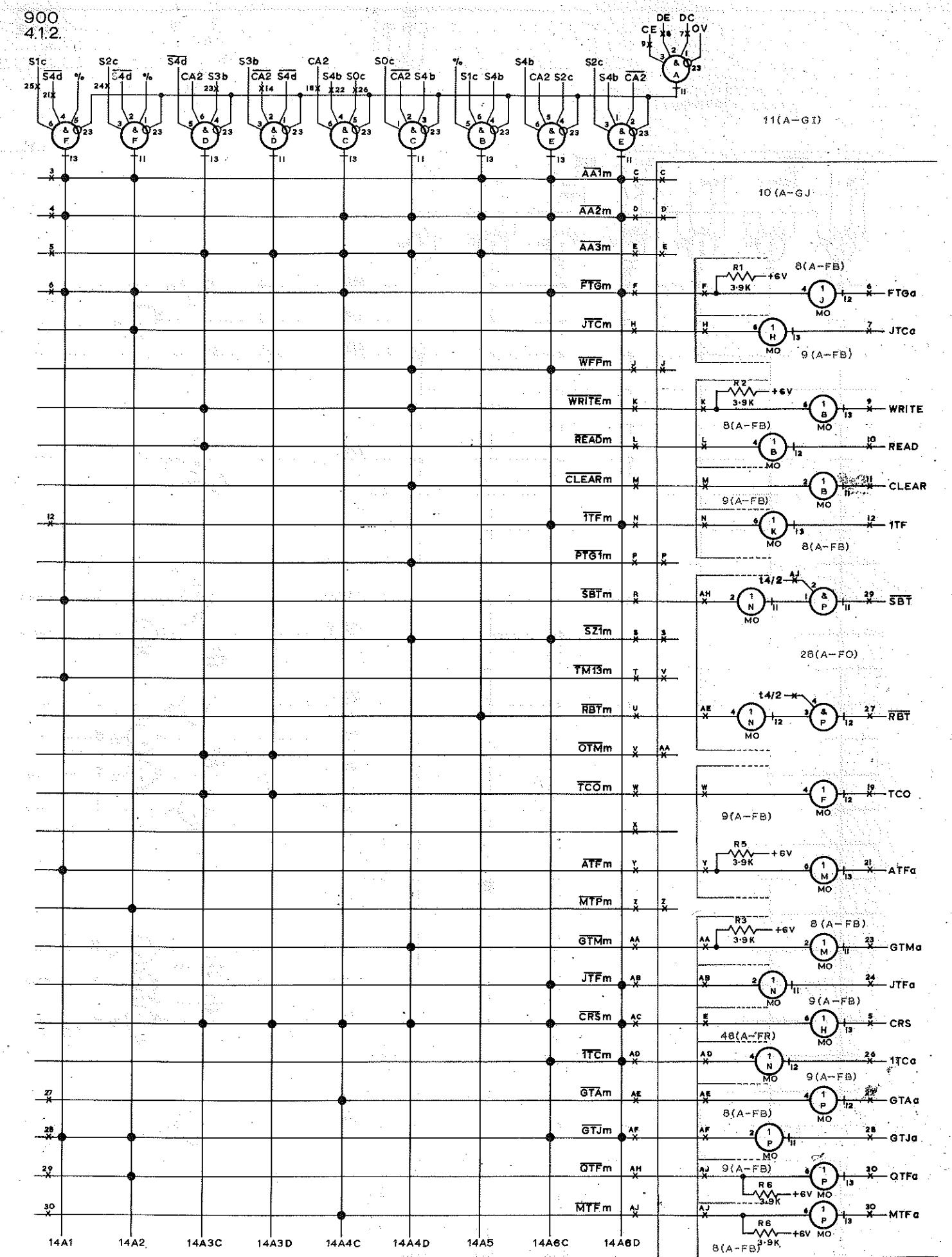
**Figure 5** (ISSUE 2)





CONTROL MATRIX FUNCTION 15

Figure 7 (ISSUE 2)



**Figure 8 (ISSUE 2)**

### CONTROL MATRIX FUNCTION 14A

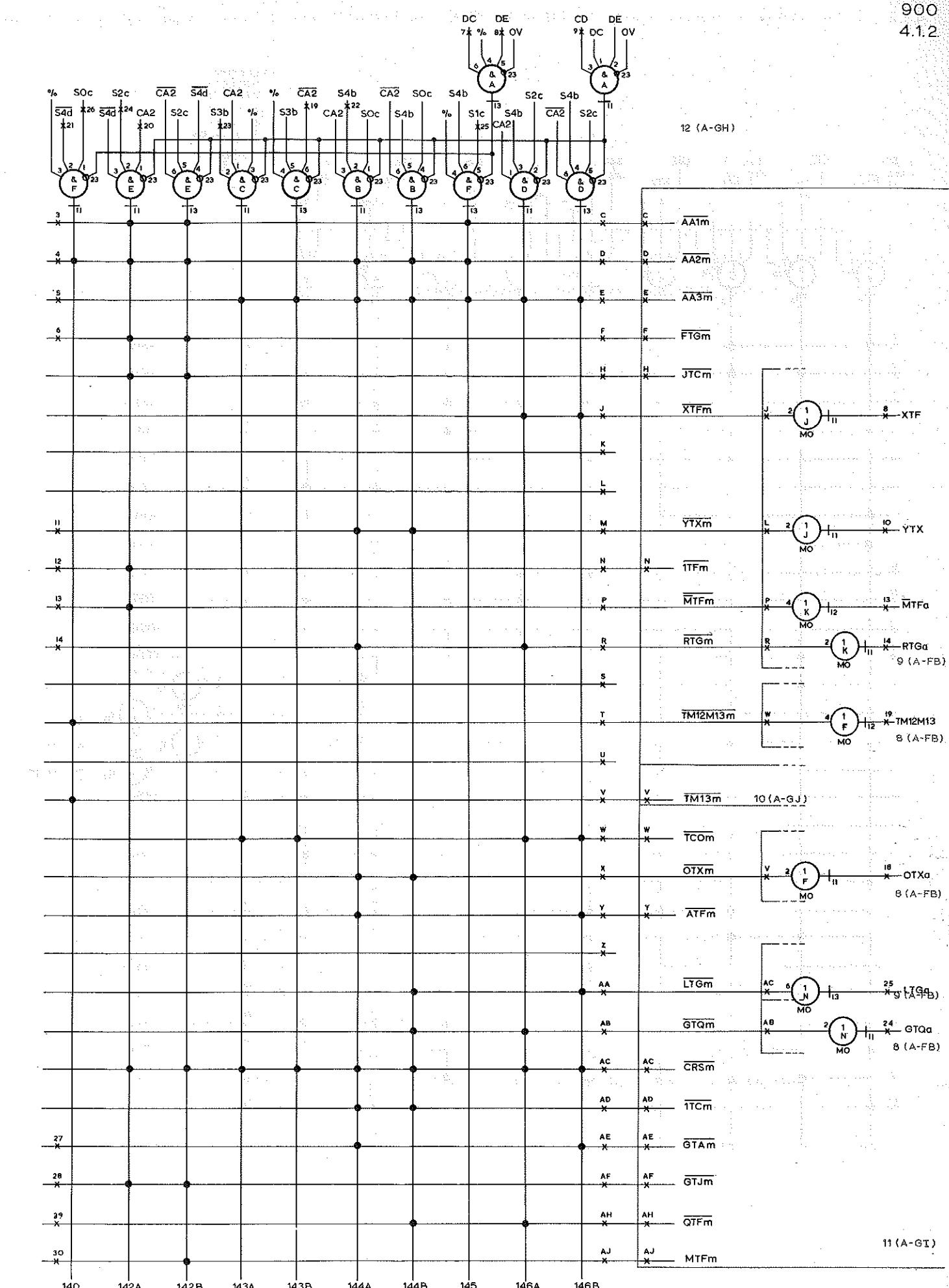


Figure 9 (ISSUE 2)

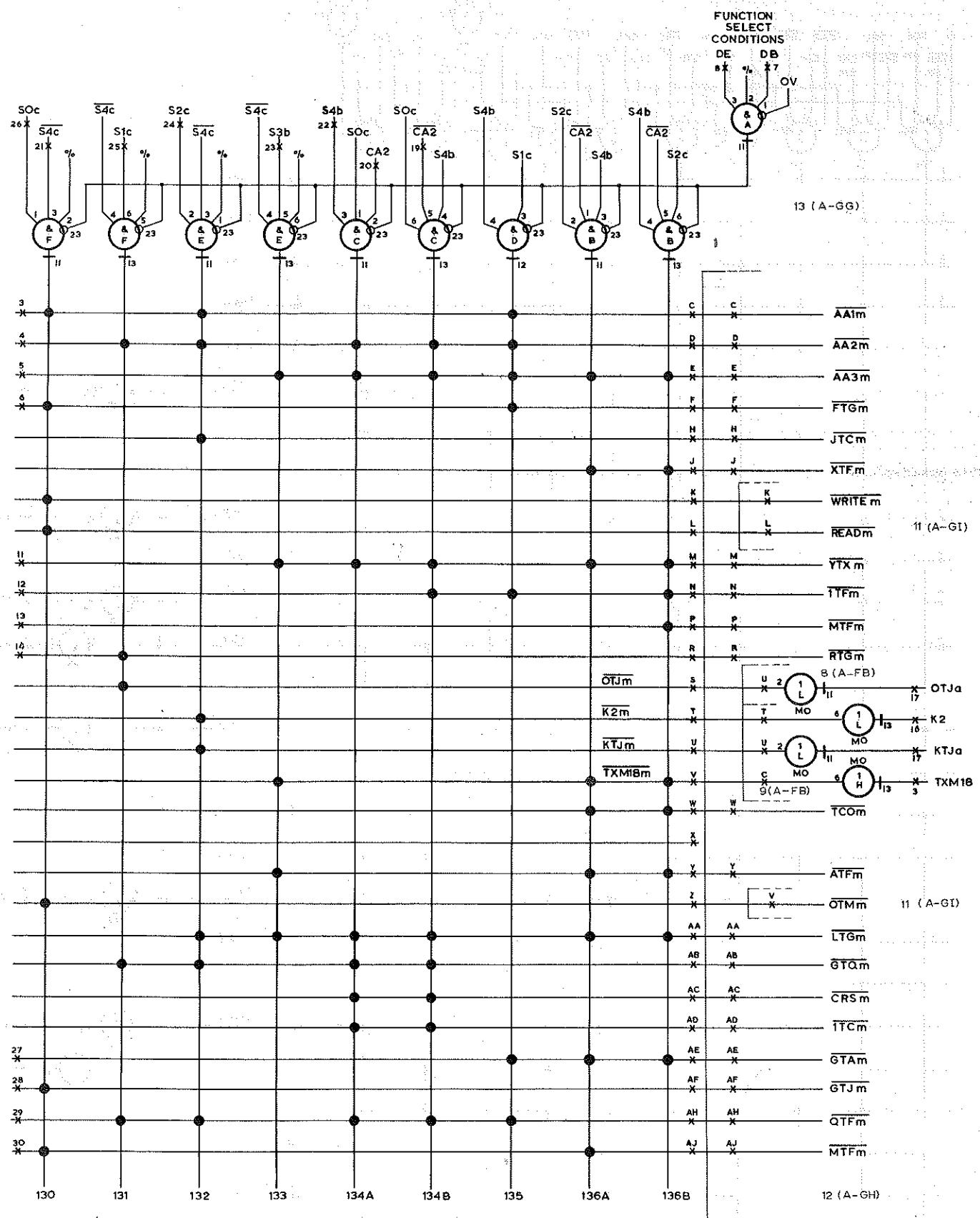
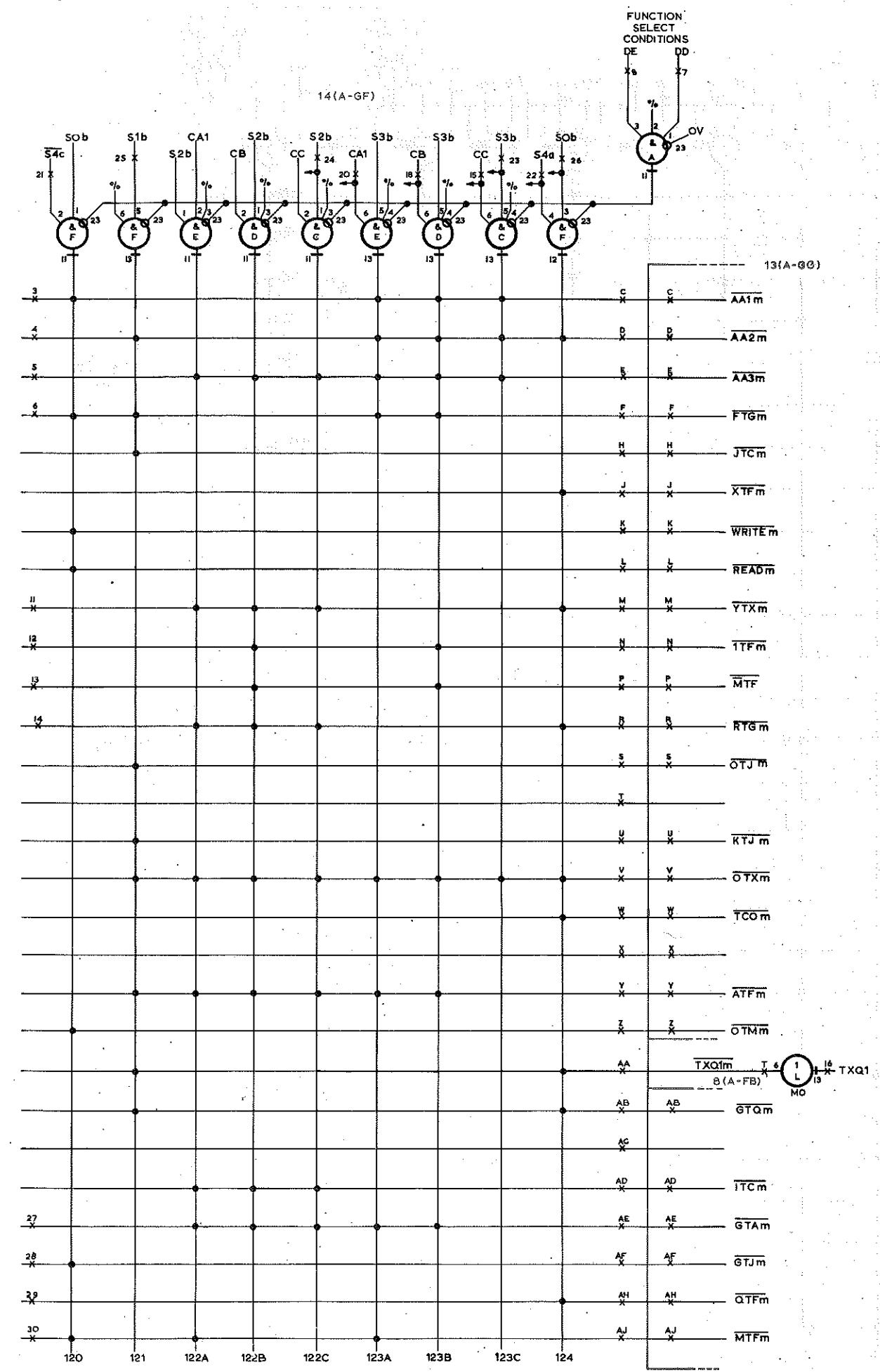
900  
4.1.2.

Figure 10 (ISSUE 2)



CONTROL MATRIX FUNCTION 12

Figure 11 (Issue 2)

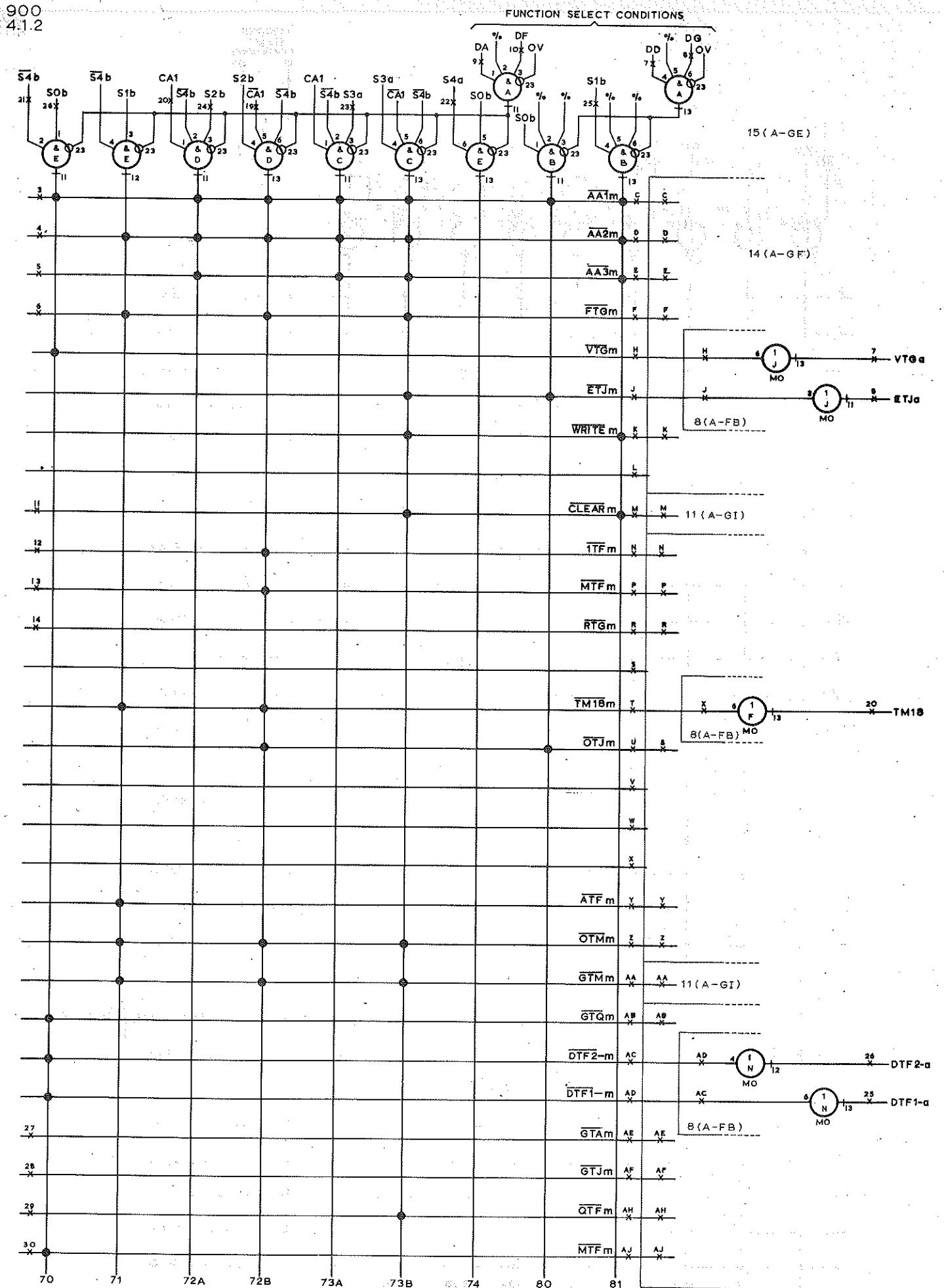
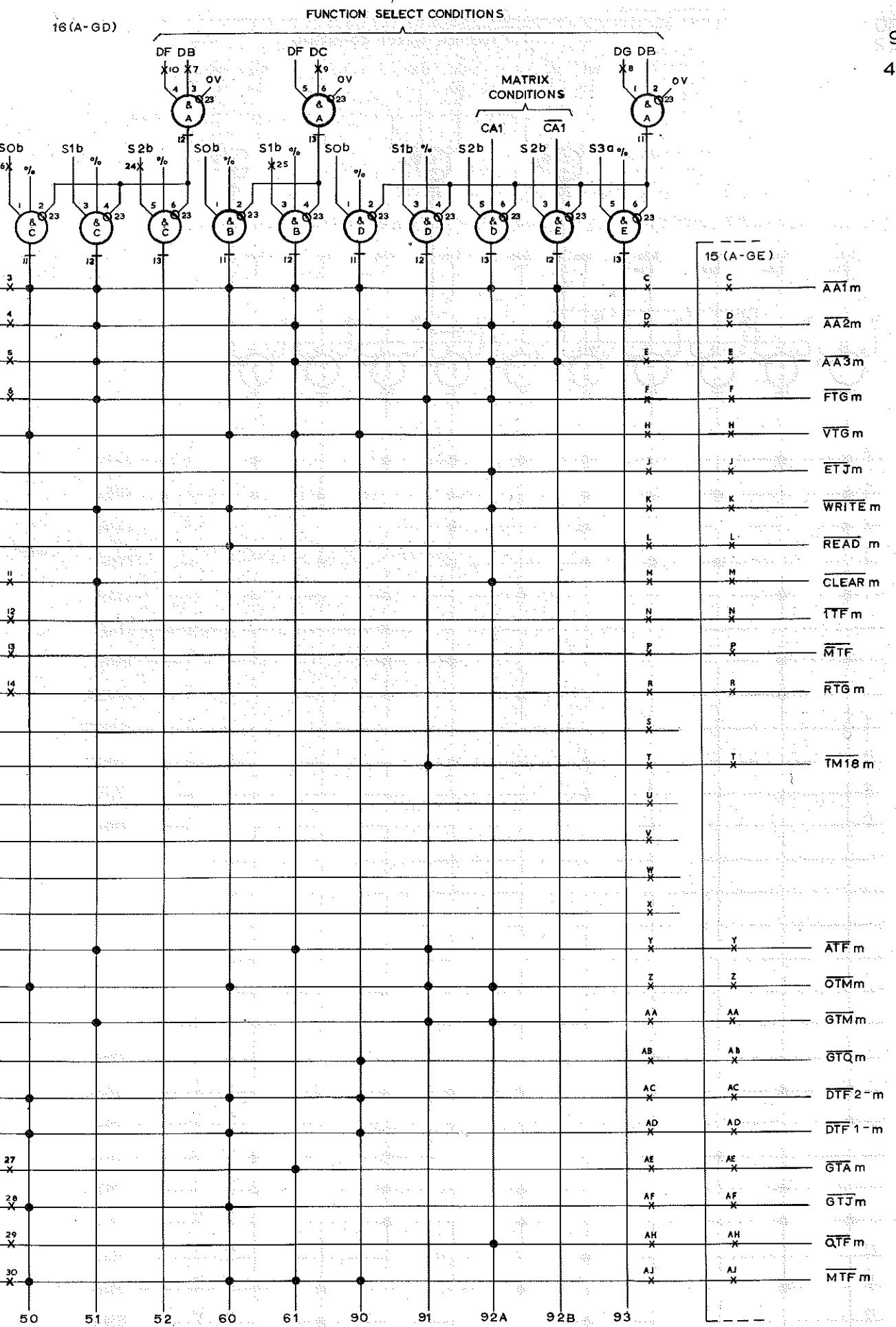


Figure 12 (ISSUE 2)



CONTROL MATRIX FUNCTIONS 5,6 &amp; 9

Figure 13 (ISSUE 2)

900  
4.1.2.

## FUNCTION SELECT CONDITIONS

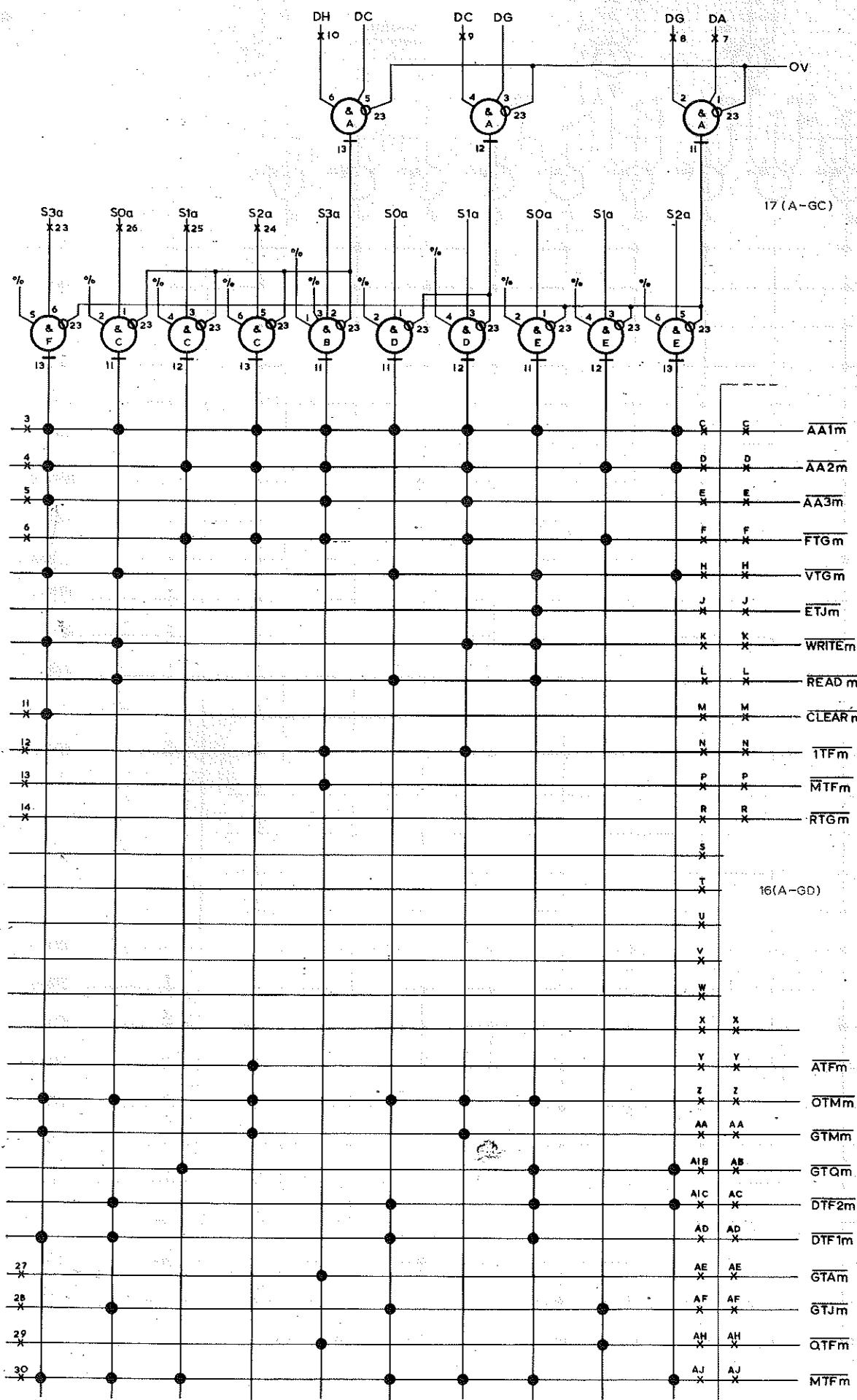
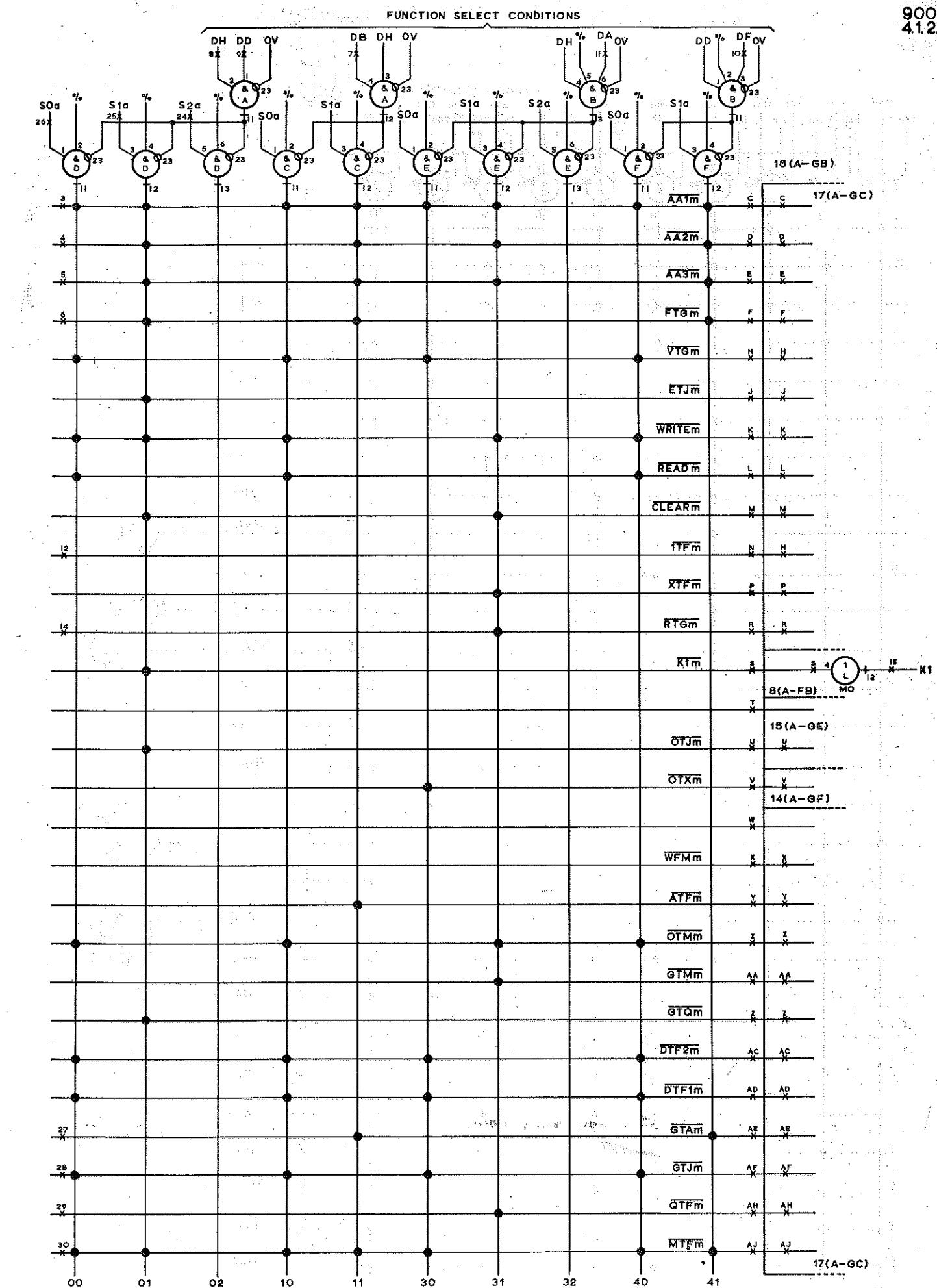


Figure 14 (ISSUE 2)



CONTROL MATRIX FUNCTIONS 0,1,3,4.

Figure 15 (ISSUE 2)

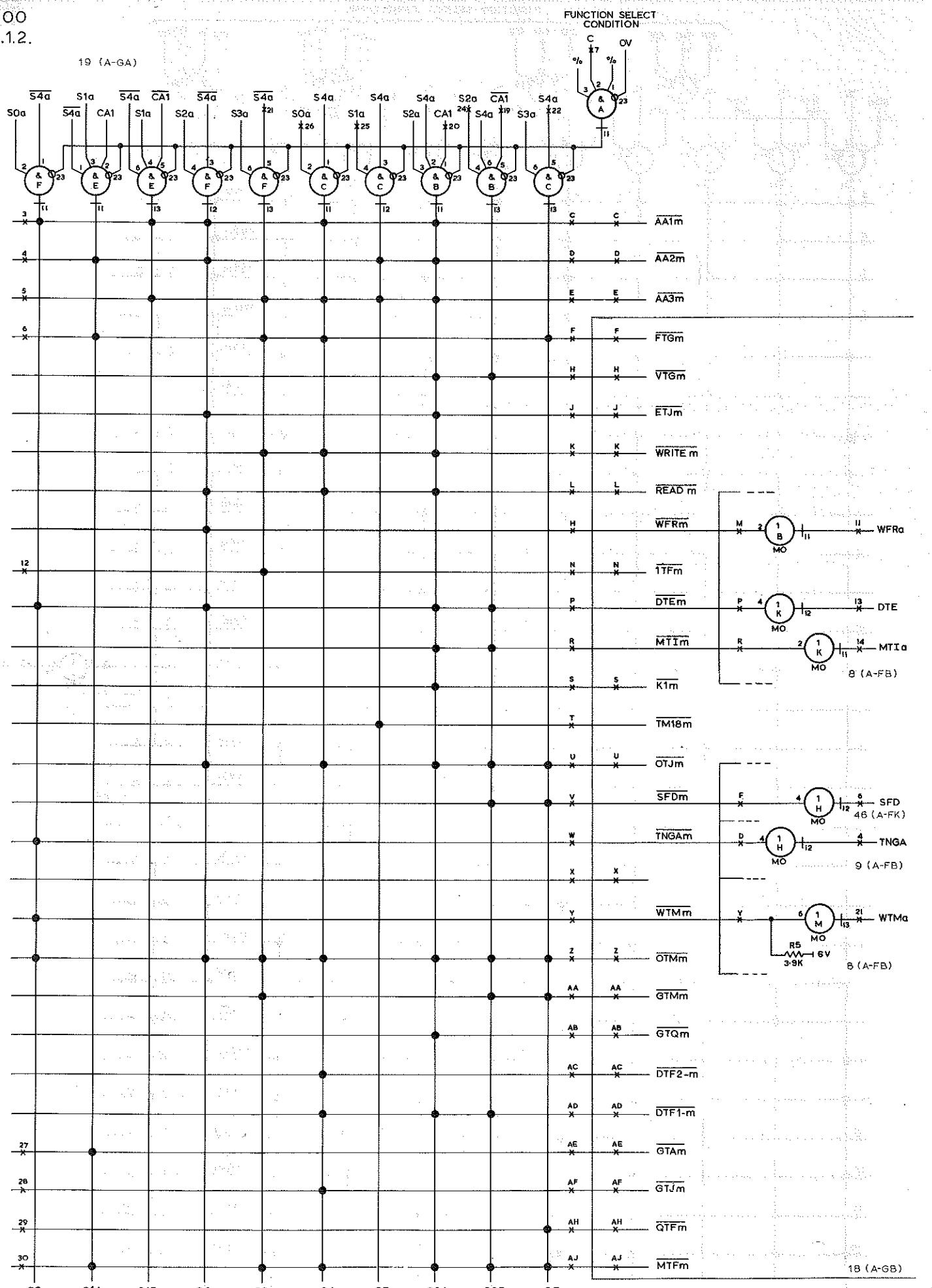


Figure 16 (ISSUE 2)

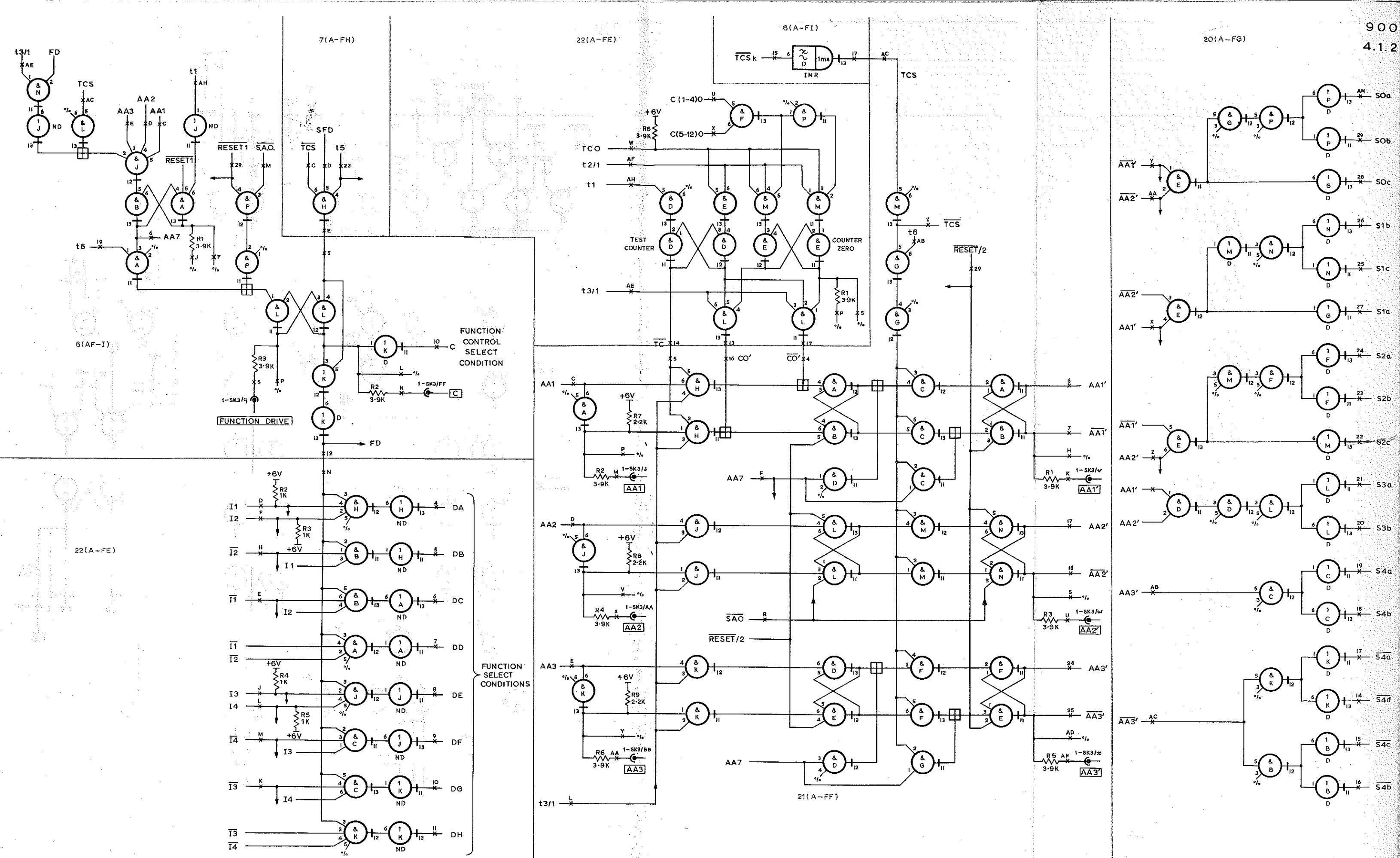


Figure 17 (ISSUE 2)

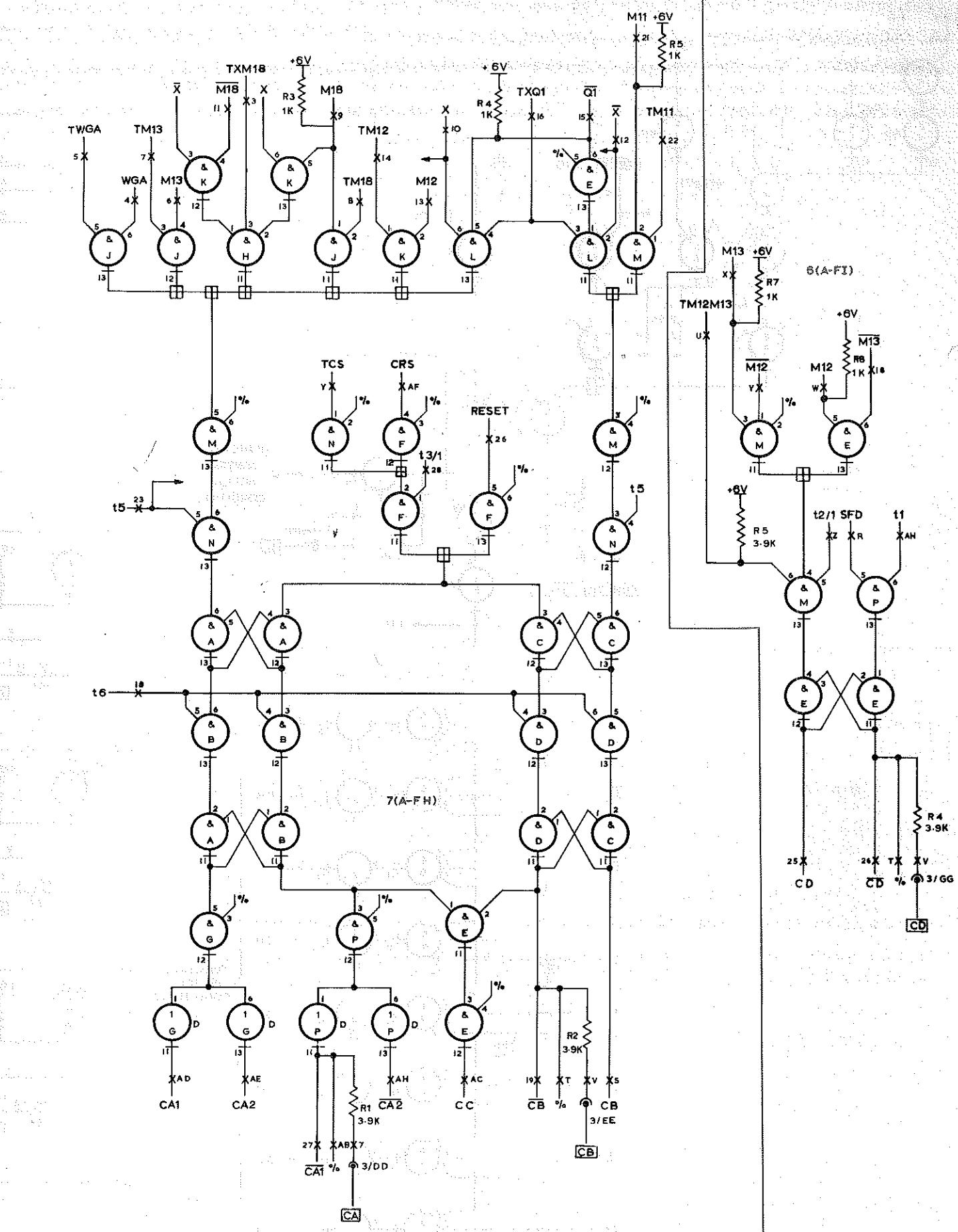
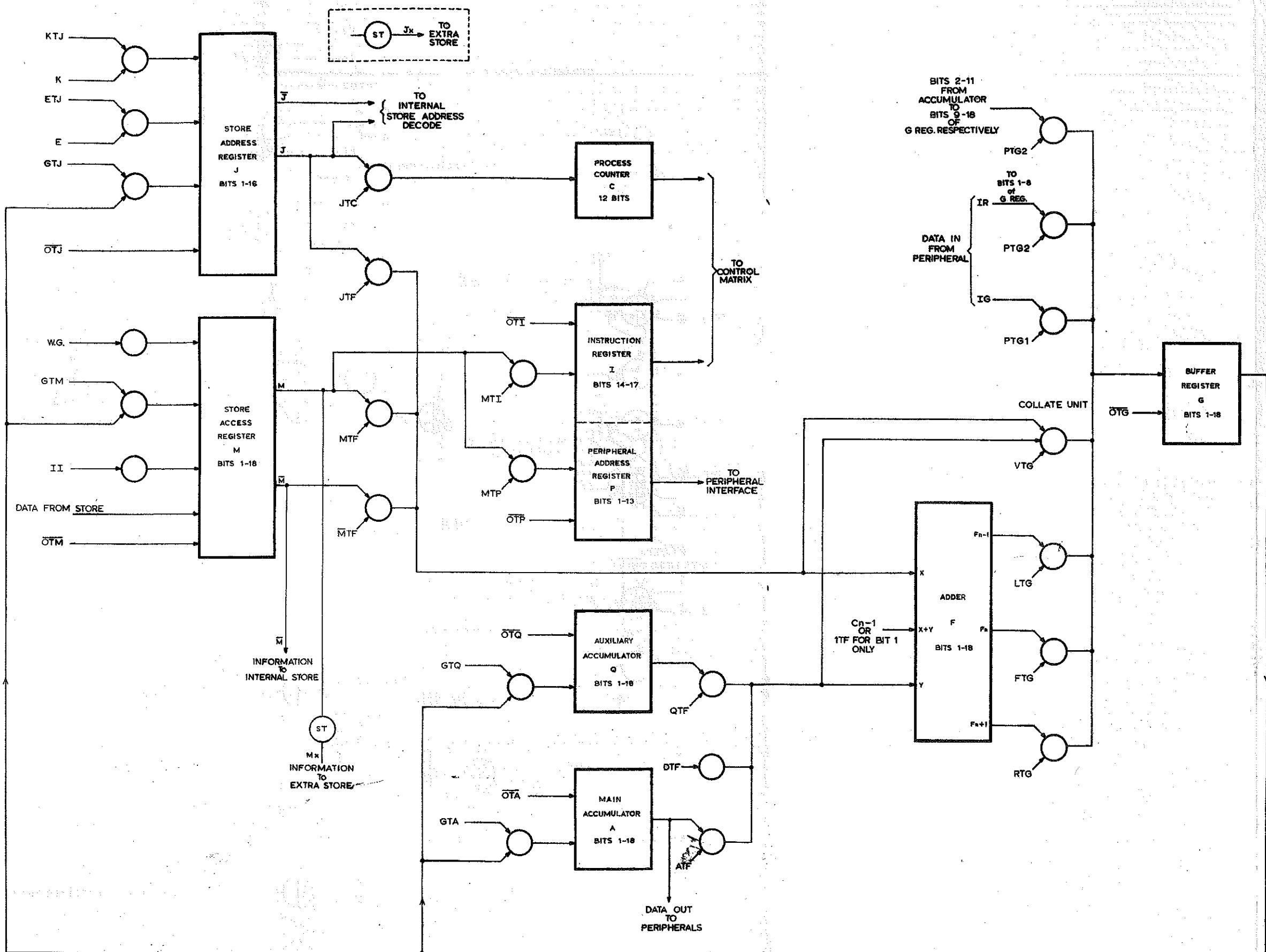
900  
4.1.2

Figure 18 (ISSUE 2)

MATRIX CONDITIONS



TITLE	DESCRIPTION
OII, OTI etc	All waveforms like these are register clear waveforms.
DTF	This waveform is divided into DTF/1 and DTF/2 and is used in conjunction with the collate unit. For more details see (FIG. 33 a, b & c).
II	INITIAL INSTRUCTIONS bits from MATRIX AMPLIFIERS. See (FIG. 38b).
W.G.	WORD GENERATOR
I.G.	This is an 18 bit data input from any peripheral device other than the paper tape reader.
I.R.	This is a 5,7 or 8 bit data input from the paper tape reader.
K	K is two waveforms K1 and K2 which are used to set up Process Counter for Mult. or Div. and K1 With E1 and E2 set up B-register address.
E	E is two waveforms E1 and E2 which are used to determine which SCR address or with K1 which Reg. address shall be selected according to the level of interrupt.

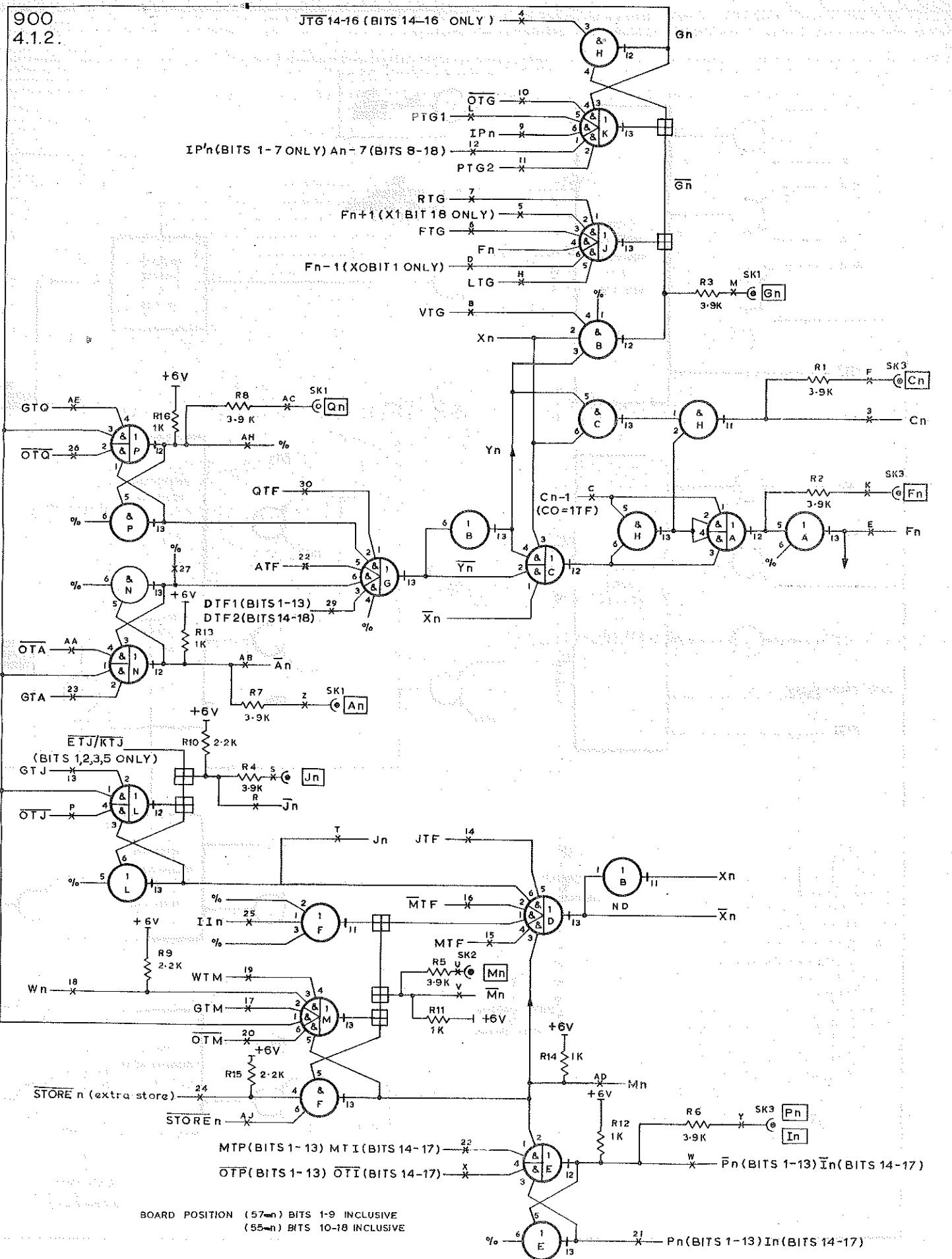


Figure 20 (ISSUE 2)

SAMPLE BIT OF REGISTERS AND FUNCTION UNIT

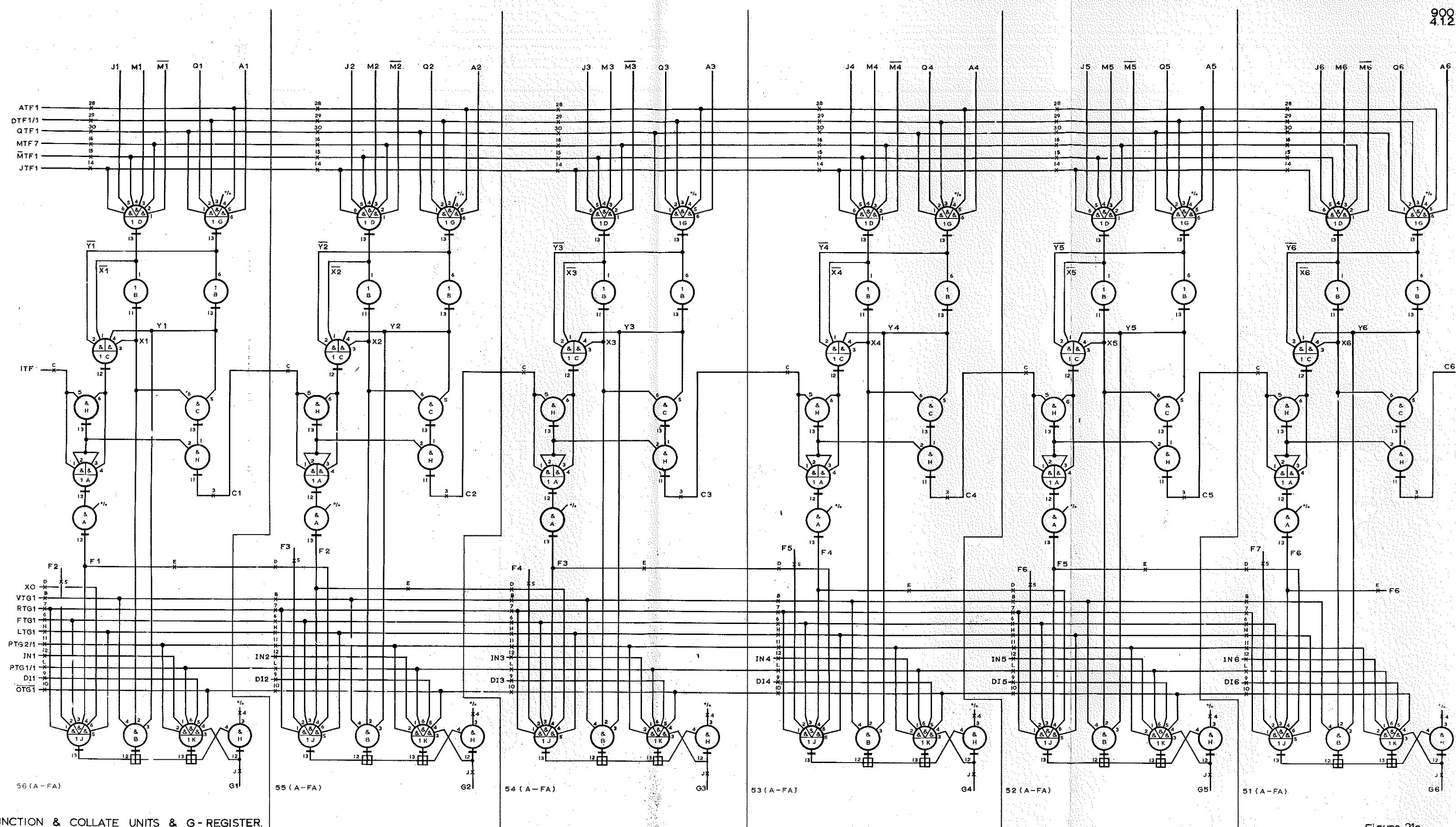
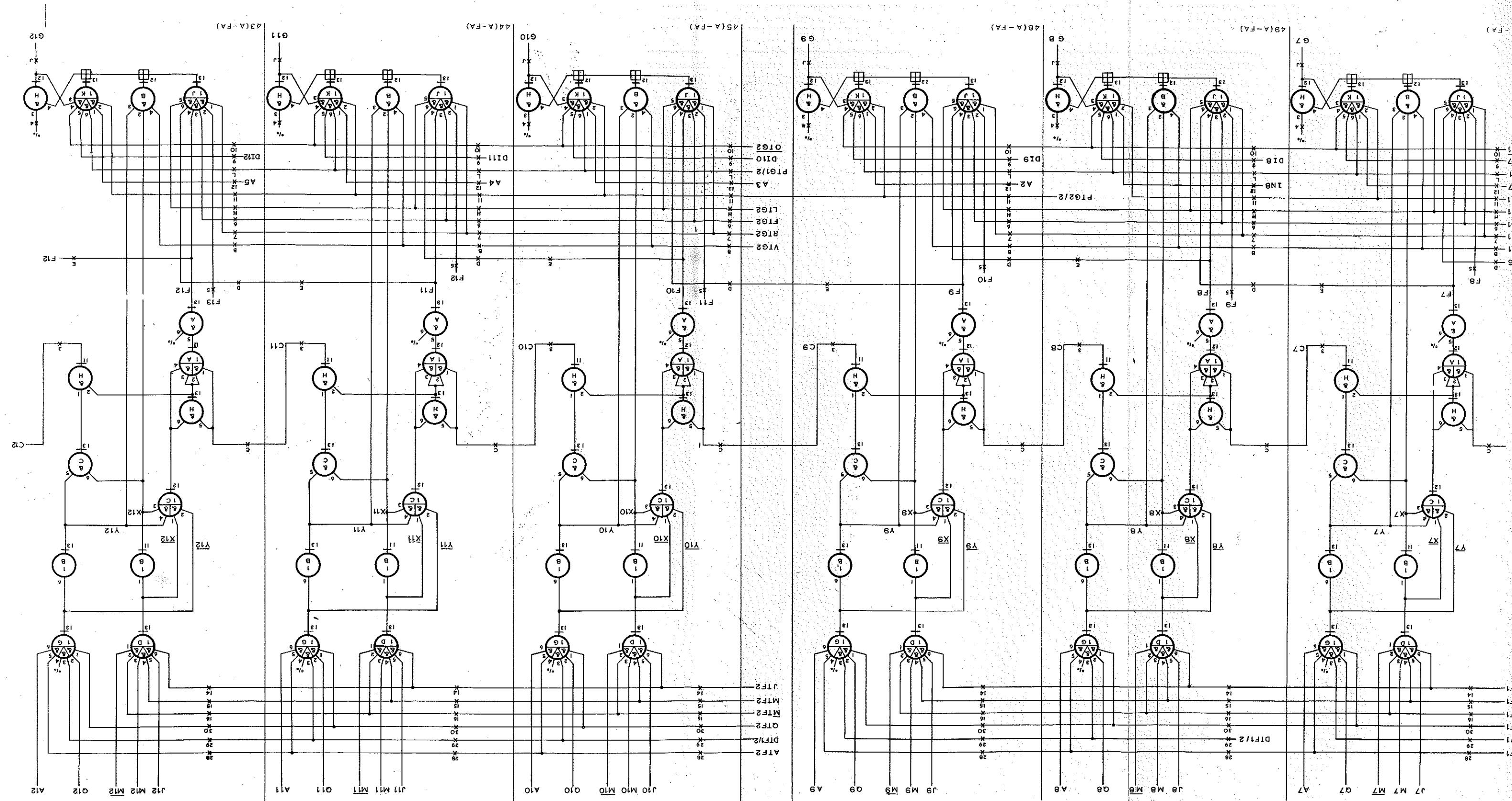
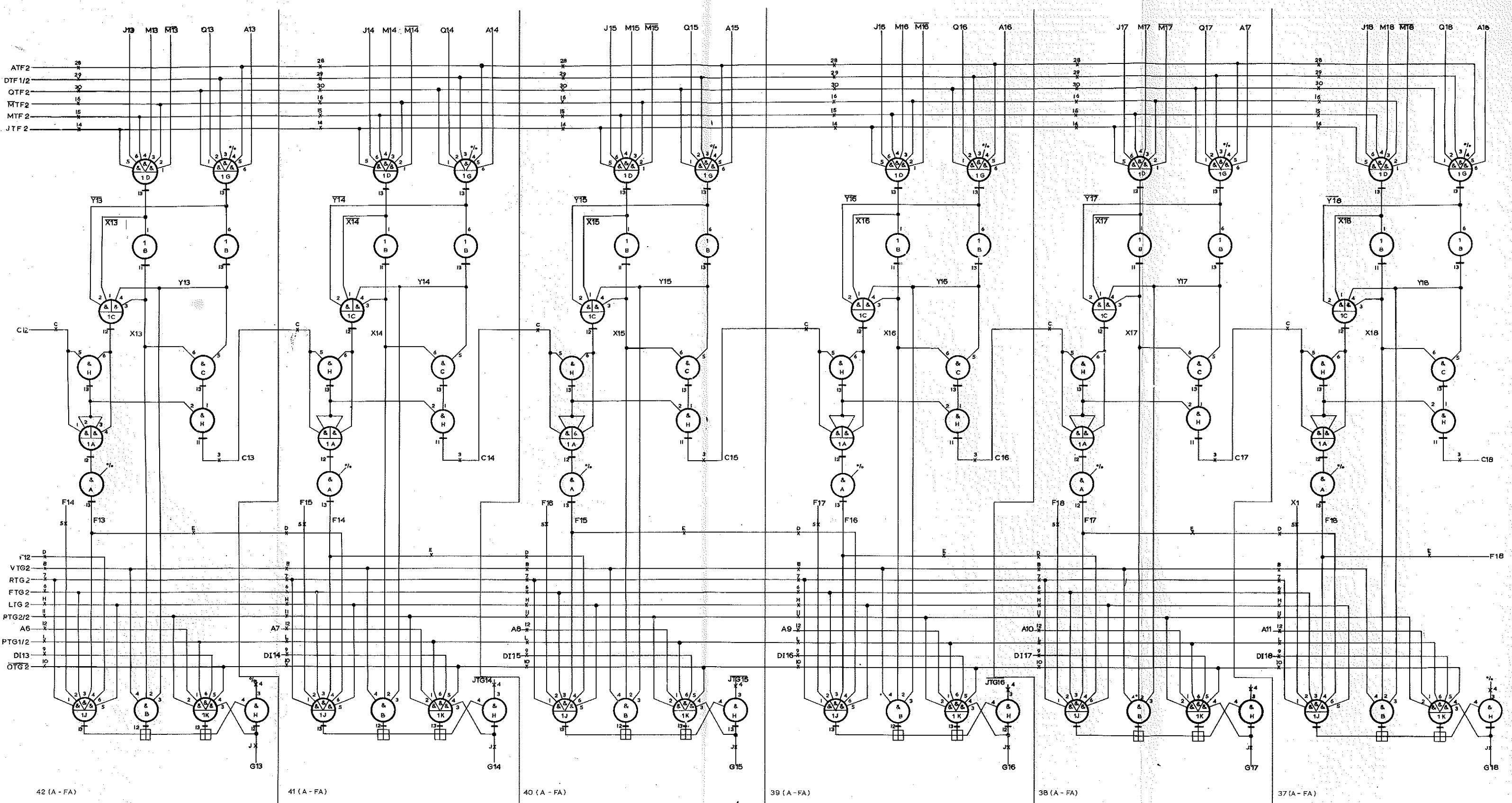
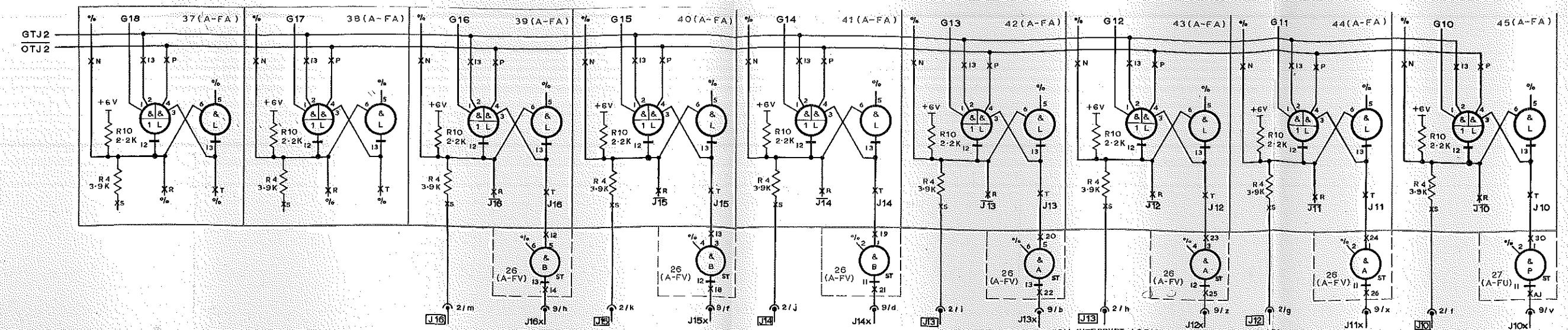


Figure 21a (issue 2)

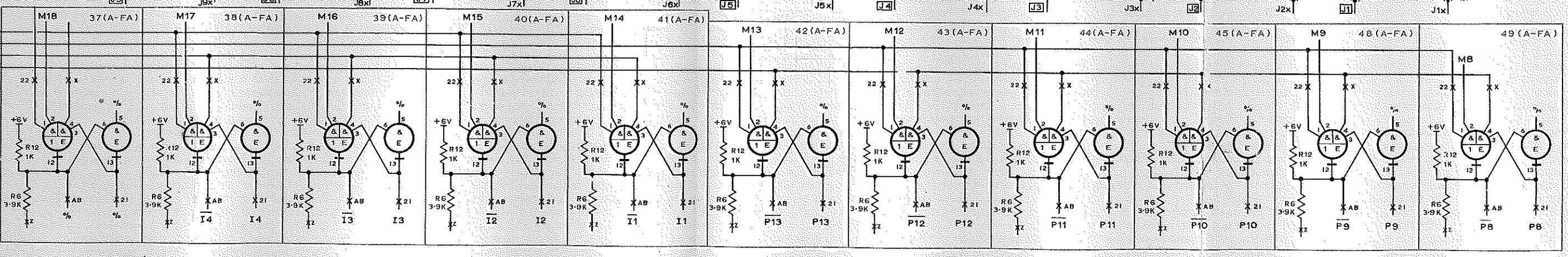
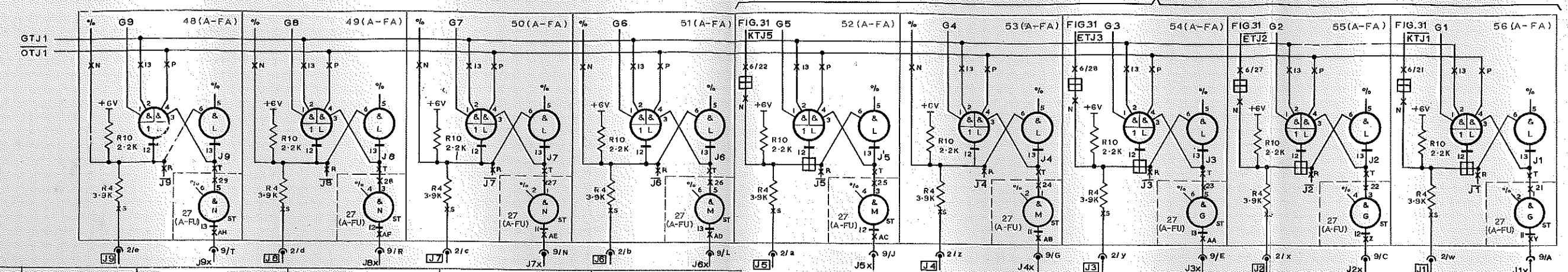
JUNCTION &amp; COLLATE UNITS &amp; G-REGISTER.



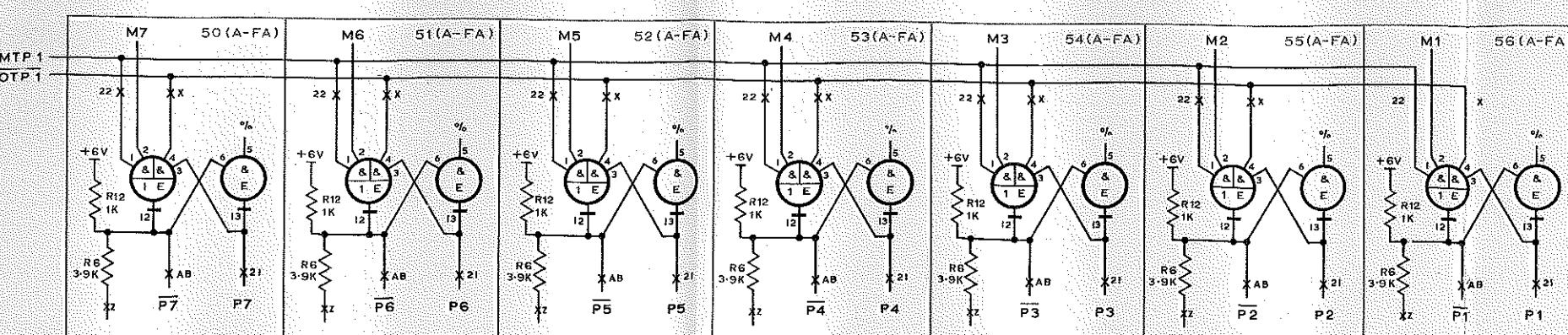


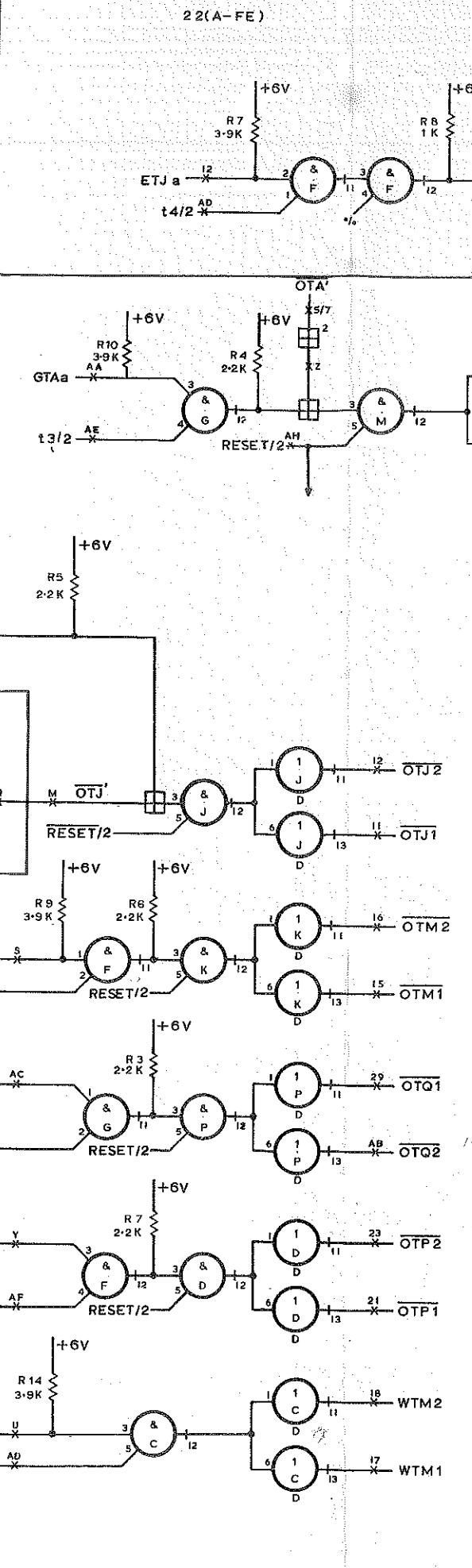
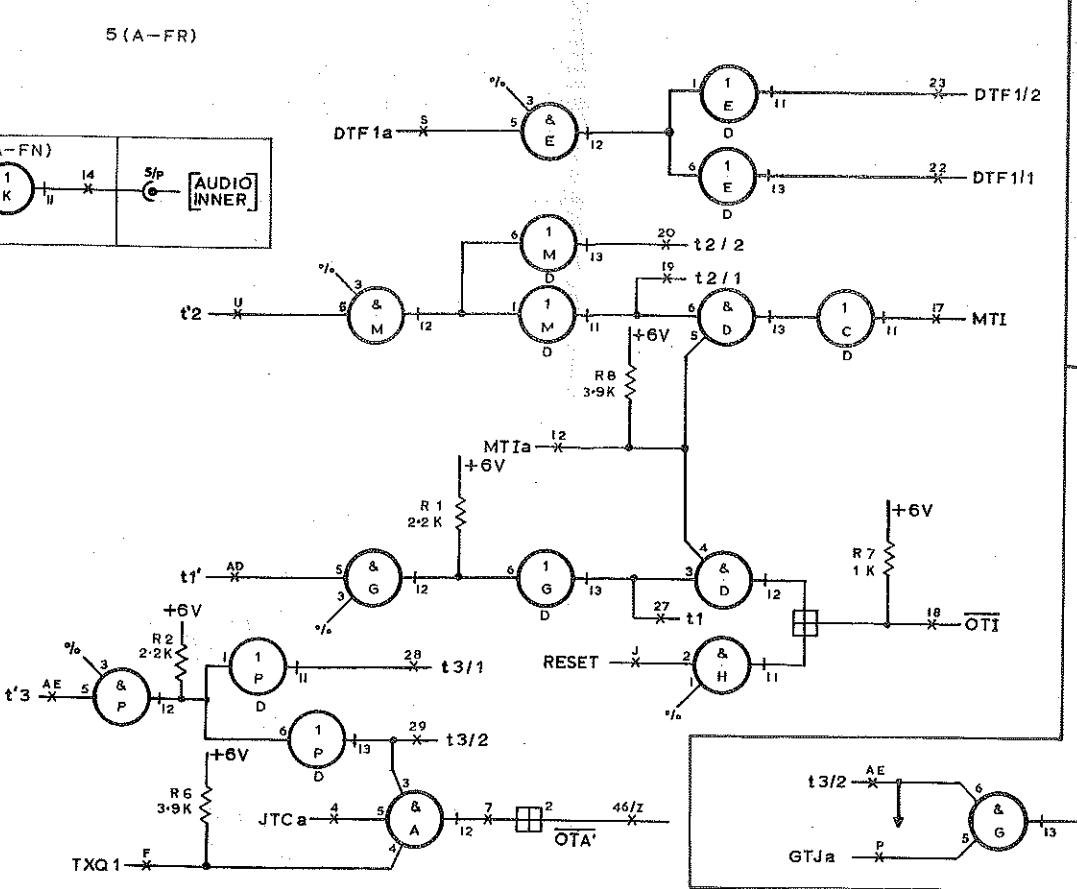
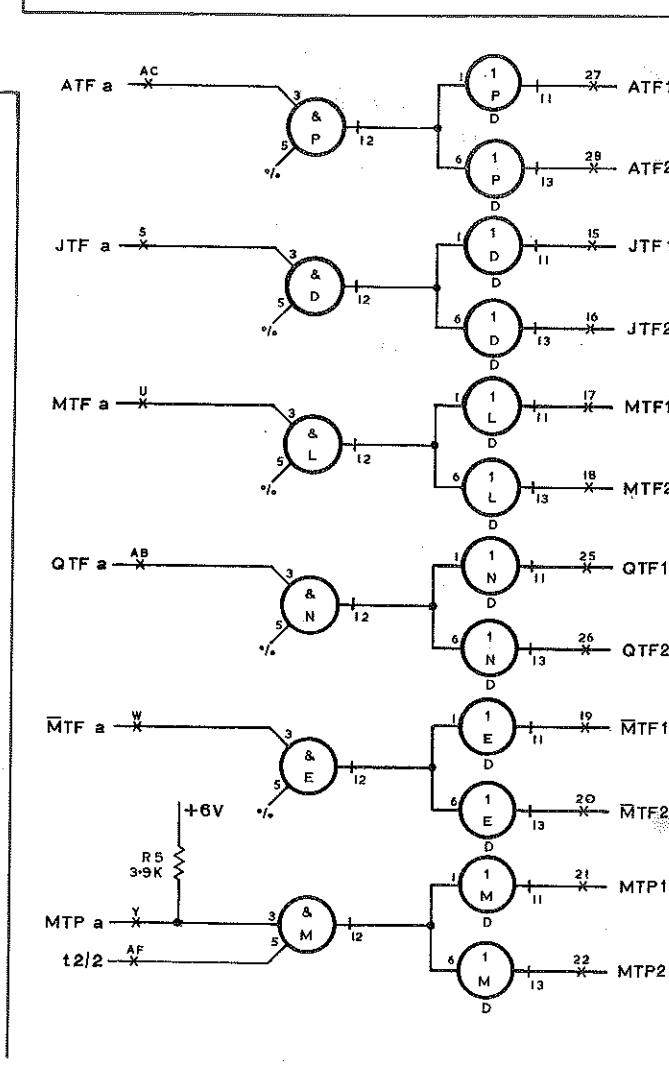
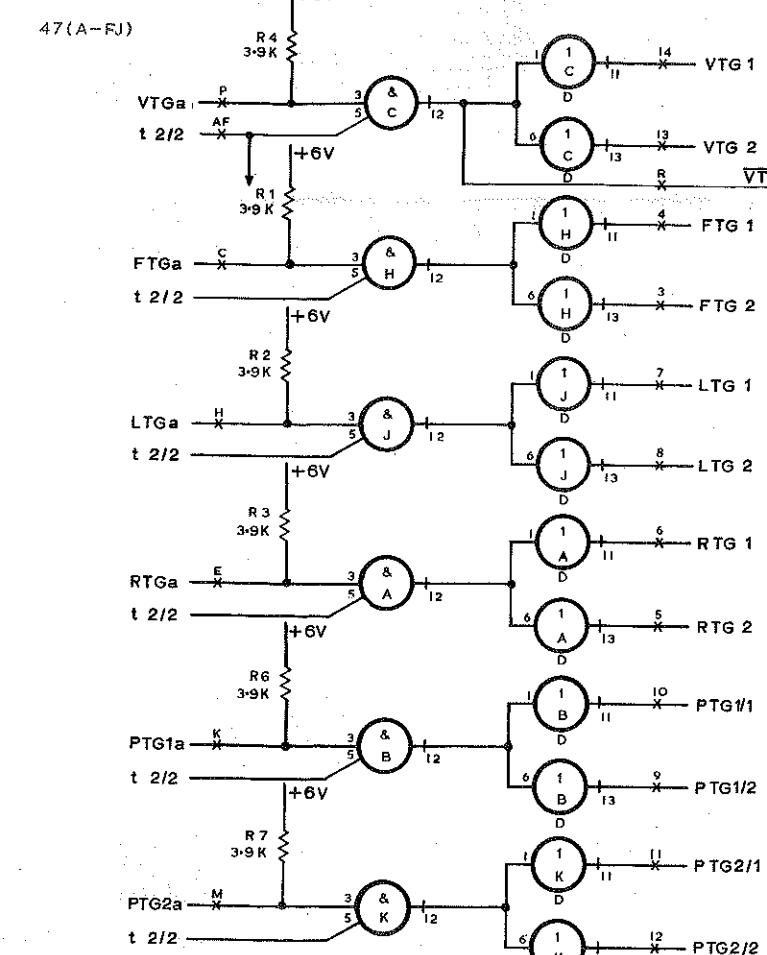


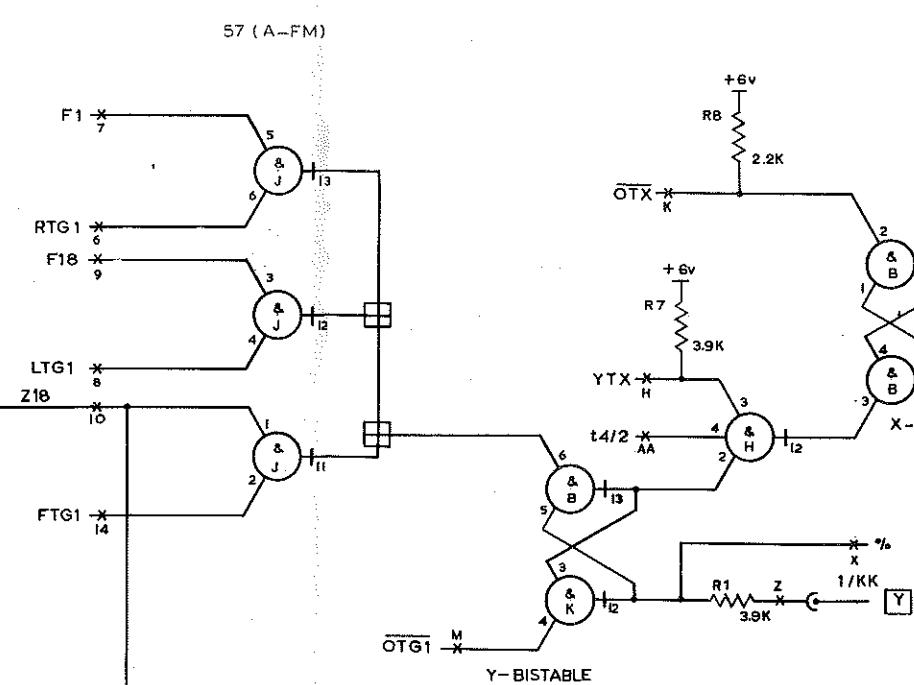
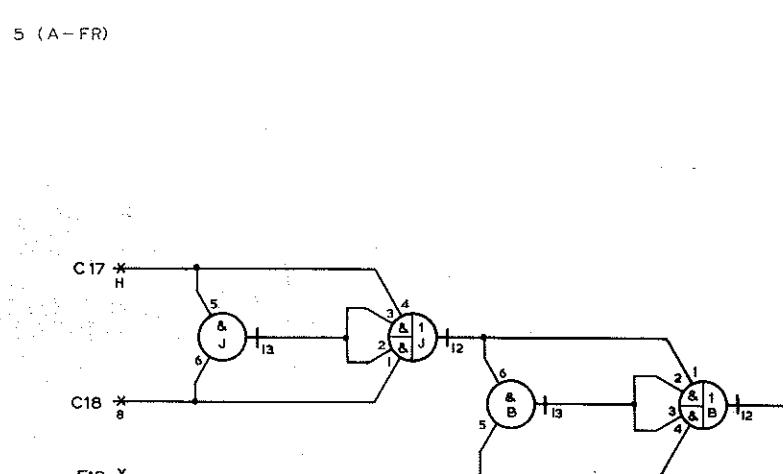
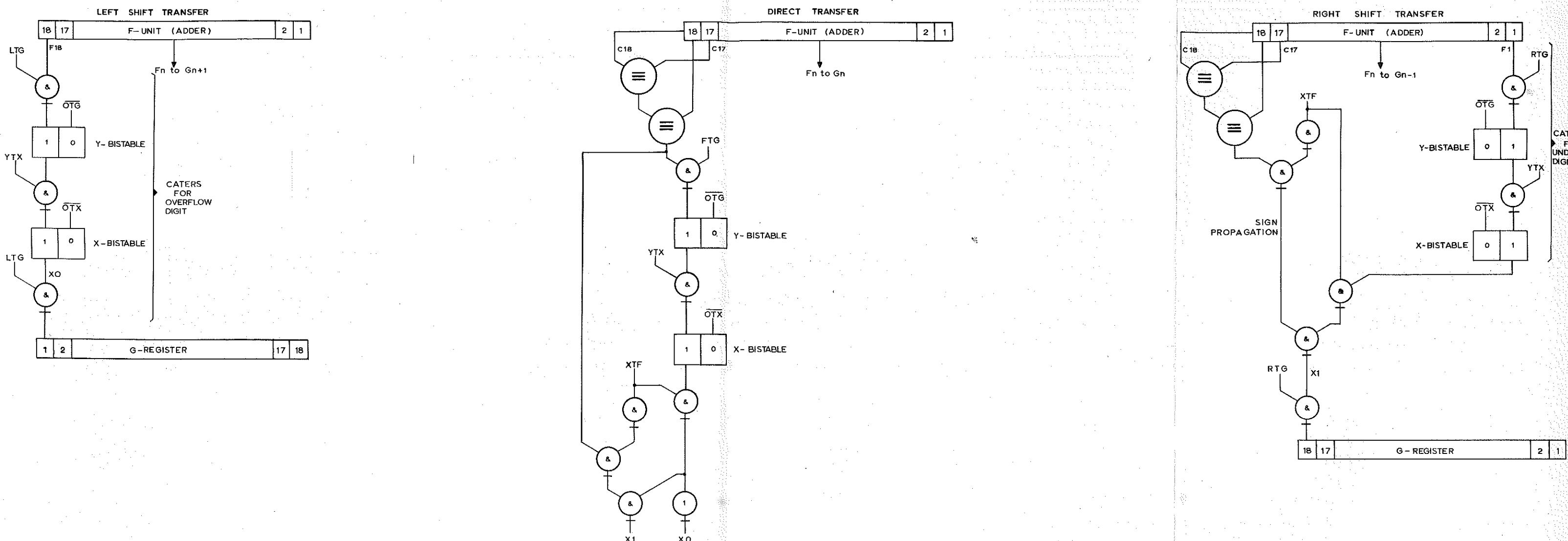
SIGNALS FROM INTERRUPT LOGIC WHICH SELECT THE ADDRESSES OF THE SEQUENCE CONTROL AND E (MODIFIER) REGISTERS.



TO FUNCTION DECODE LOGIC







## OVERFLOW & SHIFT

Figure 24

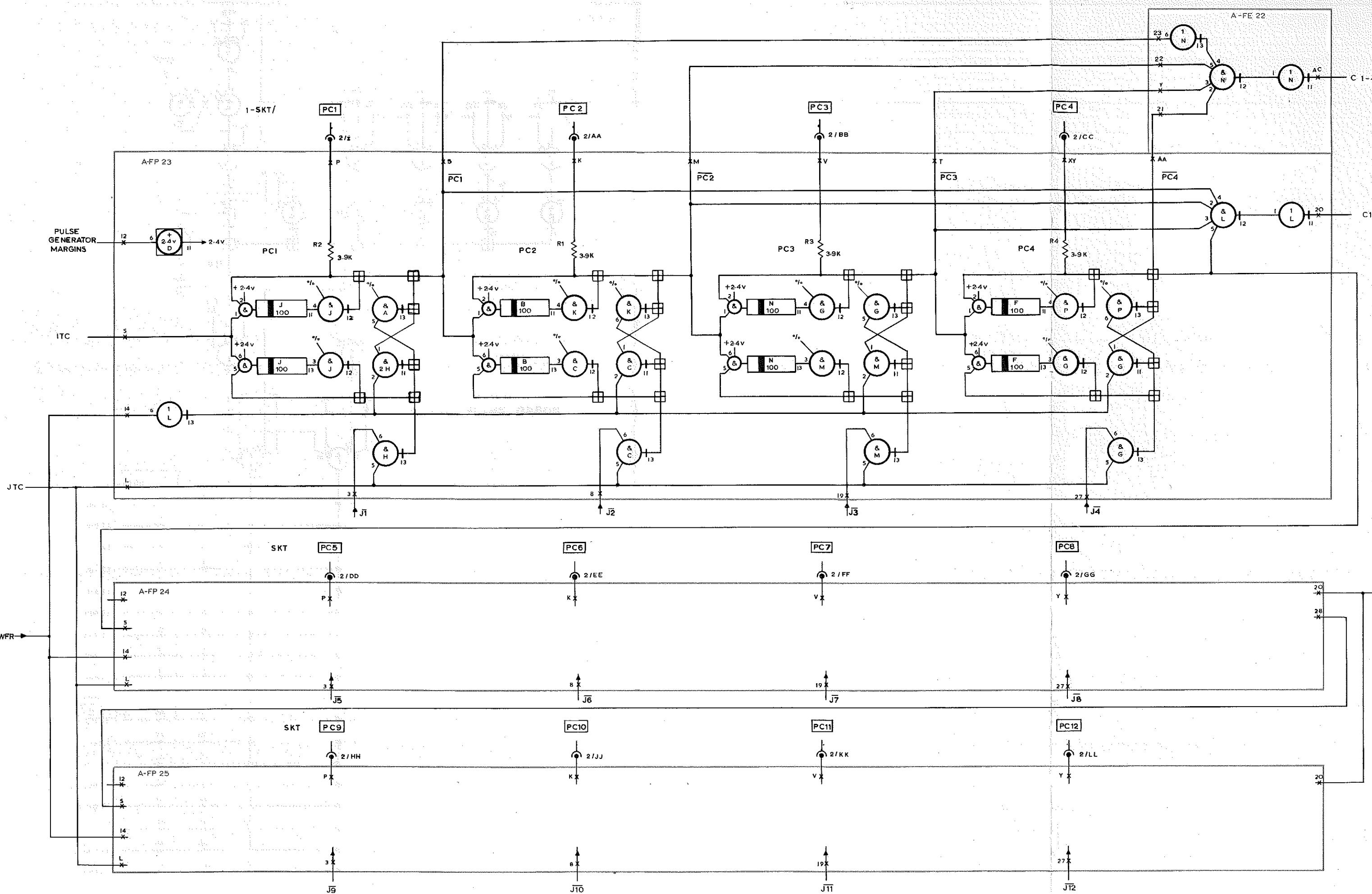
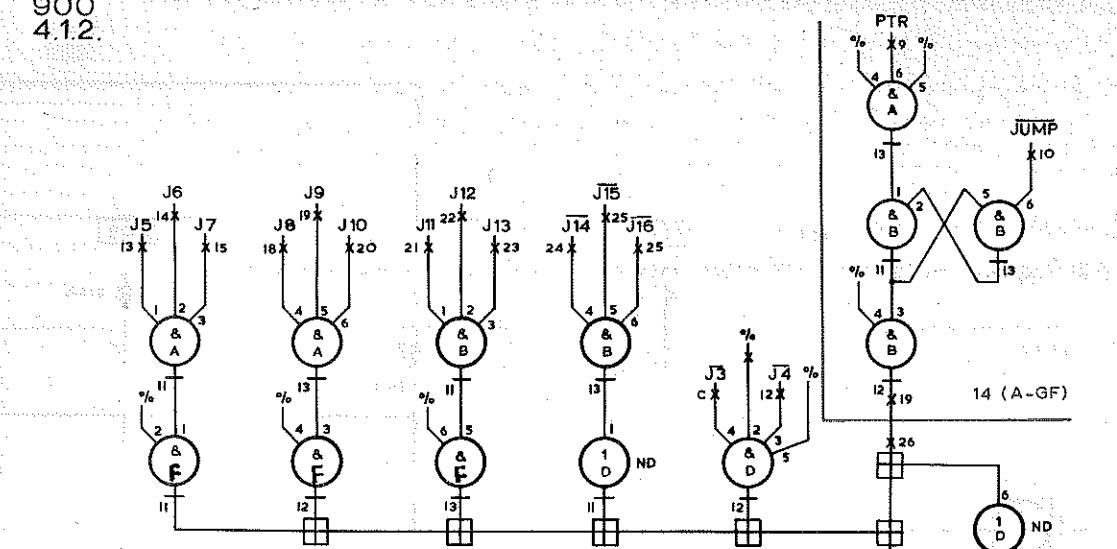


Figure 25 (Issue 2)

900  
4.1.2.



BOARD 36 (A-GK).

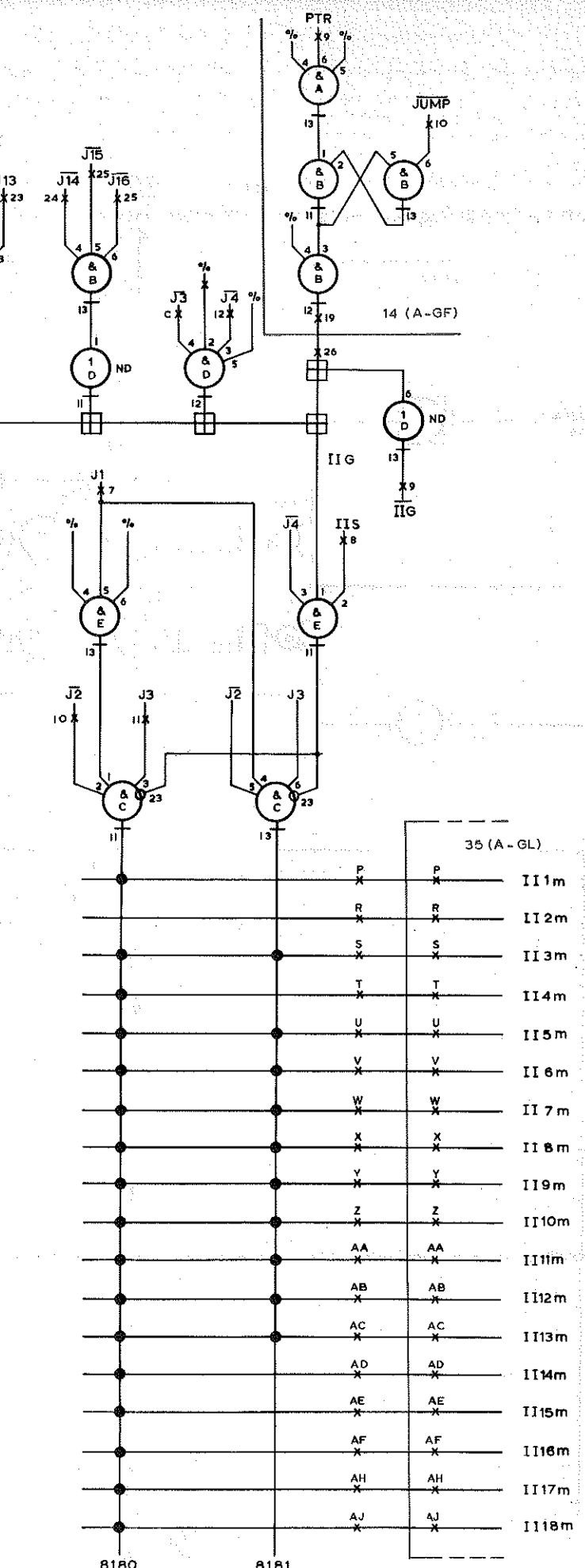
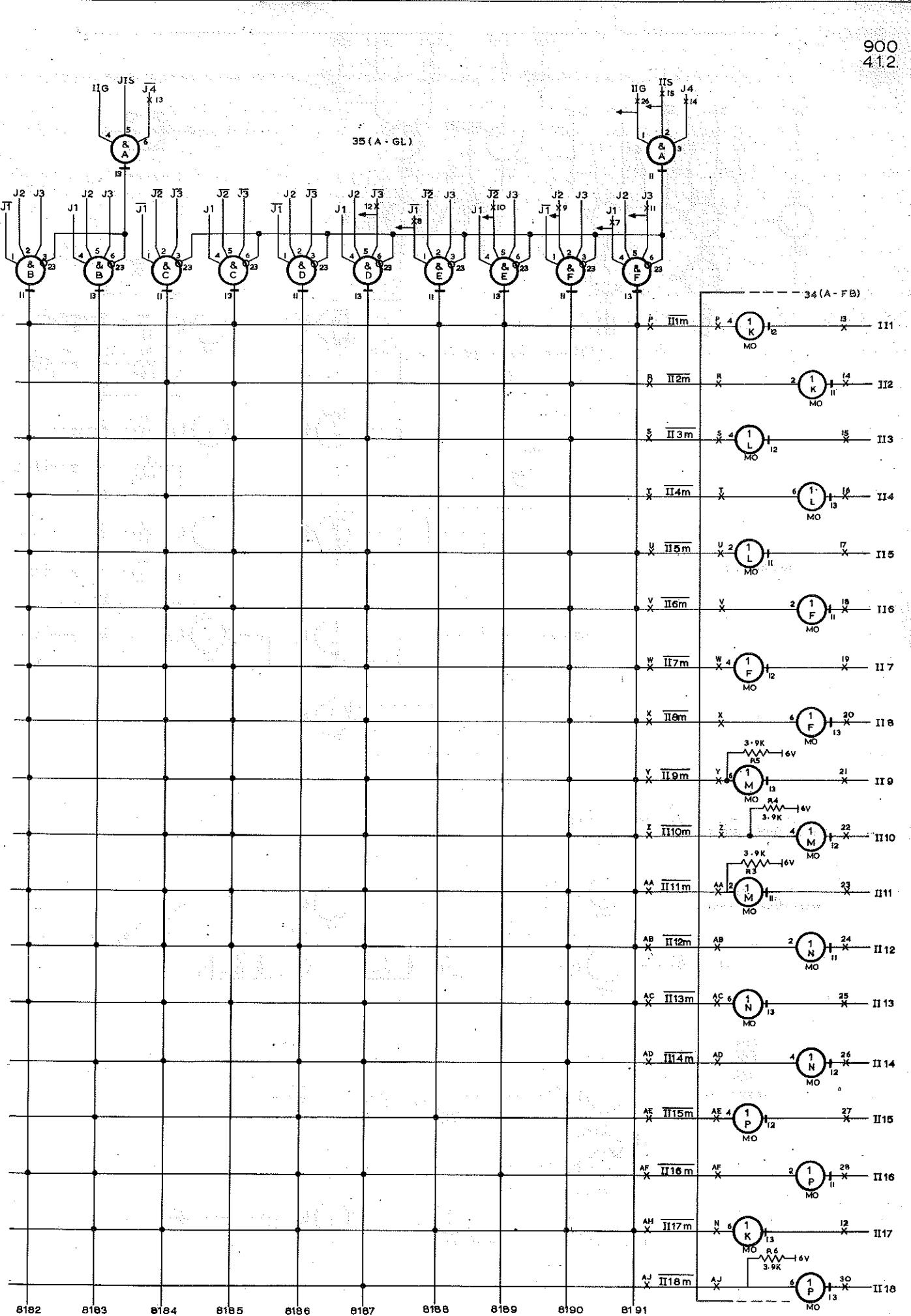


Figure 26a (ISSUE 2)



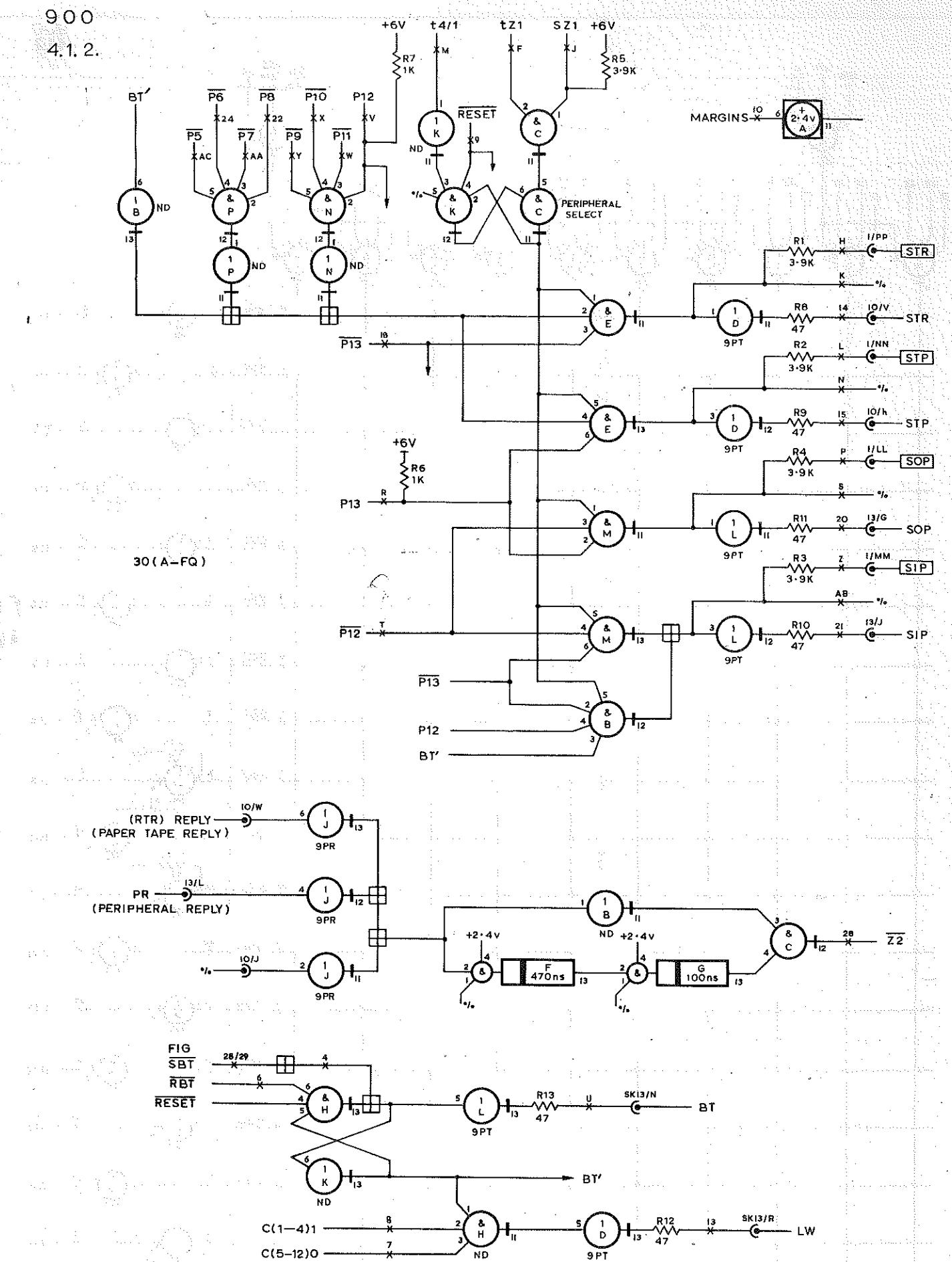
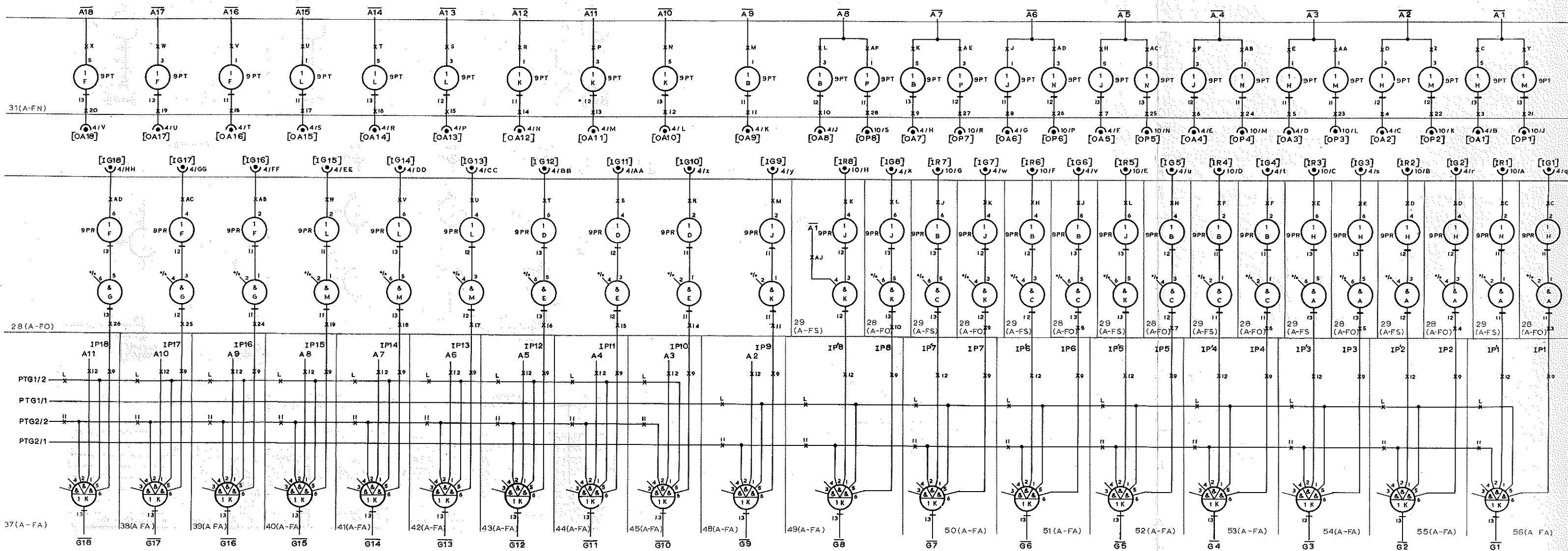
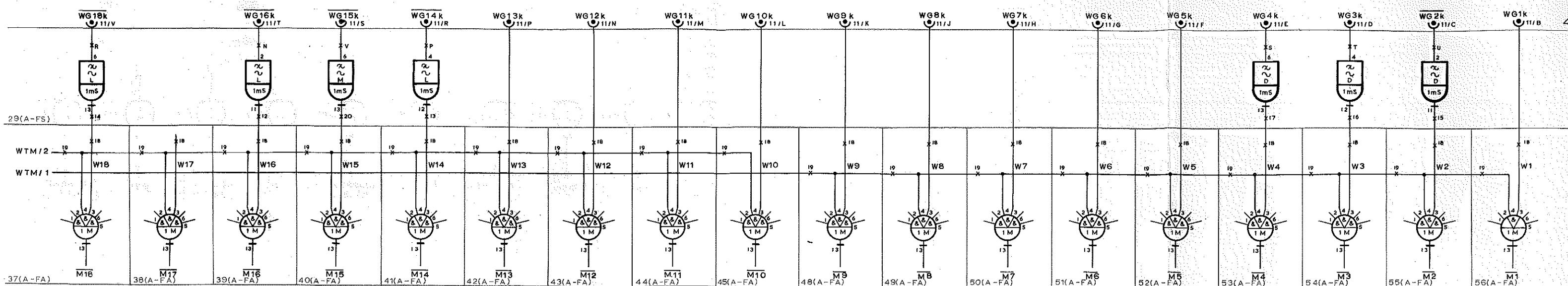


Figure 27 (ISSUE 2)

## INPUT / OUTPUT CONTROL



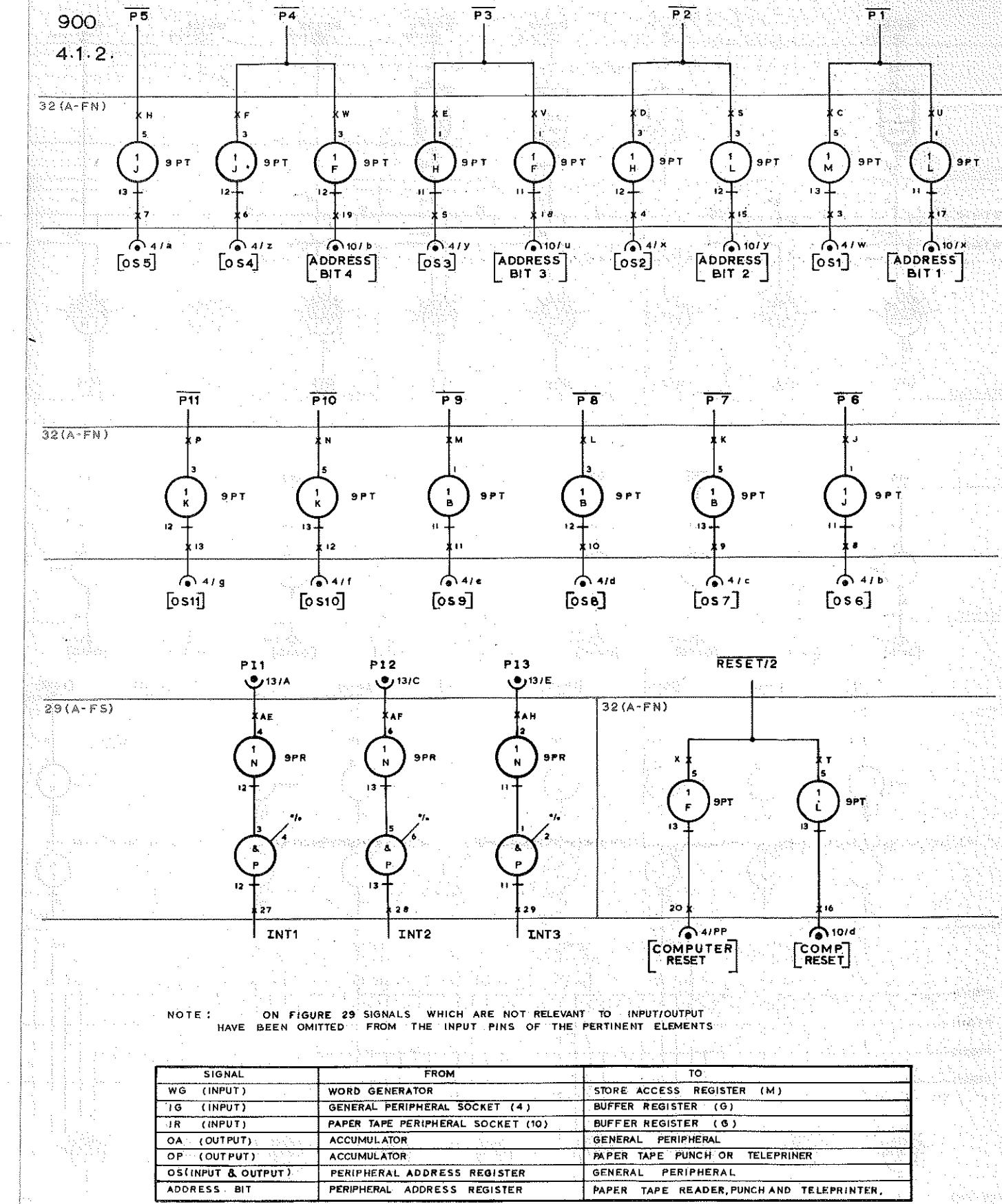
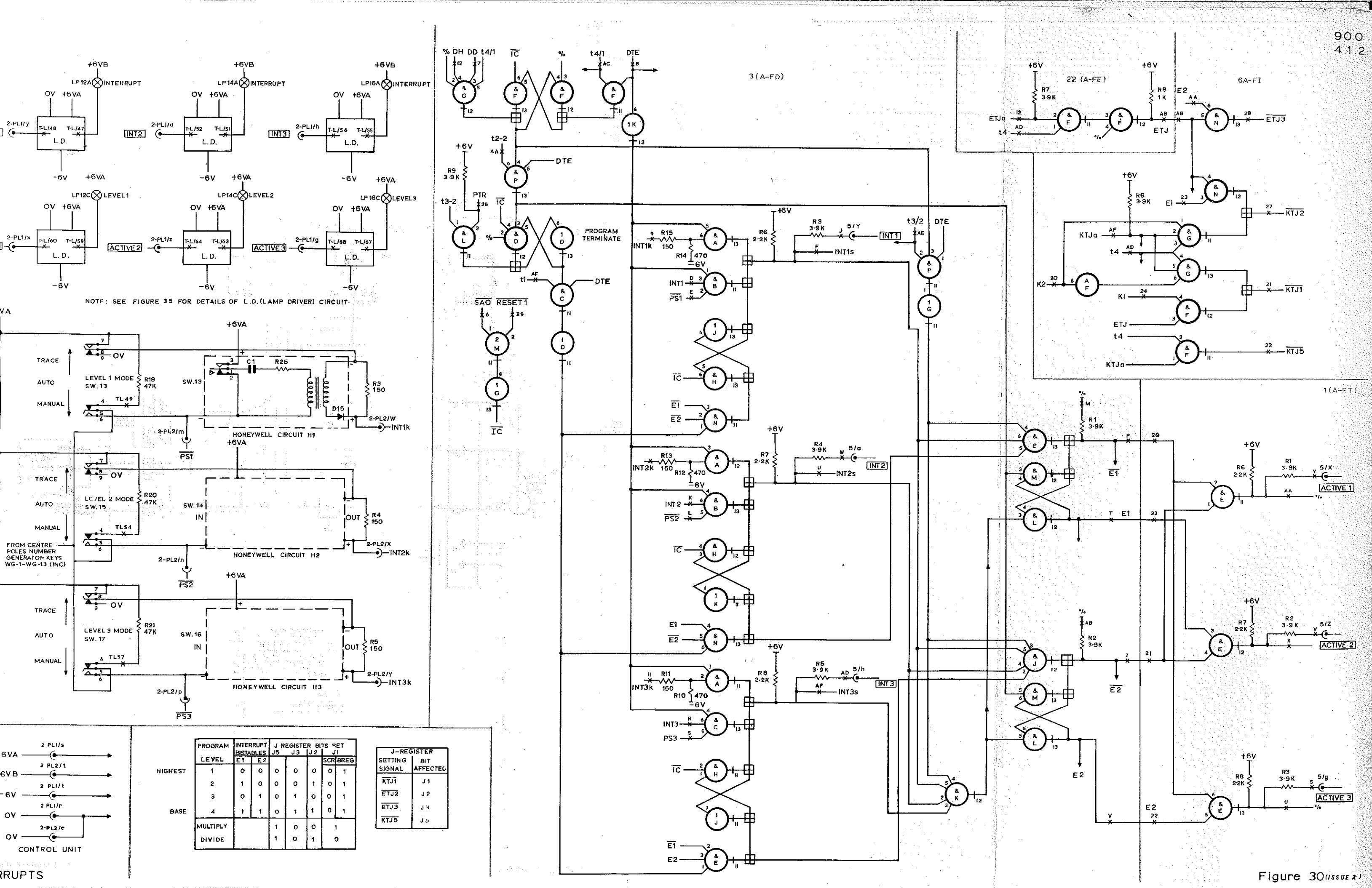
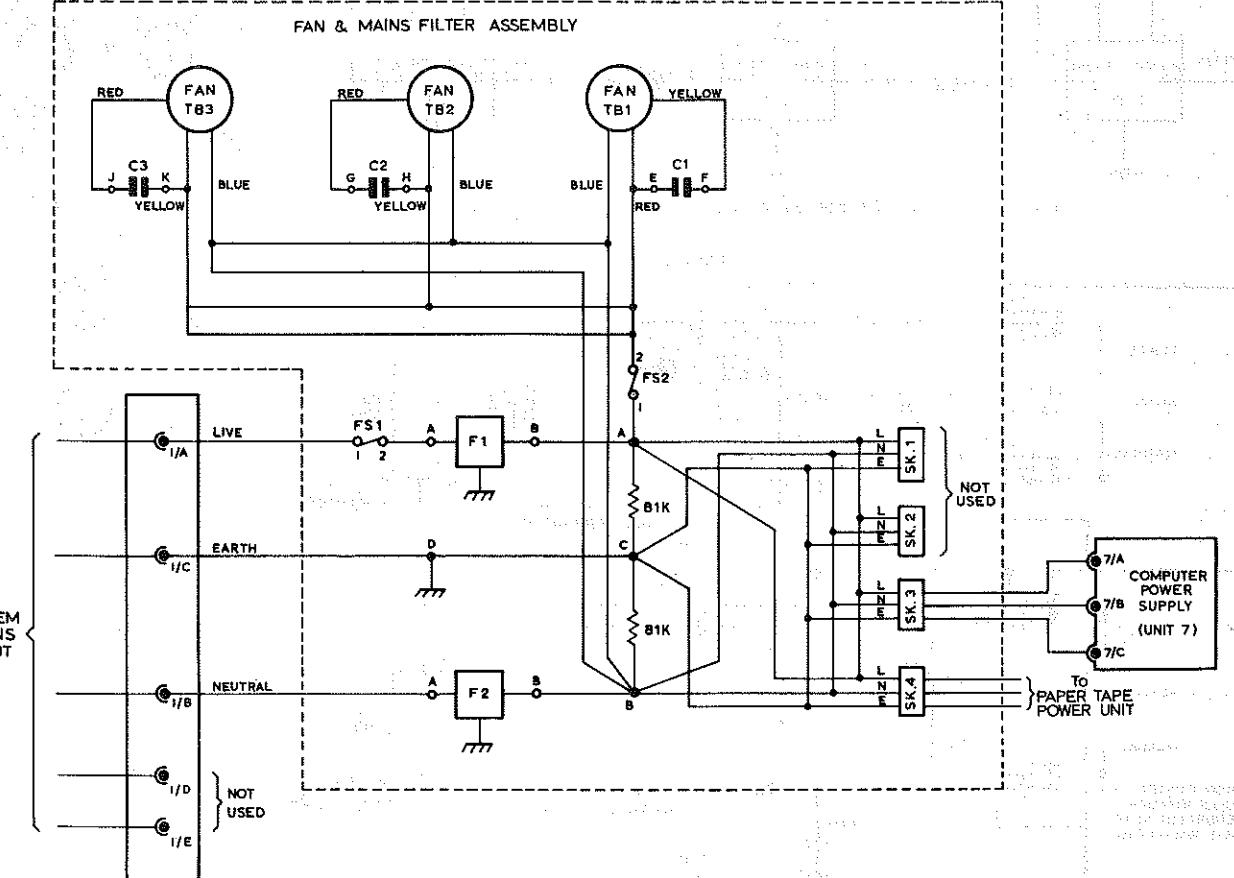


Figure 29 (ISSUE 2)

INPUT / OUTPUT



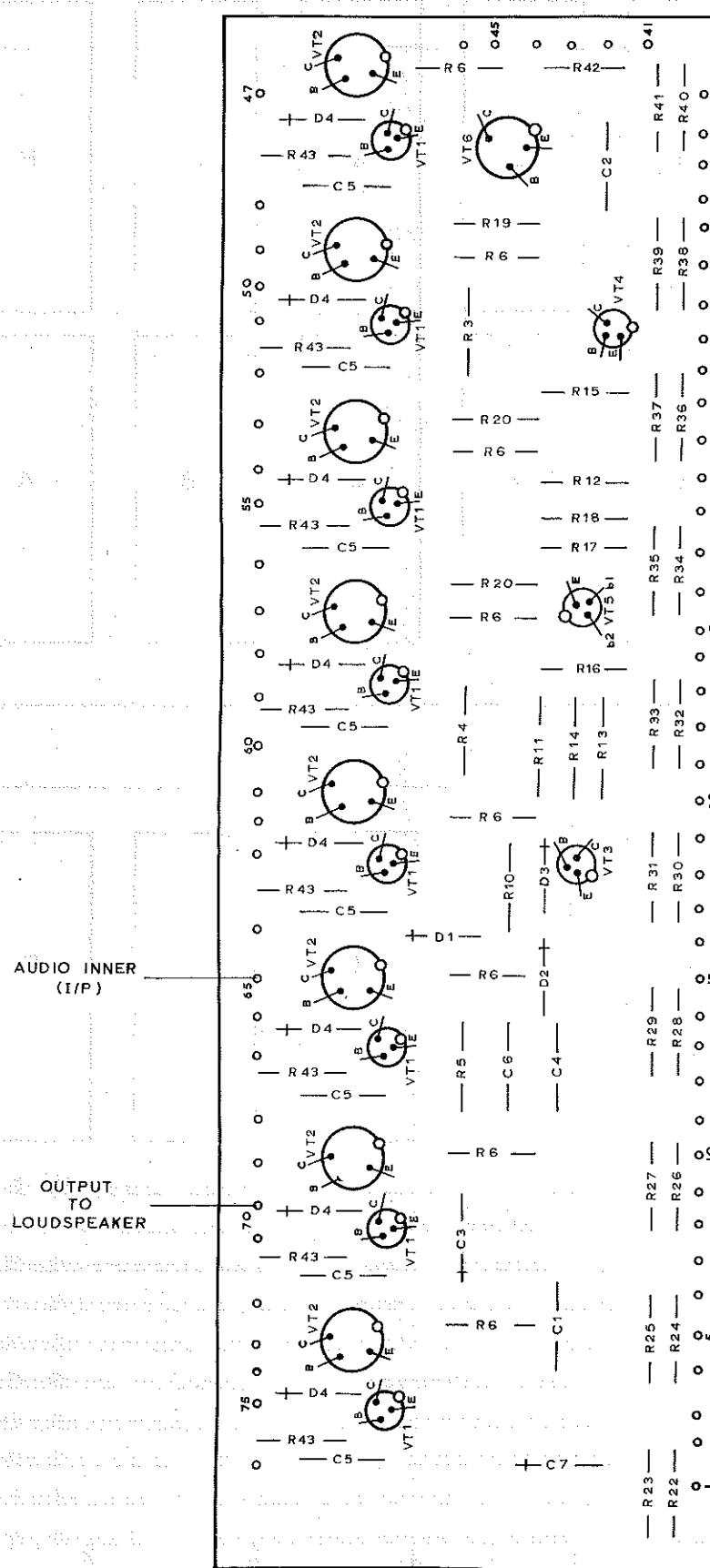
900  
4.1.2.



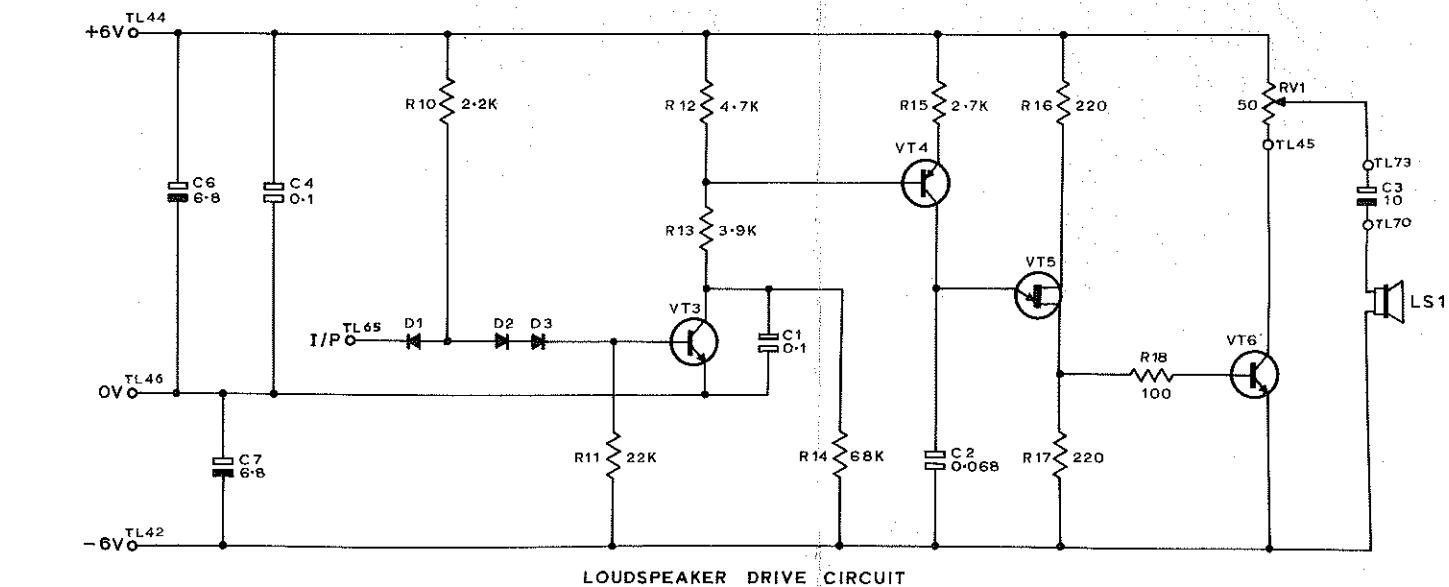
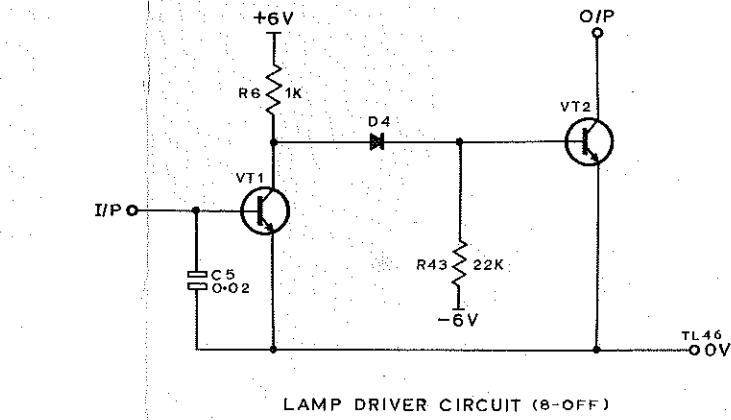
COMPONENT TABLE	
FS1	10A antisurge
FS2	3A antisurge
C1 - C3	1.0 $\mu$ F 1000V D.C.
Fan1-Fan3	
FILTER 1&2	
SK1 - SK4	
Resistors	R1K, R2K

Figure 31 (ISSUE 2)

A.C. DISTRIBUTION



COMPONENT TABLE				
R.	VALUE	WATT TOL%		
22-41	47	1/4 ± 5	9219	
16	100	1/2 ± 5 TE	5662	
3-5	150	1/2 ± 5 TE	6205	
16-17	220	1/2 ± 5 TE	5885	
6	1K	5 TE	5671	
10	2.2K	5 TE	6017	
15	2.7K	5 TE	5677	
13	3.9K	5 TE	5679	
12	4.7K	5 TE	5680	
11&43	22K	1/2 ± 5 TE	6012	
19-27	47K	1/2 ± 5 TE	5874	
14	68K	1/2 ± 5 TE	6389	
RV1	50Ω		6275	
D			11606	
V T				
1,3			11632	
2,6			11607	
4			11608	
5			11609	



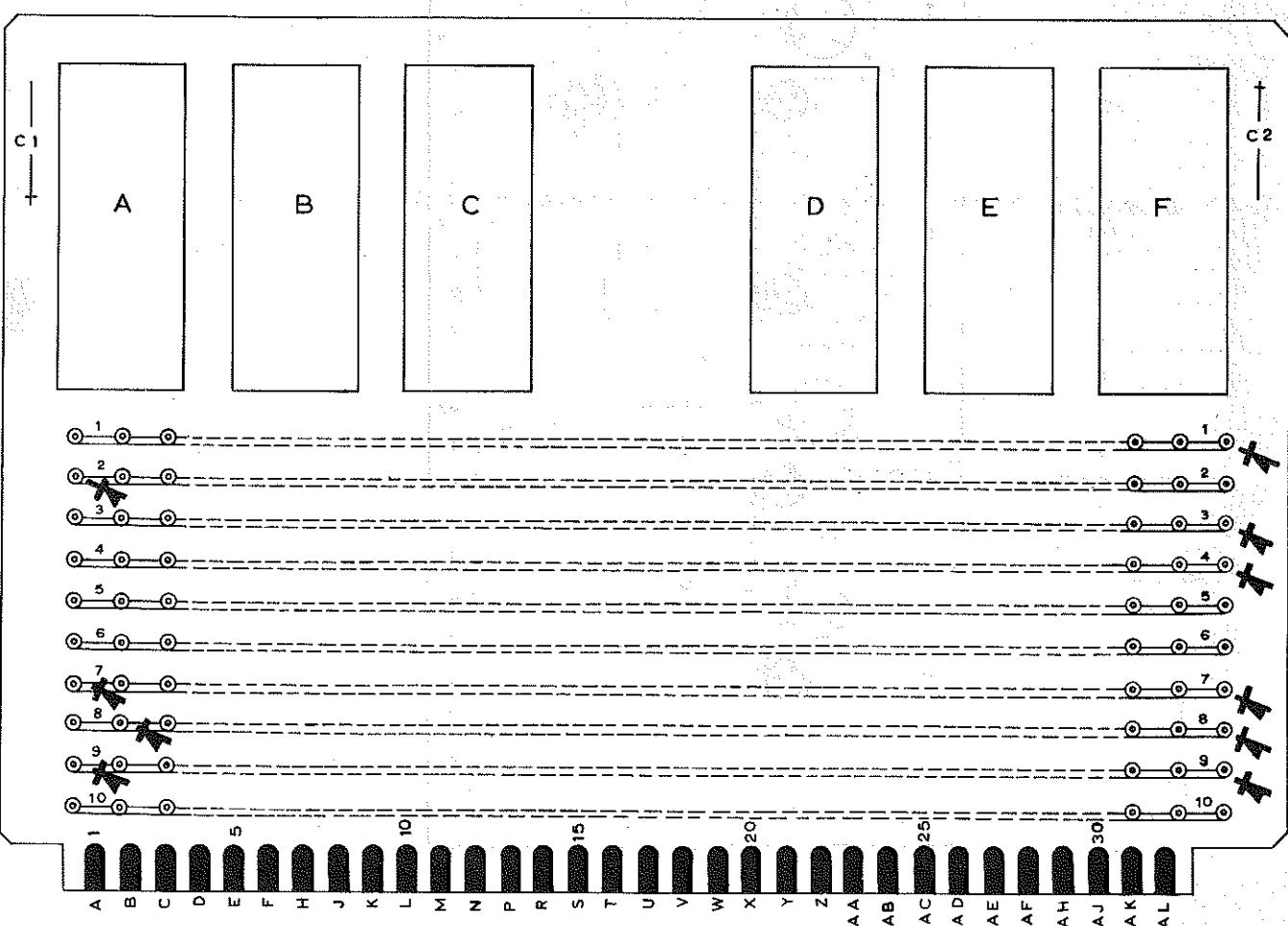
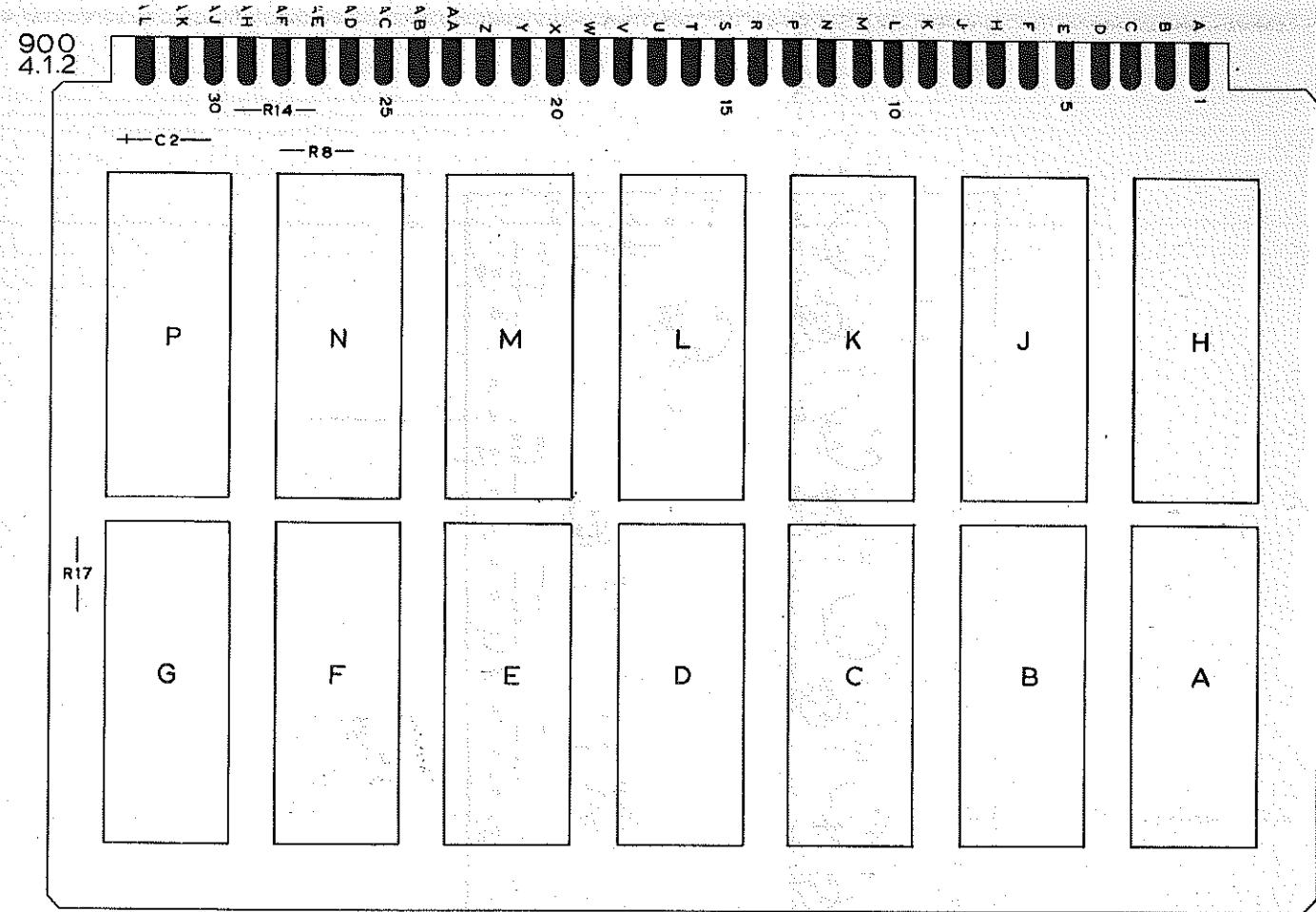
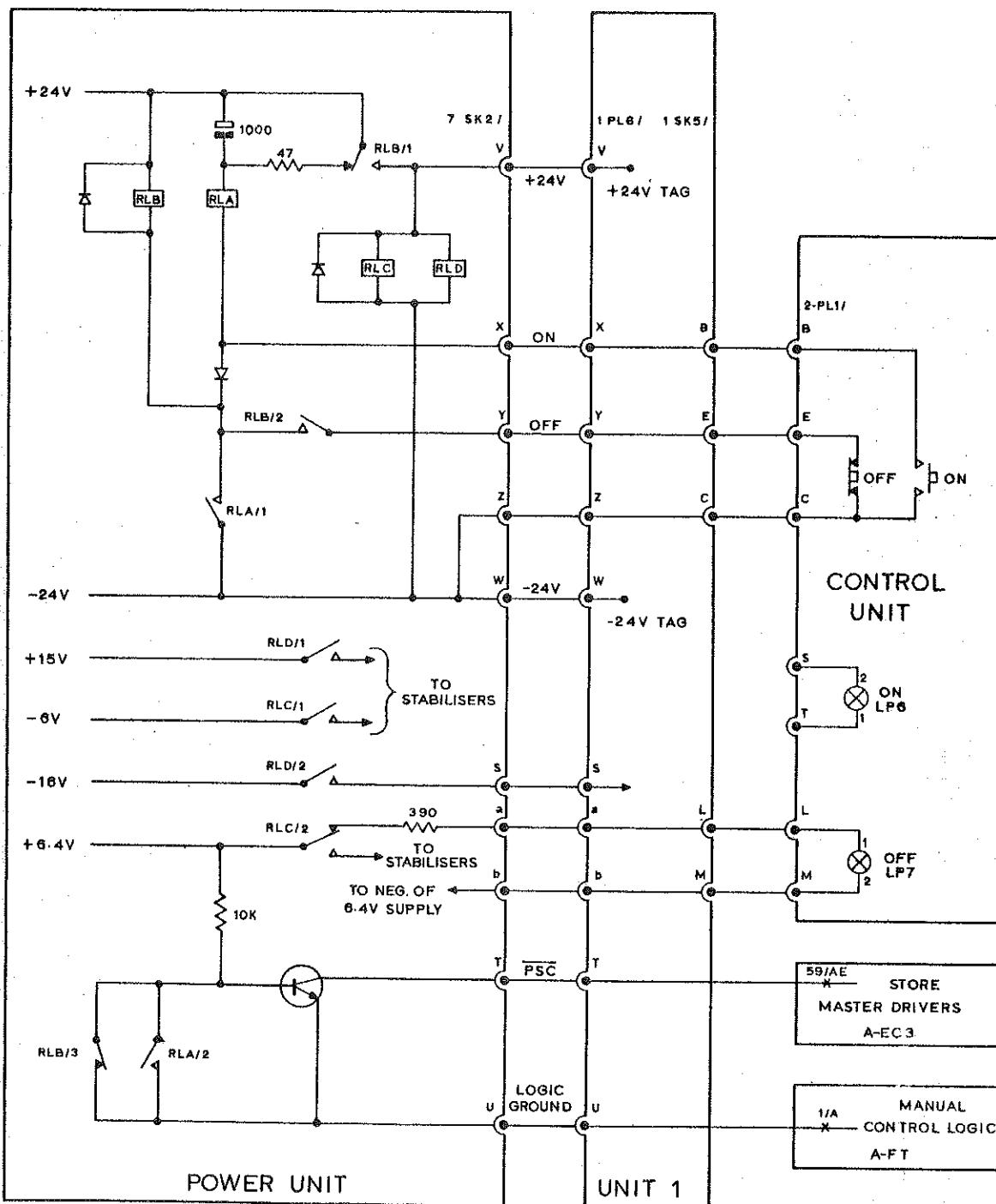
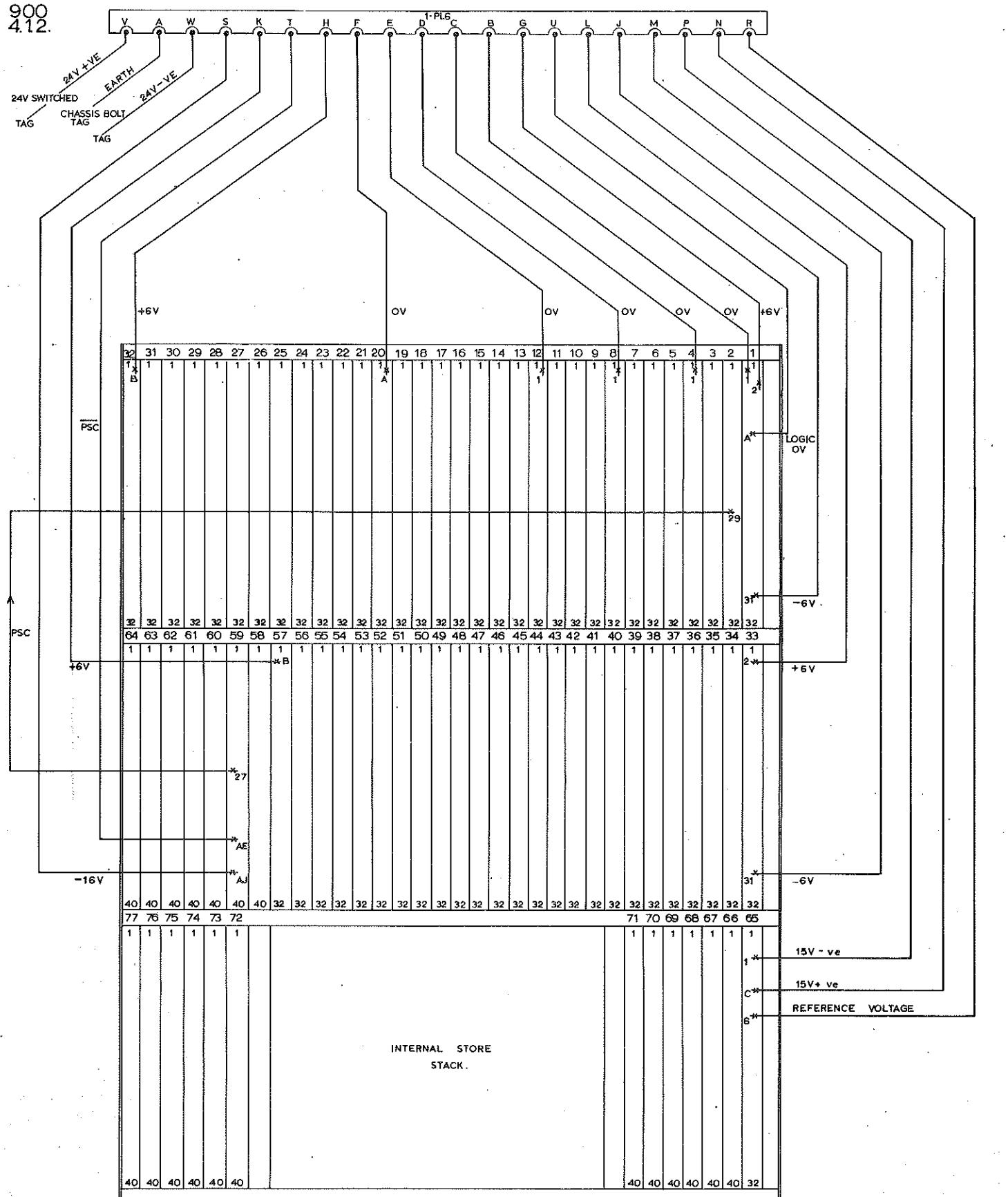


Figure 35 (ISSUE 2)



900  
4.12.

24V SWIT  
TAG



24V+ve	+6V	OV	TAG	-6V	-16V	24V-ve	15V+ve	15V-ve	
			A						SKT. 1 - DISPLAY UNIT PL1
	MM	A							SKT. 2 - DISPLAY UNIT PL 2
MM	PP	A	NN						SKT. 3 - DISPLAY UNIT PL3
P	LL	A							SKT. 4 - INPUT - OUTPUT UNIT
S	R	A	t						SKT. 5 - CONTROL UNIT PL1
		A							SKT. 7 - MARGINAL TEST UNIT PL1
	g	A							SKT. 8 - EXTRA STORE
g	j	A			h				SKT. 9 - EXTRA STORE
f	e	T			g				SKT. 10 - PAPER TAPE CONTROLLER PL2
	t	A							SKT. 11 - CONTROL UNIT PL2
HH		JJ		KK					SKT. 14

**Figure 32 (ISSUE 2)**

DISCRETE COMPONENTS			
COMPONENT	VALUE	TOL ± %	CAT. No.
RESISTOR	47	5	9219
"	150	5	9197
"	470	5	9169
"	1K	5	9159
"	2-2K	5	9160
"	3-9K	5	11229
"	100K	5	11363
CAPACITOR	4.7		11750
"	6.8		11751