USED ON
A 7201

FUNCTION 14
SHIFT

TO CYCLE 14A2
BLOCK
TRANSFER

FROM CONTROL →

140 TM12M13 / TM13

142A MTF ITF CRS FTG GTJ JTPC

142B MTF CRS FTG GTJ JTPC

143A CRS TPCO

143B CRS TPCO

144A ATF CRS YTX RTG OTX GTA ITPC

144B QTF CRS YTX LTG OTX GTQ ITPC

146A QTF XTF CRS TPCO RTG GTQ

146B ATF XTF CRS TPCO LTG GTA

145 → TO CONTROL

A-GH 12

TO F17 PIN 3 (SHEET 3)

J/06 J/06 A-FR 5

F18 PIN 3 (SHEET 3)
F18 PIN E (SHEET 3)

B/06 → Z18

A-FB 9
XTFm J/04 XTF
YTXm B/04 YTX   YTX

A-FR 5
A-FB
OTXm F/04 OTXa   OTXa
+6V R5 3·9K   K/01 OTX   H/01

A-FM 57
F18 RTGI J/01
Z18 FTGI J/01 B/01 H/03 B/01 G/01 H/03 +6V R10 1K
F18 J/01 K/01
+6V R7 3·9K   +6V R8 3·9K
LTGI OTGI YTX t40 OTX XTF
H/03 K/01 K/01 → X1 PIN 5 A-FA 37 (SHT 3)

R1 R9 1K
+6V R2 3·9K

OVERFLOW

Row labels (right side):
C AA1m
D AA2m
E AA3m
AJ MTFm
F FTGm
AF GTJm
T TM12M13m
V TM13m
Y ATFm
X OTXm
AB GTQm
H JTPCm
M YTXm
R RTGm
AD ITPCm
AE GTAm
P MTFm
N ITFm
AH QTFm
Z XTFm
W TPCOm
AA LTGm
AC CRSm

Bottom matrix boxes: 140 142B 142A 143B 143A 144B 144A 146B 146A 145

DC0 7 / DE0 8 / CD0 9 / S00 26 / S10 25 / S20 24 / S30 23 / S40 22 / S50 21 / CA0 20 / CA0 19

SO/S4 F/02 E/02 C/02 C/02 B/02 B/02 D/02 D/02 F/02
DC/DE/CD A/02
DC A/02
DE

TO BE READ IN CONJUNCTION WITH 322 A 7191

920B COMPUTER
MULTIPLY/DIVIDE SHIFT
AND OVERFLOW LOGIC
SHEET 2

A10

LATEST MI INCORPORATED: MI.2858