

USED ON
A7201

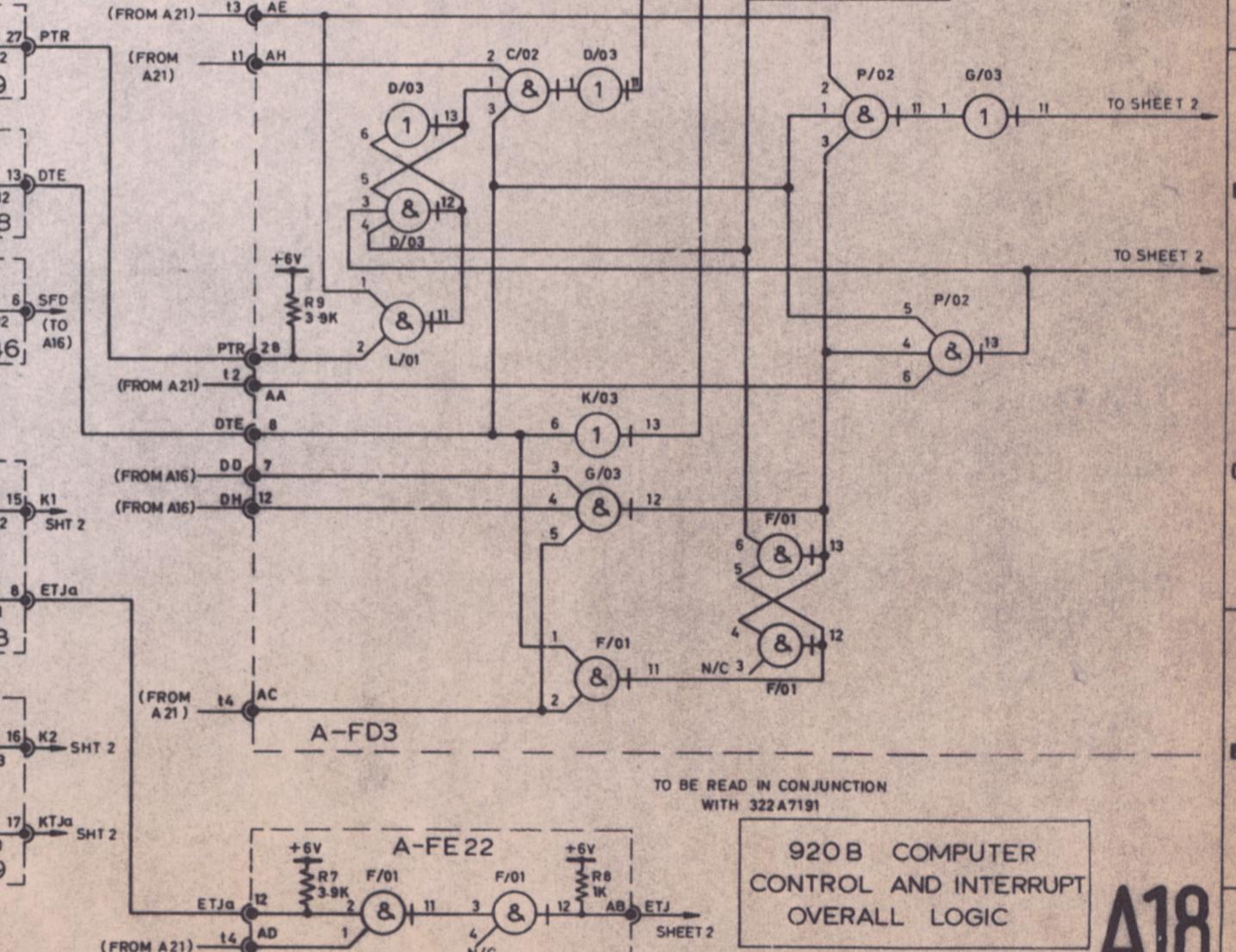
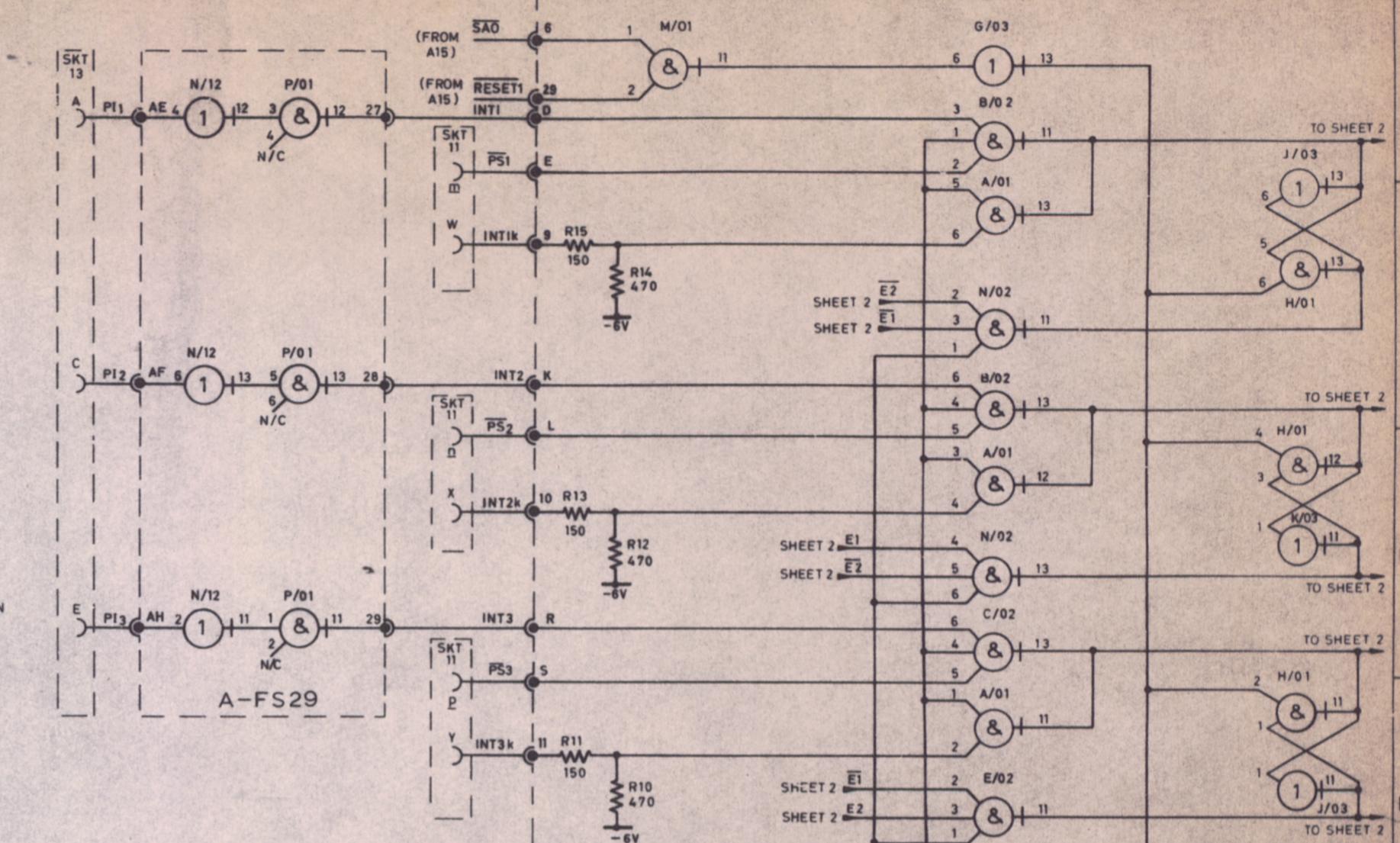
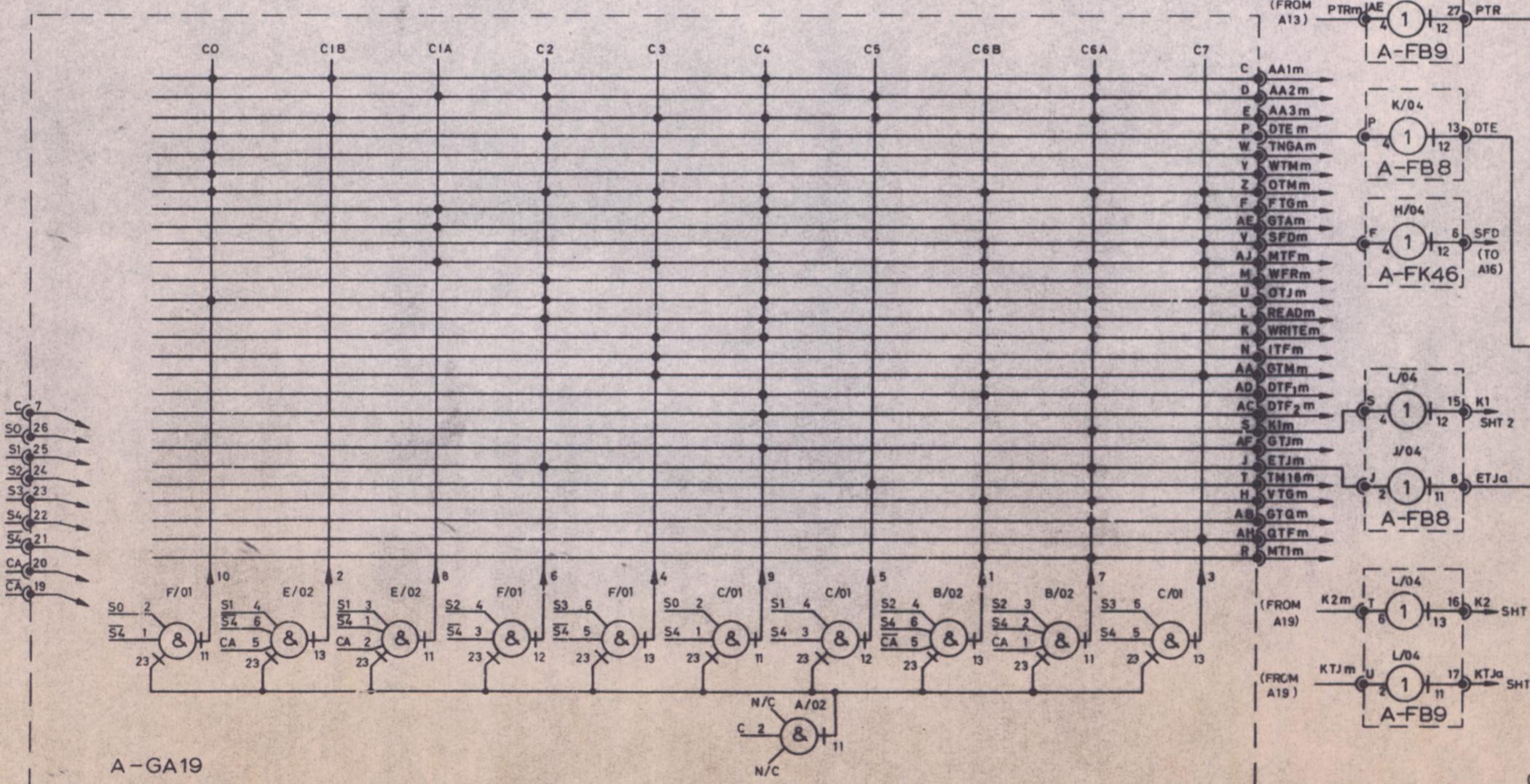
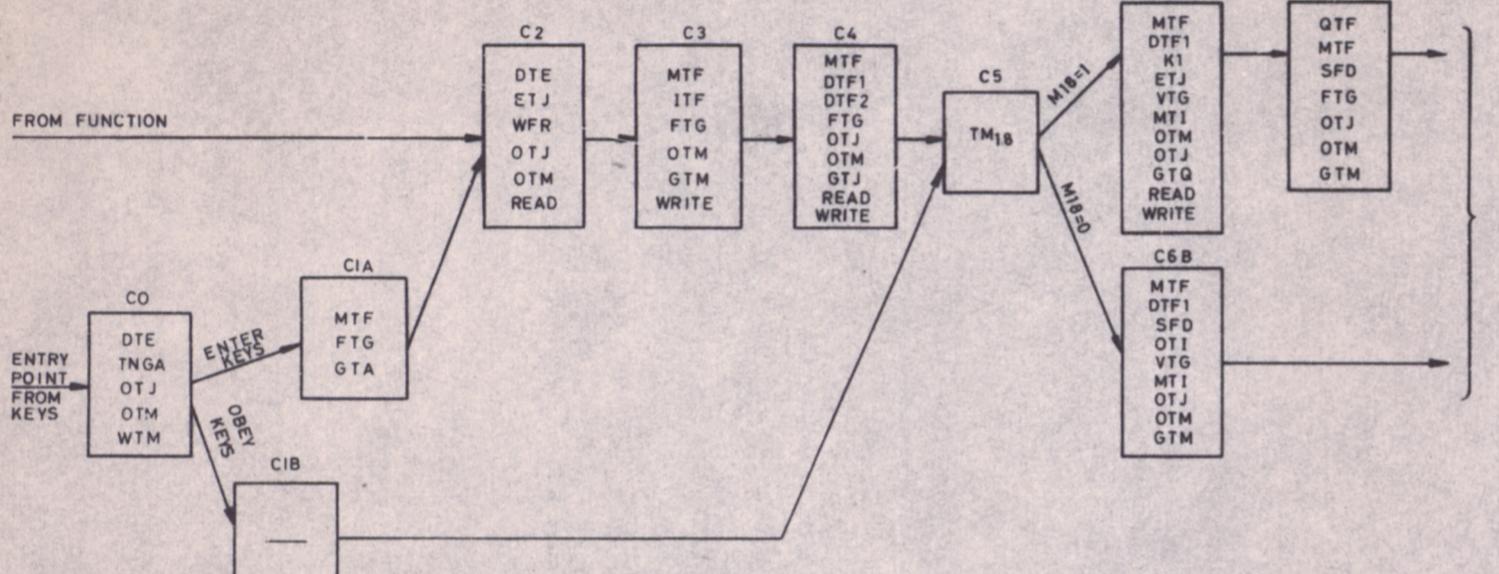
KTJ SETS UP J REGISTER FOR MULTIPLICATION
AND DIVISION AS FOLLOWS

FUNCTION	J 5	J 2	J 1
12	1	0	1
13	1	1	0

J REG
SET UP {
BY K TJ₅ $\overline{ETJ_2}$ FROM
 K TJ WHEN
 K 2 = 1 K TJ FRO
 K 2 = 0

ETJ SETS UP APPROPRIATE SCR OR B REGISTER ADDRESS IN I REGIS AS FOLLOWS

PRIORITY LEVEL	E1	E2	J 3	J 2	J1	
					FOR SCR	FOR B RE
1(HIGHEST)	0	0	0	0	0	1
2	1	0	0	1	0	1
3	0	1	1	0	0	1
4(LOWEST)	1	1	1	1	0	1



TO BE READ IN CONJUNCTION
WITH 322A7191

**920 B COMPUTER
CONTROL AND INTERRUPT
OVERALL LOGIC**

A18

LATEST M.I. INCORPORATED M.I. 2983

2998

ISSUE No.	1					MATERIAL	FINISH	ELLIOTT BROTHERS (LONDON) LTD		
ALTN. No.	1661									
DATE	4.11.66							SCALE SHT 1 OF 2		
INITIALS	W.V.P							DRAWING No.		
PARTS LIST No.		DRAWN J. S. B.	CHECKED C. B. S. S. E.	UNAPPROVED A. M. A.	DATE 27.5.66	© Elliott Brothers (London) Limited THIS DRAWING IS THE PROPERTY OF ELLIOTT BROTHERS (LONDON) LIMITED IT IS NOT TO BE COPIED OR DISCLOSED FOR ANY PURPOSE WITHOUT BY ELLIOTT BROTHERS (LONDON) LIMITED	TOLERANCES DECIMAL ± FRACTIONAL ± ANGULAR DIMENSIONS ±: UNLESS OTHERWISE STATED	TITLE 920B COMPUTER CONTROL AND INTERRUPT OVERALL LOGIC		322 D 8070