

1 Amplifiers

1.1 Active vs. Passive Circuits

We now move into the regime of so-called *active electronics*. Everything studied so far – resistors and capacitors, to voltage dividers and filters, to diode rectifiers has fallen under the realm of passive electronics. Active electronic devices, in addition to standard inputs and outputs, require additional power supplies in order to function. There are great benefits to this, as we shall see: active components can output more power than at their input, and can eliminate annoying effects such as voltage loading due to input and output impedance. The first active device we'll study is the amplifier.

1.2 The Ideal Voltage Amplifier

The idea of the basic amplifier is simple: take the input x_{in} and produce an output which is some number G times the output.

$$x_{\text{out}} = Gx_{\text{in}} \quad (1)$$

... where G is known as the gain.

There is the question of what input x_{in} is being amplified. In our course, normally x_{in} will be a voltage and so we will have a voltage amplifier. In this case the gain is often expressed in decibals as:

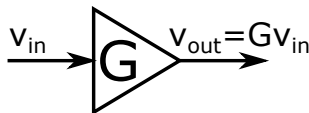
$$G_{\text{dB}} = 20 \log \frac{v_{\text{out}}}{v_{\text{in}}} \quad (2)$$

We will occasionally deal with power amplifiers whose input and output is a power. For these amplifiers, the gain is also in dB but with a 10 instead of a 20 multiplier¹:

$$G_{\text{dB}} = 10 \log \frac{P_{\text{out}}}{P_{\text{in}}} \quad (3)$$

For now though, we will restrict ourselves to voltage amplifiers. Diagrammed in figure 1. The output is described by equation 1.

Figure 1: The simplest of amplifiers: the output is the input multiplied by a constant gain G .



¹Recall that for a fixed load resistance, $P = v^2/R \propto v^2$. Thus $10 \log [P_{\text{out}}/P_{\text{in}}] = 10 \log [(v_{\text{out}}/v_{\text{in}})^2] = 20 \log [v_{\text{out}}/v_{\text{in}}]$, thus yielding consistent definitions of gain

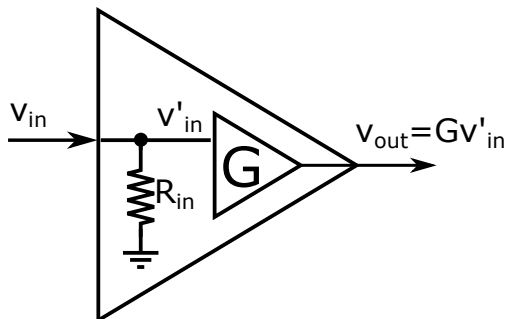
1.3 Reality Check: Amplifiers in Real Life

There are several limitations to the ideal amplifier that come into effect in practice. For example the amplifier will have to draw at least a small current from the source in order to operate. As we've learned, this is equivalent to a finite *input impedance* R_{in} . The amplifier will also have a limit to how much current it can output due to its internal resistances. This leads to a finite *output impedance* R_{out} . There are also limits to how fast the amplifier can respond to a changing input signal. This leads to a finite *gain bandwidth*, or in general, a frequency dependant gain $G(\omega)$. We will examine each of these effects presently.

1.3.1 Effects of Finite Input Impedance

We can model the non-ideal amplifier as an ideal amplifier inside a black box, precisely as we did with ideal vs real voltage sources. Inside the black-box, the input impedance is modeled as an internal resistance R_{in} to ground placed before the ideal amplifier. This forms a voltage divider with the input impedance from the rest of the circuit so that there is some voltage drop. The ideal amplifier thus sees a different voltage than v_{in} , which we denote v'_{in} . The output of the real amplifier is thus less than we would expect: $v_{out} = Gv'_{in} < Gv_{in}$.

Figure 2: We model the finite input impedance of the amplifier as a internal resistor R_{in} to ground before an ideal amplifier all in a black box.



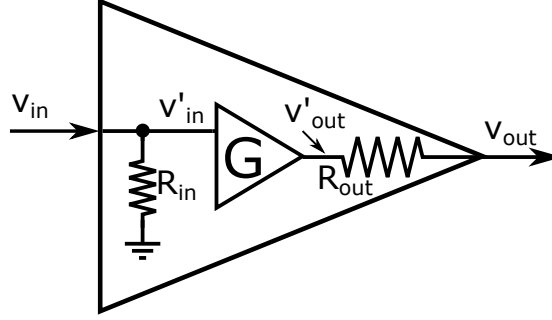
The true output, given the input impedance can be calculated given the output impedance of whatever follows. For example, with a $100\ \Omega$ input impedance amplifier with $G = 10$,

1.3.2 Effects of Finite Output Impedance

The operation of our ideal amplifier can be seen as a two part process: first it senses the voltage at its input (with the effect of finite input impedance causing some loading error), and second, it acts as a new voltage source, producing a voltage $v_{out}G_{Cin}$. As with any *real* source, it will be equivalent to an ideal source, in series with an output impedance R_{out} . This finite output impedance

leads to a maximum short-circuit current of $v_{\text{out}}/R_{\text{out}}$ and will lead to all of the loading issues discussed in chapter XXX. Figure 3 shows our improved model of how a real amplifier behaves.

Figure 3: A better model of a real amplifier has an input resistance to ground in parallel with the input and an output resistance in series with its output.



Design Example: Consider a real amplifier with $R_{\text{in}} = 10 \text{ k}\Omega$, $R_{\text{F}} = 100 \text{ }\Omega$, and $G = 25$. We use it to amplify a 10 mV signal from a photodetector having $R_{\text{out,PD}} = 1250 \text{ }\Omega$ and send it to a (lousy) voltmeter with $R_{\text{in,LVM}} = 500 \text{ }\Omega$ (see figure 4). What is the voltage measured (a) assuming an ideal $G = 25$ amplifier, and (b) with our real amplifier?

Answer: For part (a) the answer is simply $v_{\text{meas}} = 25 \times 10 \text{ mV} = 250 \text{ mV}$. For our real amplifier, we must consider the input and output stage. The input now forms a voltage divider with R_{in} (figure 4b) so that the inner ideal amplifier sees:

$$v'_{\text{in}} = \frac{R_{\text{in}}}{R_{\text{out,PD}} + R_{\text{in}}} = \frac{10 \text{ k}\Omega}{11.25 \text{ k}\Omega} \times 10 \text{ mV} \approx 8.9 \text{ mV}$$

In our model, this is then amplified by the ideal internal amplifier producing

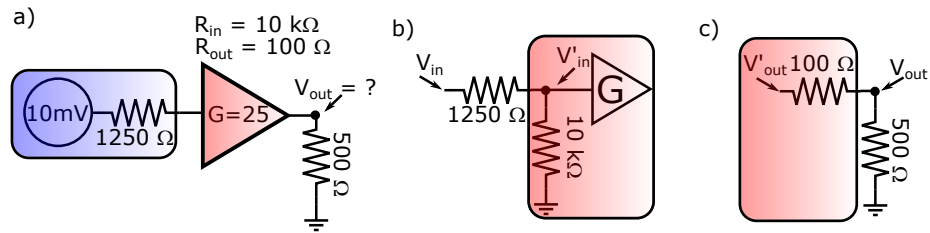
$$v'_{\text{out}} = 8.9 \text{ mV} \times 25 \approx 222 \text{ mV}$$

Finally, this is then sent to our lousy voltmeter, through its internal output resistance, forming another voltage divider (figure 4c) to yield the final voltage:

$$v_{\text{out}} = \frac{R_{\text{in,LVM}}}{R_{\text{out}} + R_{\text{in,LVM}}} v'_{\text{out}} = \frac{500 \text{ }\Omega}{600 \text{ }\Omega} \times 222 \text{ mV} \approx 185 \text{ mV}$$

We thus see that the effect of finite input and output impedance is to lessen the effective gain of the real amplifier. Even if we used an ideal voltmeter with infinite input impedance, we'd still see $222 \text{ mV} < 250 \text{ mV}$. Equivalently, we could try to build an amplifier with $R_{\text{out}} \rightarrow 0 \text{ }\Omega$ and again we'd see 222 mV , regardless of the impedance of the voltmeter. To get the full 250 mV however, we'd need to also have $R_{\text{in}} \rightarrow \infty$, so that $v'_{\text{in}} = v_{\text{in}} = 25 \text{ mV}$.

Figure 4: Example Problem.



From this we can infer the ideal characteristics of a real amplifier: we want the input impedance as high as possible while having output impedance as low as possible. This can be accomplished using solid state transistors, discussed later in the course.

1.3.3 Effects of Finite Frequency Response

1.4 Effects of Noise: Noise Factor and SNR

1.5 Differential Amplifiers

1.6 Characterizing Differential Amplifier Performance: CMRR

1.6.1 Common Mode Gain

1.6.2 Common Mode Rejection Ratio

1.7 Special Purpose Amplifiers

1.7.1 Audio Amplifiers

1.7.2 Power Amplifiers

1.7.3 Instrumentation Amplifiers

1.7.4 Lock-In Amplifiers